

## 2SC4291

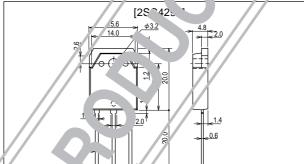
# Ultrahigh-Definition Color Display Horizontal Deflection Output Applications

#### **Features**

- · High speed ( $t_f$ =0.3 $\mu$ s max).
- · High breakdown voltage (V<sub>CBO</sub>=1500V).
- · High reliability (adoption of HVP process).
- · Adoption of MBIT process.
- · On-chip damper diode.

#### **Package Dimensions**

unit:mm 2022A



# 5.45

### 1 : Base

- 2 : Collector 3 : Emitter
- SANYO: TO-3PB

### **Specifications**

#### **Absolute Maximum Ratings** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCPO		1500	V
Collector-to-Emitter Voltage	VCEO		800	V
Emitter-to-Base Voltage	V <sub>E</sub> BO		7	V
Collector Current	l <sub>C</sub>		5	Α
Collector Current (Pulse)	ICP		16	Α
Collector Dissipation	Po	//	2.5	W
	1 25°C	- //	100	W
Junction Temperature	Ti	//	150	°C
Storage Temperature	Tsi	7	-55 to +150	°C

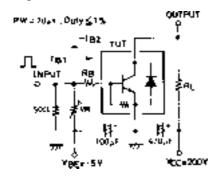
# Electrical Characteristics at T

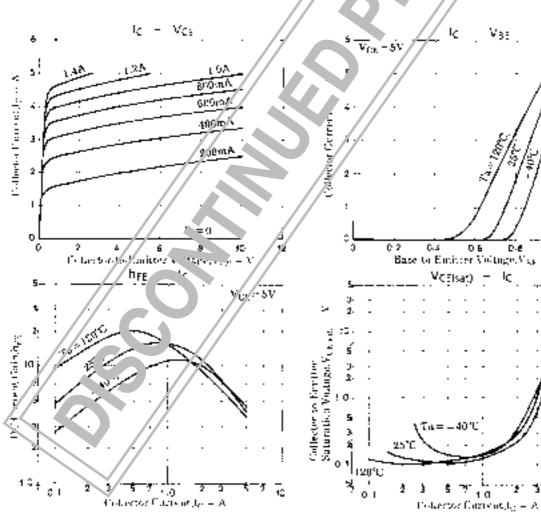
Parameter	ymbol	Conditions	Ratings			Unit
i arametsi	Cyribor	Conditions	min	typ	max	Offic
Collector Cutoff Current	ICBO	V <sub>CB</sub> =800V, I <sub>E</sub> =0			10	μΑ
	ICES	V <sub>CE</sub> =1500V, R <sub>BE</sub> =0			1.0	mA
Collector-to-Emitte/ Sastain Voltuge	VCEO(sus)	I <sub>C</sub> =100mA, I <sub>B</sub> =0	800			V
Emitter Cutoff Current	EBO	V <sub>EB</sub> =4V, I <sub>C</sub> =0	40		130	mA
Collector-to-F.nit.er Satur on ' 3	VCE(sat)	I <sub>C</sub> =4A, I <sub>B</sub> =1A			5	V
Base-to-Enlitter Satura in Voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =4A, I <sub>B</sub> =1A			1.5	V

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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
DC Current Gain	h <sub>FE</sub> 1	V <sub>CE</sub> =5V, I <sub>C</sub> =1A	8			
	h <sub>FE</sub> 2	V <sub>CE</sub> =5V, I <sub>C</sub> =4A	4		6	
Diode Forward Voltage	٧ <sub>F</sub>	I <sub>EC</sub> =5A, I <sub>B</sub> =0			2.0	V
Storage Time	t <sub>stg</sub>	I <sub>C</sub> =4A, I <sub>B1</sub> =0.8A, I <sub>B2</sub> =-1.6A			3.0	μs
Fall Time	t <sub>f</sub>	I <sub>C</sub> =4A, I <sub>B1</sub> =0.8A, I <sub>B2</sub> =-1.6A	77		0.3	μs

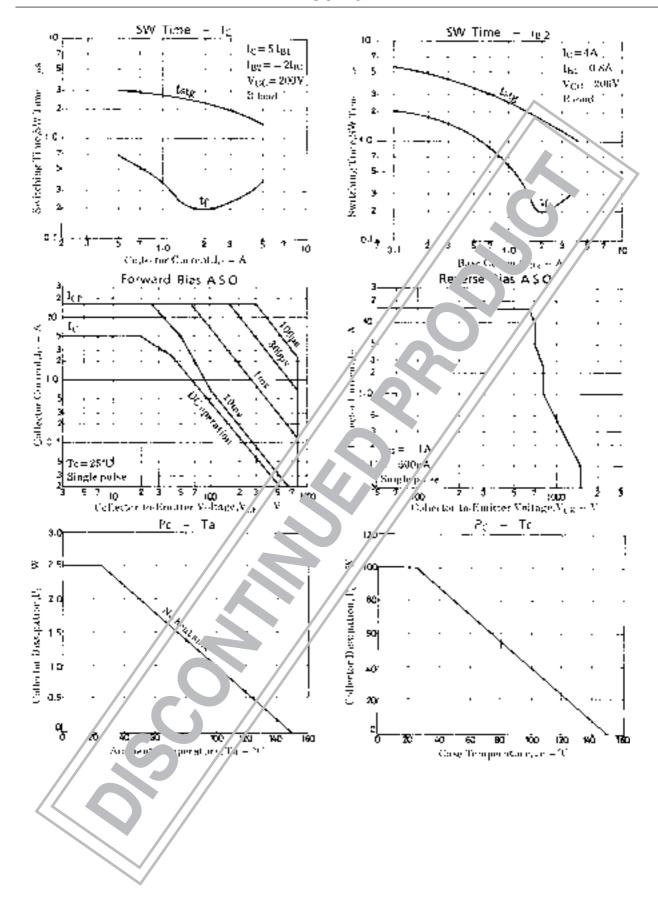
#### **Switching Time Test Circuit**

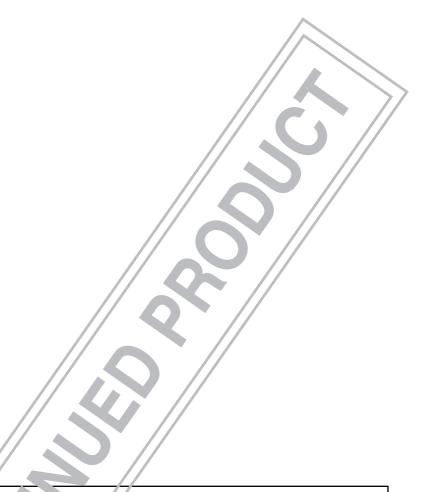




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 $I_{\rm C}/I_{\rm B}=8$ 





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