Unit: mm

TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT process)

2SA1736

Power Amplifier Applications Power Switching Applications

- Low saturation voltage: $V_{CE (sat)} = -0.5 \text{ V (max) (I}_{C} = -1.5 \text{ A})$
- High speed switching time: $t_{stg} = 0.2 \mu s$ (typ.)
- Small flat package
- $P_C = 1.0$ to 2.0 W (mounted on ceramic substrate)
- Complementary to 2SC4541

Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Collector-base voltage	V_{CBO}	-60	V	
Collector-emitter voltage	V _{CEO}	-50	V	
Emitter-base voltage	V _{EBO}	-6	V	
Collector current	IC	-3	Α	
Base current	Ι _Β	-0.6	Α	
	P _C	500	mW	
Collector power dissipation	PC	1000		
	(Note)	1000		
Junction temperature	Tj	150	°C	
Storage temperature range	T _{stg}	−55 to 150	°C	

Note: Mounted on ceramic substrate (250 mm² × 0.8 t)

4.6MAX. 1,6MAX. 1,6MAX. 1,6MAX. 1,6MAX. 1,7MAX. 0.4±0.05 1,5±0.1 1,5±0

2-5K1A

Weight: 0.05 g (typ.)

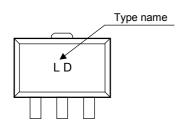
TOSHIBA

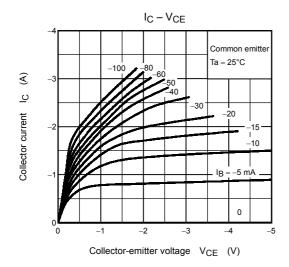
Electrical Characteristics (Ta = 25°C)

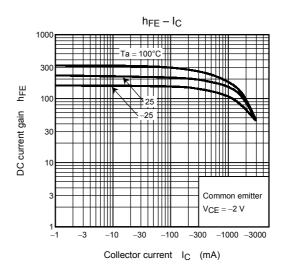
Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Collector cut-off cu	rrent	I _{CBO}	V _{CB} = -60 V, I _E = 0	_	_	-0.1	μΑ	
Emitter cut-off current		I _{EBO}	V _{EB} = -6 V, I _C = 0	_	_	-0.1	μΑ	
Collector-emitter br	eakdown voltage	V (BR) CEO	$I_C = -10 \text{ mA}, I_B = 0$	-50	_	_	V	
DC current gain		h _{FE (1)}	V _{CE} = -2 V, I _C = -100 mA	120	_	400		
		h _{FE (2)}	V _{CE} = -2 V, I _C = -2 A	40	_	_		
Collector-emitter sa	aturation voltage	V _{CE} (sat)	I _C = -1.5 A, I _B = -75 mA	_	_	-0.5	V	
Base-emitter saturation voltage		V _{BE (sat)}	I _C = -1.5 A, I _B = -75 mA	_	_	-1.2	V	
Transition frequency		f _T	V _{CE} = -2 V, I _C = -100 mA	_	100	_	MHz	
Collector output capacitance		C _{ob}	V _{CB} = -10 V, I _E = 0, f = 1 MHz	_	32	_	pF	
Switching time	Turn-on time	t _{on}	$\begin{array}{c c} I_{B2} & \text{OUTPUT} \\ \hline I_{B1} & \hline \\ I_{B2} & \hline \\ I_{B1} & \hline \\ I_{B1} & \hline \\ I_{B2} & \hline \\ I_{B1} & \hline \\ I_{B1} & \hline \\ I_{B1} & \hline \\ I_{B2} & \hline \\ I_{B1} & \hline \\ I_{B1} & \hline \\ I_{B2} & \hline \\ I_{B1} & \hline \\$	_	0.1	_		
	Storage time	t _{stg}		_	0.2	_	μs	
	Fall time	t _f		_	0.1	_		

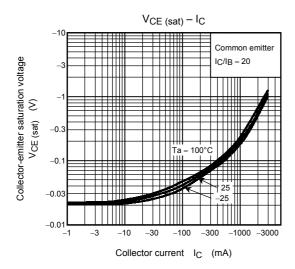
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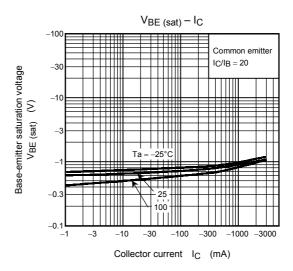
Marking

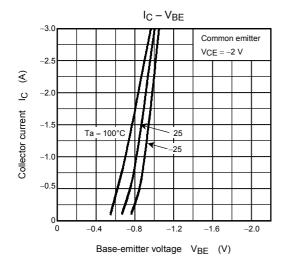


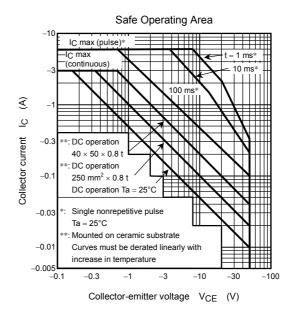


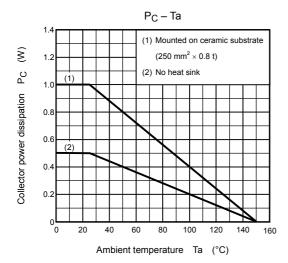












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