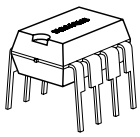
SIEMENS

64 Kbit (8192 × 8 bit) Serial CMOS EEPROMs, I²C Synchronous 2-Wire Bus, Page Protection Mode™

SLx 24C64/P

Preliminary

Features



P-DIP-8-4

P-DSO-8-3

- **Data EEPROM internally organized as 8192 bytes and 256 pages × 32 bytes**
- **Page protection mode, flexible page-by-page hardware write protection**
  - Additional protection EEPROM of 256 bits, 1 bit per data page
  - Protection setting for each data page by writing its protection bit
  - Protection management without switching WP pin
- **Low power CMOS**
- **V_{CC} = 2.7 to 5.5 V operation**
- **Two wire serial interface bus, I²C-Bus compatible**
- **Three chip select pins to address 8 devices**
- **Filtered inputs for noise suppression with Schmitt trigger**
- **Clock frequency up to 400 kHz**
- **High programming flexibility**
  - Internal programming voltage
  - Self timed programming cycle including erase
  - Byte-write and page-write programming, between 1 and 32 bytes
  - Typical programming time 6 ms (< 10 ms) for up to 32 bytes
- **High reliability**
  - Endurance 10^6 cycles[1)]
  - Data retention 40 years[1)]
  - ESD protection 4000 V on all pins
- **8 pin DIP/DSO packages**
- **Available for extended temperature ranges**
  - Industrial: – 40 °C to + 85 °C
  - Automotive: – 40°C to + 125 °C

1) Values are temperature dependent, for further information please refer to your Siemens Sales office.