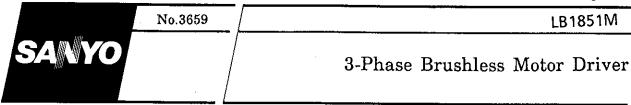
LB1851M

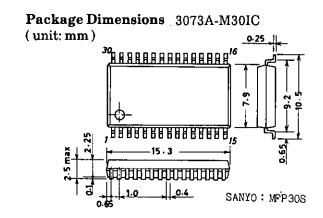


The LB1851M is a 3-phase brushless motor driver IC ideally suited for use in VCR capstan motor driver, drum motor driver, and DAT motor driver applications.

Features

- 120°C voltage linear type
- · Less power dissipation because of speed control based on motor voltage control (suitable for use in portable sets)
- · Torque ripple compensation circuit on chip
- · Small capacitance of external capacitor because of soft switching method (chip capacitor)
- · Thermal shutdown circuit on chip
- · FG amp on chip

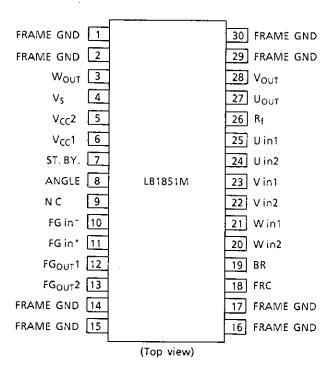
| Absolute Maximum Ratings at | $Ta = 25^{\circ}C$ | | unit |
|------------------------------|-----------------------|--------------------|------|
| Maximum Supply Voltage1 | V _{CC} 1 max | 7 | V |
| Maximum Supply Voltage2 | $ m V_{CC}2$ max | 16 | V |
| Maximum Supply Voltage3 | V_S max | $ m V_{CC}2$ | V |
| Output Supply Voltage | V_{O} max | $V_S + 2V$ | V |
| Output Current | $I_{\mathbf{O}}$ max | 1.5 | Α |
| Allowable Power Dissipation | Pd max | 1.0 | W |
| Operating Temperature | Topr | -20 to +75 | °C |
| Storage Temperature | Tstg | -55 to +125 | °C |
| Allowable Operating Conditio | ns at Ta = 25°C | | unit |
| Supply Voltage1 | $V_{CC}1$ | 4.0 to 6.0 | v |
| Supply Voltage2 | $V_{CC}2$ | 4 to 14 | V |
| Supply Voltage3 | v_s | up to $ m V_{CC}2$ | V |



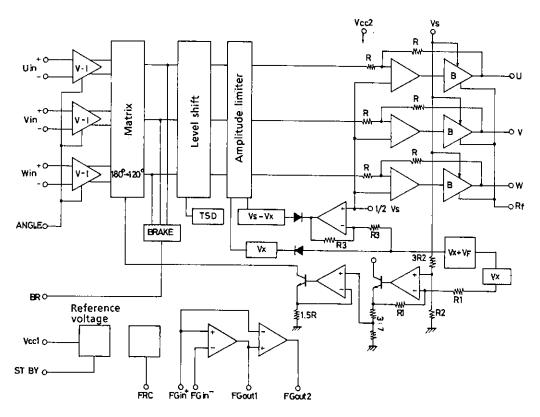
| | Ta = 25°C, | $V_{CC}1 = 5V, V_{CC}2 = 7V, V_{S} = 3V$ | min | typ | max | unit | N |
|-------------------------------|---------------------|---|------|------|------|---------|----------|
| Supply Current 1 | $I_{CC}1$ | $V_{BR} = 5V$ | | 4.5 | 6.5 | mA | |
| Supply Current 2 | $I_{CC}2$ | $V_{BR} = 5V$ | | 15 | 20 | mΑ | |
| Supply Current 3 | $I_{\mathbf{S}}$ | $V_{BR} = 5V$, $R_L = \infty$ | | 6.5 | 9.0 | mΑ | |
| Output Standby Current 1 | I_{CCOQ} | $V_{STBY} = 0V$ | | | 180 | μ A | |
| Output Standby Current 2 | I_{SOQ} | $V_{STBY} = 0V$, $R_L = \infty$ | | | 150 | μ A | |
| Output Saturation Voltage | V_{O} (sat) | $I_{OUT} = 1.0A$, sink + source | | | 2.3 | v | |
| Output TRS Voltage | V_{O} (sus) | $I_{OUT} = 20 mA$ | 16 | | | V | * |
| Output Standby Voltage | v_{oq} | $I_{BR} = 5V$ | 1.4 | 1.5 | 1.6 | V | |
| Hall Amp Input | $V_{H off set}$ | | -5 | | +5 | mV | Ж |
| Offset Voltage | | | | | | | |
| Hall Amp Common-Mode | V_{HCOM} | · | 1.4 | | 2.8 | V | |
| Input Voltage Range | | | | | | | |
| Hall Input-Output | $G_{ m VHO}$ | Rangle = $8.2k\Omega$ | 31.5 | 34.5 | 37.5 | dB | |
| Voltage Gain | V | | | | | | |
| Brake Pin 'H'-Level Voltage | | | 2.0 | | | V | |
| Brake Pin 'L'-Level Voltage | | | | | 0.8 | v | |
| Brake Pin Input Current | | | | | 100 | μA | |
| Brake Pin Leakage Current | | | | | -30 | μA | |
| FRC Pin 'H'-Level Voltage | | | 2.8 | | -00 | V | |
| FRC Pin 'L'-Level Voltage | | | 2.0 | | 1.2 | v | |
| FRC Pin Input Current | | | | | 100 | μA | |
| FRC Pin Leakage Current | | | | | -30 | μA | |
| Upper Residual Voltage | v_{xH} | $I_{OUT} = 100 \text{mA}, V_{CC} = 6V$ | 0.38 | | 0.55 | μA V | |
| - Phot man tombo | ' An | $V_S=2V$ | 0.56 | | 0.00 | ٧ | |
| Lower Residual Voltage | V_{XL} | $I_{OUT} = 100 \text{mA}, V_{CC} = 6 \text{V},$ | 0.41 | | 0.5 | v | |
| Ū | *** | $V_S=2V$ | **** | | 0.0 | • | |
| Residual Voltage Inflection F | oint | ~ | | 2.0 | | v | |
| Overlap Amount | | $V_{\rm CC}2=6V$, $V_{\rm S}=3V$ | 60 | 70 | 80 | % | |
| Standby ON Voltage | | | -0.2 | | +0.1 | V | |
| Standby OFF Voltage | | Open: Standby Off (Note 1) | 2 | | 5 | v | |
| Standby Pin Bias Current | | Pin GND | | | 10 | μA | |
| Operating Temperature of | | | 150 | 180 | 210 | °C | . |
| Thermal Shutdown Circuit | | | 100 | 100 | 210 | C | ** |
| Hysteresis of Thermal Shutd | own Circui | i t | | 15 | | °C | × |
| [FG Amp] | own on ca | ιο. | | 10 | | C | ж |
| FG Amp Input | $V_{FGoffset}$ | | 0 | | . 0 | 37 | |
| Offset Voltage | Y FG off set | | -8 | | +8 | mV | |
| Open Loop Voltage Gain | G_{VFG} | f=1kHz | | 60 | | .170 | • |
| Source Side Output | | | 9.77 | 60 | | dB | |
| Saturation Voltage | V _{FG OUT} | $I_0 = -2mA$ | 3.7 | | | V | |
| Sink Side Output | V | I 9 A | | | | | |
| Saturation Voltage | V_{FGOD} | $I_0 = 2mA$ | | • | 1.3 | V | |
| Common-Mode | CUP | | | • | | | |
| | CHR | | | 80 | | dB | × |
| Signal Rejection | 77 | | _ | | _ | | |
| FG Amp Common-Mode | $V_{FG\ CH}$ | | 0 | | 3.5 | V | |
| Input Voltage Range | | | | | | _ | |
| Phase Margin | | ** | | 20 | | °C | Ж |
| Schmitt Amp Threshold Volta | age | V_{FG} in + = 2.5V, V_{FGOUT} 2 | 2.45 | 2.50 | 2.55 | V | |
| | | at H to L | | | | | |
| Schmitt Amp Hysteresis | | $V_{FGin} + = 2.5V$ | 20 | | | | |

Overlap spec. are regarded as test spec.

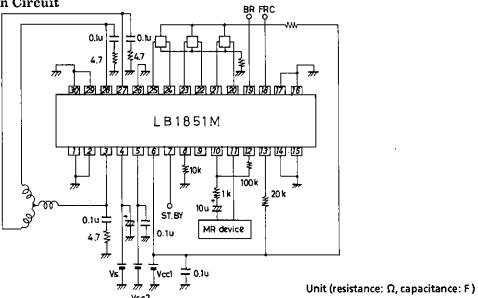
Pin Assignment



Block Diagram







Truth Table

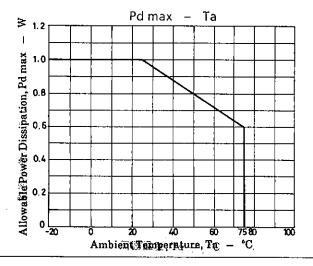
| Mada | Source | Input | | | Forward/Reverse |
|------|-----------------|-------|----|----|-----------------|
| Mode | Sink | U | v | W | Control |
| 1 | W phase→V phase | Н | Н | L | L |
| | V phase→W phase | | | | Н |
| 2 | W phase→U phase | Н | L | L | L |
| | U phase→W phase | 11 | | | Н |
| 3 | V phase→W phase | L | L | Н | L |
| | W phase→V phase | 1 | | | Н |
| 4 | U phase→V phase | L | Н | L | L |
| | V phase→U phase | 7 | 11 | יי | Н |
| 5 | V phase→U phase | Н | L | Н | L |
| | U phase→V phase | 17 | | | Н |
| 6 | U phase→W phase | L | Н | Н | L |
| | W phase→U phase | ע | 11 | 11 | Н |

Input: "H": Input 1 of each phase is at a potential which is higher by more than 0.2V relative to input 2.

"L": Input 1 of each phase is at a potential which is lower by more than 0.2V relative to input 2.

Forward/reverse control : "H" : 2.8V to $V_{CC}1$

"L": 0V to 1.2V



Pin Description

| עוגגיי | escriptio | n | | • |
|------------|---------------------|---|---|--|
| Pin No. | Pin Symbol | Pin Voltage | Equivalent Circuit | Pin Function |
| 4 | V _S | <v<sub>CC2</v<sub> | | Power supply pin for fixing the output amplitude. Must be lower than $V_{\rm CC}2$ voltage. |
| 5 | V _{CC} 2 | 4V to 14V | | Power supply pin for power amp circuit other than motor driver transistor. Power supply pin for supplying voltage to other than the control section whose supply voltage is V _{CC} 1. |
| 6 | V _{CC} 1 | 4V to 6V | | Power supply pin for supplying voltage to the hall amp, forward/reverse control, FG amp, thermal shutdown circuit. |
| 7 | ST. BY | L: 0.1V max H: 2.0V min (When V _{CC} 1=5V) | Vcc1 100 k | When this pin is grounded, all the circuitry stops operating. In this case, the supply current is approximately $100\mu A$. In the normal operation mode, this pin is left open or made to be at a potential of more than 2V. |
| 8 | ANGLE | | Vcc1 → 2200 → m | The hall input-output gain (slope of motor waveform) can be changed by changing the resistance connected across this pin and GND. |
| 10 | FG in* | min 0V max 3.5V (When V _{CC} 1=5V) | Vccl Vccl Vccl Vccl Vccl Vccl Vccl Vccl | FG signal input pin |
| 12 | FG _{OUT} 1 | | Vcc1 38 38 38 38 38 38 38 38 38 38 38 38 38 | FG amp output pin |
| 13 | FG _{OUT} 2 | | Vocal (I) | FG schmitt amp output pin |
| 18 | FRC | H: 2.8V min L: 1.2V max (When V _{CC} 1=5V) | 100 k | Pin for forward/reverse control of motor L level : Forward (Less than 1.2V : When $V_{CC}1=5V$) H level : Reverse (More than 2.8V : When $V_{CC}1=5V$) |

Unit (resistance: Ω)

| Continued from pr | eceding page. |
|-------------------|---------------|
|-------------------|---------------|

| Pin No. | Pin Symbol | Pin Voltage | Equivalent Circuit | Pin Function | |
|------------------------------------|--|-------------------------------|--|---|--|
| 19 | BR | H: 2.0V min L: 0.8V max | Vcc2 50k 70k 70k 70k 70k 70k 70k 70k | Pin for stopping the motor L level: Motor drive (Less than 0.8V) H level: Motor stop (More than 2.0V) | İ |
| 20 | Win2 | | | W phase hall element input pin | |
| 21 | Win1 | min 1.4V | ▼ Vcc1 | Logic "H": Win1>Win2 | |
| 22 | Vin2 | max 2.8V | Ø 200. ∮ ₹ 200 Ø | V phase hall element input pin | |
| 23 | Vin1 | (When V _{CC} 1 = 5V) | | Logic "H": Vin1>Vin2 | |
| 24 | Uin2 | | | U phase hall element input pin | İ |
| 25 | Uin1 | | // | Logic "H": Uin1>Uin2 | ļ. |
| 26 | Rf | | | GND for output transistor | |
| 27 28 3 | U _{OUT} V _{OUT} W _{OUT} | | ○ V8 ② ③ ○ Rf | Output pin | ······································ |
| 1, 2 14, 15 16, 17 29, 30 | FRAME (GND) | | | GND for other than output | i |

Unit (resistance: Ω)

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