

No.2614B

LA7952

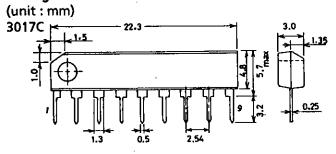
Video Switch for TV / VCR Use

Features

- . On-chip driver with 4 inputs, 1 output, 75ohm termination
- . On-chip 6dB amp
- . Excellent crosstalk characteristic
- . Wide band
- . Input with DC restoration circuit

Maximum Ratings at Ta=25°C				•		unit
Maximum Supply Voltage	V7max	•			12	ı v
Maximum Input Supply Voltage(1) V4max.	V6max				
,	V8max,				`	•
Maximum Input Supply Voltage(2) V2max,				11	v
Maximum Output Current	I1max,	42mgry 4CC-144				
Allowable Power Dissipation	Pdmax	Ta≦65°C			10	
Operating Temperature		1a=05 C			540	_
	Topr			-20 t	_	_
Storage Temperature	Tstg		-	55 to	+150) °C
Operating Conditions at Ta=25°C					112	it
	10 5	+- 17				
Recommended Supply Voltage V _{CC}	ΟÞ		10.5	to 13		V
recommended pubbly Activate ACC					12	V
Operating Characteristics at Ta=	25°C, V _{CC}	=12V	min	typ	max	unit
				20		mA
Input Bias Voltage	I _{CC} V4,V6,		14 2.7		3.3	V
-	V8, V9		-•,	J. 0	٠.٠	٧
Output Bias Voltage	V1		11 6	6.1	7.6	V
Output DC Offset Voltage	vos	Note 1	7.0	15	-	-
Control Threshold Voltage			2.0	כו	100	mV
omered im concid voltage	V _{2Н} , V _{3Н}		3.0			V
Control Innut Cumant	V _{2L} , V _{3L}			_	1.5	V
Control Input Current	₁ 2,13		-20			uĄ
Voltage Gain	G _V 12,13	f=1MHz,V _{IN} =1Vpp,	5.6	6.1	6.6	dΒ
-		Note 1				
Frequency Characteristic	$^{\mathrm{G}}\mathtt{V-f}$	0dB at $f=100kHz$,	- 3	0		dΒ
		Note 1				
		$f=10MHz, V_{IN}=1Vpp$				
		Co	ntinued	on n	ext p	age.
					•	-

Package Dimensions



SANYO: SIP9

Continued from preceding page.

			min	typ	max unit
Output Dynamic Range	V_{DR}	$f=15kHz,V_{TN}=1.5p-p,Note 1$	1.4	1.5	Vpp
Crosstalk (Note 2)	CT"	V _{TN} =1Vp-p, f=3MHz, Note 1	48	58	₫B
		V _{TN} =1Vp-p,f=5MHz,Note 1	45	55	dB

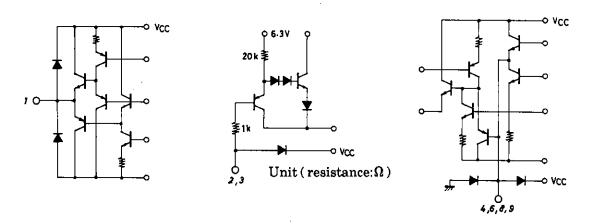
* Current direction: Plus: Flowing into IC
Minus: Flowing out of IC

Video Switch Truth Table

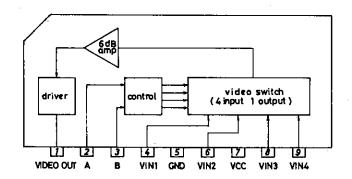
S2	S3	VIN	1 Vin	2 V I N 3	V I N 4
(Pin :	2) (pin	3) (Pin	4) (Pin ((Pin 8)	(Pin 9)
Н	Н	ON	OFF	ÖFF	OFF
L	Н	OFF	ON	OFF	OFF
H	L	OFF	OFF	ON	OFF
L	L	OFF	OFF	OFF	ON

Note 1: Refer to this Truth Table and make measurements by switching S2, S3.

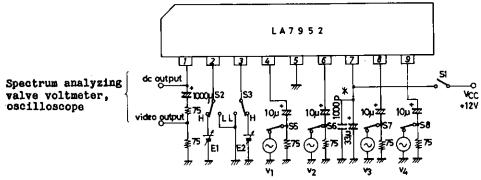
Input/Output Equivalent Circuit



Equivalent Circuit Block Diagram

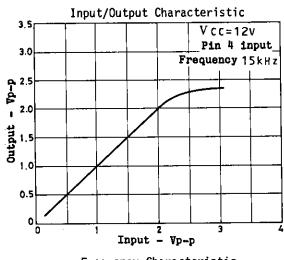


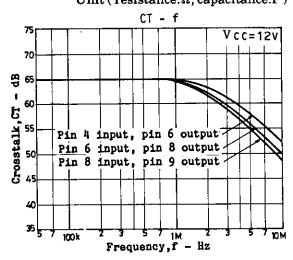


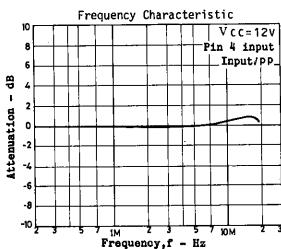


*Connect the capacitor for V_{CC} as close to pin 7 as possible.

Unit(resistance: Ω , capacitance:F)







Proper Cares in Using the IC If the signal source impedance is increased, the sync pulse will shrink because of the DC restoration circuit contained in the input. Therefore, the signal source impedance must be kept low.

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