

No.2873

LA7853

CRT Display Synchronization Deflection Circuit

The LA7853 is a sync deflection circuit IC dedicated to CRT display use. It can be connected to the LA7832,7833,7837,7838 (for vertical output use) to form a sync deflection circuit that meets every requirement for CRT display use.

So far, ICs for color TV use have been applied to the sync deflection circuit for CRT display use and general-purpose ICs such as one-shot multivibrator, inverter and a lot of transistors have been used to form the peripherals such as sync input interface, horizontal phase shifter. The LA7853 contains these peripherals on chip and adopts a stable circuit for hoirizontal oscillation from 15kHz to 100kHz aiming at improving the characteristics required for CRT display use.

The LA7853 has independent GND pins for the horizontal block and vertical block, thus facilitating pattern layout for applications where the LA7853 is used at high frequencies.

On-chip Functions

[Horizontal Block]

- ·AFC
- · Horizontal OSC
- · X-ray protector
- · Horizontal phase shift
- · AFC sawtooth wave generator
- · Horizontal pulse duty setting

[Vertical Block]

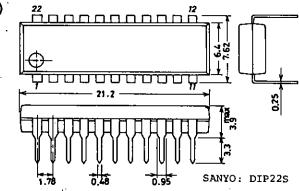
- · Vertical OSC
- · Vertical sawtooth wave generator
- · Sampling type DC voltage control

Features

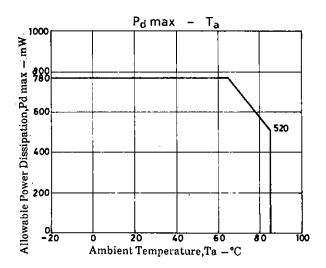
- \cdot The vertical pull-in range is approximately 20Hz, 2 times that of the LA7852, at $f_V = 60$ Hz.
- · The horizontal oscillation frequency can be adjusted stably from 15kHz to 100kHz.
- The horizontal display can be shifted right/left.
- · The horizontal/vertical sync input can be used intact regardless of the difference in pulse polarity and pulse width.
- . The AFC feedback sawtooth wave can be obtained by simply applying a flyback pulse to the IC as a trigger pulse.
- · Any duty of the horizontal pulse can be set.
- · Good vertical linearity because DC bias at vertical output stage is subjected to sampling control within retrace time.
- · Excellent interlace and vertical jitter characteristics on the high-definition display because of independent GND pins for the horizontal block and vertical block.

Package Dimensions

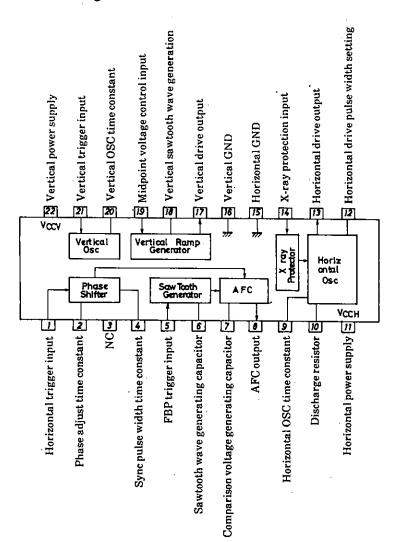
(unit :mm) 3059



		.			
Maximum Ratings at Ta=25°C			** to *** to ***	unit	
	$\mathbf{V_{11},V_{22}}$		14		
Allowable Power Dissipation	P _d max	Ta≦65°C	780		
Operating Temperature	\mathbf{Topr}		-20 to +8		
Storage Temperature	Tstg		-55 to +12	5°C	
Operating Conditions at Ta=25°	C			unit	
Recommended Supply Voltage	•	V_{11},V_{22}	1:		
Operating Voltage Range		V_{11},V_{22} V_{11},V_{22}	9 to 13.		•
Recommended Vertical Pulse In	out Doole V			5 Vp-p	
Operating Vertical Pulse Input F				6 Vp-p	
Recommended Horizontal Pulse				5 Vp-p	
Operating Horizontal Pulse Inpu Peak Value Range	ıt	${ m H}_{ m PULSE}$	2 to	6 Vp-p	
reak value wange					
Operating Characteristics at Ta	=25°C		min t	ур тах	unit
$V_{ m CC11}$ Current Dissipation	I_{11}		12	30	mA
V _{CC22} Current Dissipation	I_{22}		5	12	mA
Vertical Frequency Pull-in Rang	e V _p IN	Vertical sync 60Hz	19.0	23.0	Hz
Vertical Free-running Frequenc		f _v center 55 Hz	50	60	Hz
[Increased/Reduced Voltage		$V_{22} = 12 \pm 1 \text{V},55 \text{Hz} \text{ at } 12 \text{V}$	-0.1	0.1	Hz
Characteristic of Vertical Freque		20			
Midpoint Control Threshold Leve	-		3.8	4.4	v
Vertical OSC Start Voltage	$F_{v \cdot st}$		0.0	4.0	v
(Temperature Characteristic of	- V-St	$Ta = -10 \text{ to } +60^{\circ}\text{C}$	0.028	0.028	
Vertical Frequency		1a= -10 to +00 C	0.020	0.020	11 <i>Li</i> C
(Vertical Priver	$G_{\mathbf{v}}$		10	10	σι
	$\alpha_{\rm v}$		12	18	dB
Amplification Factor	т +		0.05		
Horizontal AFC DC Loop Gain	I _{AFC} ⁺	•	0.85	1.6	mA
II ' (1 m ' m	I _{AFC}	C	-1.6	-0.85	mΑ
Horizontal Free-running Freque		f _H center 15.734kHz	– 750	750	Hz
Horizontal OSC Start Voltage	$f_{H\cdot st}$			4.0	V
Increased/Reduced Voltage		$V_{11} = 12V \pm 1V, 15.734$ kHz at 1	2V 50	50	Hz
Characteristic of Horizontal Free	-	_			
Horizontal OSC Warm-up Drift	$\Delta \mathrm{f}_{\mathrm{H}}$	5s. to 30min.	-50	50	Hz
<u>. </u>		after application of power			
Temperature Characteristic of		$Ta = -10 \text{ to } +60^{\circ}C$	-2.9	2.9	Hz/°C
l Horizontal Frequency					
Horizontal Output Drive Curren	t I_{13}		6.0	12.0	mΑ
Increased/Reduced Voltage		$V_{11} = 12 \pm 1V$	-0.5	0.5	%/V
Characteristic of Phase Shifter					
Delay Time					
Temperature Characteristic of Phase Shifter Delay Time		$Ta = -10 \text{ to } +60^{\circ}C$	-0.1	0.1	%/°C
Increased/Reduced Voltage		$V_{11} = 12 \pm 1V$	-1.0	1.0	%/V
Characteristic of Phase Shifter		11-12-17	-1.0	1.0	/U/ ¥
Pulse Width					
Temperature Characteristic of		$Ta = -10 \text{ to } +60^{\circ}C$	-0.13	0.13	%/°C
Phase Shifter Pulse Width	n:	15 60 ALL G B.D.D.			
AFC Phase Comparison Center 7	rime	15.734kHz after F.B.P. input	9.9	11.5	
Increased/Reduced Voltage		$V_{11} = 12 \pm 1 V$	-1.5	1.5	%/V
Characteristic of AFC Phase					
Comparison Center Time					
Temperature Characteristic of		$Ta = -10 \text{ to } +60^{\circ}C$	-0.2	0.2.	%/°C
LAFC Phase Comparison Center 7		·			
Comparison Waveform Generati	ng V_5		0.6	0.9	V
Input Operation Voltage		•	•		
Pin 14 Voltage at Hold-down	V_{14}		0.5	0.8	V
l Operation Start			•		



Equivalent Circuit Block Diagram



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