

LM4041

Precision Micropower Shunt Voltage Reference

General Description

Ideal for space critical applications, the LM4041 precision voltage reference is available in the sub-miniature SC70 and SOT-23 surface-mount packages. The LM4041's advanced design eliminates the need for an external stabilizing capacitor while ensuring stability with any capacitive load, thus making the LM4041 easy to use. Further reducing design effort is the availability of a fixed (1.225V) and adjustable reverse breakdown voltage. The minimum operating current is 60 μA for the LM4041-1.2 and the LM4041-ADJ. Both versions have a maximum operating current of 12 mA.

The LM4041 utilizes fuse and zener-zap reverse breakdown or reference voltage trim during wafer sort to ensure that the prime parts have an accuracy of better than ±0.1% (A grade) at 25°C. Bandgap reference temperature drift curvature correction and low dynamic impedance ensure stable reverse breakdown voltage accuracy over a wide range of operating temperatures and currents.

Features

- Small packages: SOT-23, TO-92, and SC70
- No output capacitor required
- Tolerates capacitive loads
- Reverse breakdown voltage options of 1.225V and adjustable

Key Specifications (LM4041-1.2)

■ Output voltage tolerance (A grade, 25°C)

±0.1%(max)

■ Low output noise (10 Hz to 10kHz)

 $20\mu V_{rms}$

Wide operating current rangeIndustrial temperature range

60μA to 12mA -40°C to +85°C

■ Extended temperature range

 -40°C to $+125^{\circ}\text{C}$

■ Low temperature coefficient

100 ppm/°C (max)

Applications

- Portable, Battery-Powered Equipment
- Data Acquisition Systems
- Instrumentation
- Process Control
- Energy Management
- Automotive
- Precision Audio Components

Connection Diagrams

SOT-23

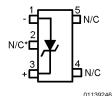


*This pin must be left floating or connected to pin 2.

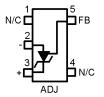
ADJ 01139240

Top View See NS Package Number MF03A (JEDEC Registration TO-236AB)

SC-70



*This pin must be left floating or connected to pin 1.

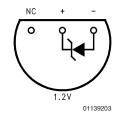


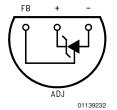
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Top View
See NS Package Number MAA05A

Connection Diagrams (Continued)

TO-92





Bottom View See NS Package Number Z03A

Ordering Information

Reverse Breakdown	Package						
Voltage Tolerance at 25°C and Average	M3 (S	M3 (SOT-23) M7 (SC70)		Z (TO-92)	NS Package		
Reverse Breakdown Voltage Temperature Coefficient	Supplied as 1000 Units Tape and Reel	Supplied as 3000 Units Tape and Reel	Supplied as 1000 Units Tape and Reel	Supplied as 3000 Units Tape and Reel		Number	
±0.1%, 100 ppm/°C max (A grade)	LM4041AIM3-1.2	LM4041AIM3X-1.2			LM4041AIZ-1.2	MF03A, Z03A	
±0.2%, 100 ppm/°C max (B grade)	LM4041BIM3-1.2	LM4041BIM3X-1.2	LM4041BIM7-1.2	LM4041BIM7X-1.2	LM4041BIZ-1.2	MF03A, Z03A, MAA05A	
±0.5%, 100 ppm/°C max (C grade)	LM4041CEM3-1.2 LM4041CIM3-1.2 LM4041CEM3-ADJ LM4041CIM3-ADJ	LM4041CEM3X-1.2 LM4041CIM3X-1.2 LM4041CEM3X-ADJ LM4041CIM3X-ADJ	LM4041CIM7-1.2 LM4041CIM7-ADJ	LM4041CIM7X-1.2 LM4041CIM7X-ADJ	LM4041CIZ-1.2 LM4041CIZ-ADJ	MF03A, Z03A, MAA05A	
±1.0%, 150 ppm/°C max (D grade)	LM4041DEM3-1.2 LM4041DIM3-1.2 LM4041DEM3-ADJ LM4041DIM3-ADJ	LM4041DEM3X-1.2 LM4041DIM3X-1.2 LM4041DEM3X-ADJ LM4041DIM3X-ADJ	LM4041DIM7-1.2 LM4041DIM7-ADJ	LM4041DIM7X-1.2 LM4041DIM7X-ADJ	LM4041DIZ-1.2 LM4041DIZ-ADJ	MF03A, Z03A, MAA05A	
±2.0%, 150 ppm/°C max (E grade)	LM4041EEM3-1.2 LM4041EIM3-1.2	LM4041EEM3X-1.2 LM4041EIM3X-1.2	LM4041EIM7-1.2	LM4041EIM7X-1.2	LM4041EIZ-1.2	MF03A, Z03A, MAA05A	

SOT-23 and SC70 Package Marking Information

Only three fields of marking are possible on the SOT-23's and SC70's small surface. This table gives the meaning of the three fields.

Part Marking	Field Definition
R1A (SOT-23 Only)	First Field:
R1B	R = Reference
R1C	Second Field:
R1D	1 = 1.225V Voltage Option
R1E	A = Adjustable
	Third Field:
RAC	A–E = Initial Reverse Breakdown
RAD	Voltage or Reference Voltage Tolerance
	$A = \pm 0.1\%$, $B = \pm 0.2\%$, $C = \pm 0.5\%$, $D = \pm 1.0\%$, $E = \pm 2.0\%$

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Reverse Current 20 mA Forward Current 10 mA

Maximum Output Voltage

(LM4041-ADJ) 15V

Power Dissipation ($T_A = 25^{\circ}C$) (Note 2)

 M3 Package
 306 mW

 Z Package
 550 mW

 M7 Package
 241mW

 Storage Temperature
 -65°C to +150°C

Lead Temperature M3 Packages

Vapor phase (60 seconds) +215°C

Infrared (15 seconds) +220°C

Z Package

Soldering (10 seconds) +260°C

ESD Susceptibility

Human Body Model (Note 3) 2 kV Machine Model (Note 3) 200V

See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of soldering surface mount devices.

Operating Ratings(Notes 1, 2)

Reverse Current

LM4041-1.2 60 μA to 12 mA LM4041-ADJ 60 μA to 12 mA

Output Voltage Range

LM4041-ADJ 1.24V to 10V

LM4041-1.2

Electrical Characteristics (Industrial Temperature Range)

Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX} ; all other limits $T_A = T_J = 25^{\circ}C$. The grades A and B designate initial Reverse Breakdown Voltage tolerances of $\pm 0.1\%$ and $\pm 0.2\%$, respectively.

Symbol	Parameter	Conditions	Typical	LM4041AIM3	LM4041BIM3	Units
			(Note 4)	LM4041AIZ	LM4041BIZ	(Limit)
				Limits	LM4041BIM7	
				(Note 5)	Limits	
					(Note 5)	
V_R	Reverse Breakdown Voltage	I _R = 100 μA	1.225			V
	Reverse Breakdown Voltage	I _R = 100 μA		±1.2	±2.4	mV (max)
	Tolerance (Note 6)			±9.2	±10.4	mV (max)
I _{RMIN}	Minimum Operating Current		45			μΑ
				60	60	μA (max)
				65	65	μA (max)
$\Delta V_R/\Delta T$	Average Reverse Breakdown	I _R = 10 mA	±20			ppm/°C
	Voltage Temperature	I _R = 1 mA	±15	±100	±100	ppm/°C (max)
	Coefficient (Note 6)	I _R = 100 μA	±15			ppm/°C
$\Delta V_R/\Delta I_R$	Reverse Breakdown Voltage	$I_{RMIN} \le I_{R} \le 1 \text{ mA}$	0.7			mV
	Change with Operating			1.5	1.5	mV (max)
	Current Change			2.0	2.0	mV (max)
	(Note 9)	1 mA ≤ I _R ≤ 12 mA	4.0			mV
				6.0	6.0	mV (max)
				8.0	8.0	mV (max)
Z _R	Reverse Dynamic Impedance	$I_R = 1 \text{ mA}, f = 120 \text{ Hz},$	0.5			Ω
		I _{AC} = 0.1 I _R		1.5	1.5	Ω (max)
e _N	Wideband Noise	I _R = 100 μA	20			μV_{rms}
		10 Hz ≤ f ≤ 10 kHz				
ΔV_{R}	Reverse Breakdown Voltage	t = 1000 hrs				
	Long Term Stability	T = 25°C ±0.1°C	120			ppm
		I _R = 100 μA				
V _{HYST}	Thermal Hysteresis (Note 10)	$\Delta T = -40^{\circ} \text{C to } +125^{\circ} \text{C}$	0.08			%

LM4041-1.2

Electrical Characteristics (Industrial Temperature Range) (Continued)

LM4041-1.2

Electrical Characteristics (Industrial Temperature Range)

Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX} ; all other limits $T_A = T_J = 25^{\circ}$ C. The grades C, D and E designate initial Reverse Breakdown Voltage tolerances of $\pm 0.5\%$, $\pm 1.0\%$ and $\pm 2.0\%$, respectively.

Symbol	Parameter	Conditions	Typical (Note 4)	LM4041CIM3 LM4041CIZ LM4041CIM7 Limits (Note 5)	LM4041DIM3 LM4041DIZ LM4041DIM7 Limits (Note 5)	LM4041EIM3 LM4041EIZ LM4041EIM7 Limits (Note 5)	Units (Limit)
V _R	Reverse Breakdown Voltage	I _R = 100 μA	1.225				V
	Reverse Breakdown Voltage	I _R = 100 μA		±6	±12	±25	mV (max)
	Tolerance (Note 6)			±14	±24	±36	mV (max)
I _{RMIN}	Minimum Operating Current		45				μΑ
				60	65	65	μA (max)
				65	70	70	μA (max)
$\Delta V_R / \Delta T$		I _R = 10 mA	±20				ppm/°C
	Coefficient (Note 6)	$I_R = 1 \text{ mA}$	±15	±100	±150	±150	ppm/°C (max)
		I _R = 100 μA	±15				ppm/°C
$\Delta V_R / \Delta I_R$	Reverse Breakdown Voltage Change with	$I_{RMIN} \le I_{R} \le 1 \text{ mA}$	0.7				mV
	Operating Current Change			1.5	2.0	2.0	mV (max)
	(Note 9)			2.0	2.5	2.5	mV (max)
		1 mA ≤ I _R ≤ 12 mA	2.5				mV
				6.0	8.0	8.0	mV (max)
				8.0	10.0	10.0	mV (max)
Z _R	Reverse Dynamic Impedance	I _R = 1 mA, f = 120 Hz	0.5				Ω
		$I_{AC} = 0.1 I_{R}$		1.5	2.0	2.0	Ω(max)
e _N	Wideband Noise	I _R = 100 μA	20				μV_{rms}
		10 Hz ≤ f ≤ 10 kHz					
ΔV_R	Reverse Breakdown	t = 1000 hrs					
	Voltage Long Term	$T = 25^{\circ}C \pm 0.1^{\circ}C$	120				ppm
	Stability	I _R = 100 μA					
V _{HYST}	Thermal Hysteresis (Note 10)	$\Delta T = -40$ °C to +125°C	0.08				%

LM4041-1.2 Electrical Characteristics (Extended Temperature Range)

Boldface limits apply for T_A = T_J = T_{MIN} to T_{MAX}; all other limits $T_A = T_J = 25^{\circ}C$. The grades C, D and E designate initial Reverse Breakdown Voltage tolerance of $\pm 0.5\%$, $\pm 1.0\%$ and $\pm 2.0\%$ respectively.

Symbol	Parameter	Conditions	Typical	LM4041CEM3	LM4041DEM3	LM4041EEM3	Units
			(Note 4)	Limits	Limits	Limits	(Limit)
				(Note 5)	(Note 5)	(Note 5)	
V _R	Reverse Breakdown	I _R = 100 μA	1.225				V
	Voltage						
	Reverse Breakdown	I _R = 100 μA		±6	±12	±25	mV (max)
	Voltage Error						
	(Note 6)			±18.4	±31	±43	mV (max)
I_{RMIN}	Minimum Operating		45				μΑ
	Current			60	65	65	μA (max)
				68	73	73	μA (max)
$\Delta V_R/\Delta T$	VR Temperature	I _R = 10 mA	±20				ppm/°C
	Coefficient(Note 6)	I _R = 1 mA	±15	±100	±150	±150	ppm/°C
							(max)
		I _R = 100 μA	±15				ppm/°C
$\Delta V_R/\Delta I_R$	Reverse Breakdown	$I_{RMIN} \le I_{R} \le 1.0 \text{ mA}$	0.7				mV
	Change with			1.5	2.0	2.0	mV (max)
	Current			2.0	2.5	2.5	mV (max)
	(Note 9)	$1 \text{ mA} \le I_R \le 12 \text{ mA}$	2.5				mV
				6.0	8.0	8.0	mV (max)
				8.0	10.0	10.0	mV (max)
Z _R	Reverse Dynamic Impedance	$I_R = 1 \text{ mA, f} = 120 \text{ Hz,}$	0.5				Ω
		I _{AC} = 0.1 I _R		1.5	2.0	2.0	Ω (max)
e _N	Noise Voltage	I _R = 100 μA	20				μV_{rms}
		10 Hz ≤ f ≤ 10 kHz					
ΔV _R	Long Term Stability	t = 1000 hrs					
	(Non-Cumulative)	T = 25°C ±0.1°C	120				ppm
		I _R = 100 μA					
V _{HYST}	Thermal Hysteresis (Note 10)	$\Delta T = -40^{\circ}C \text{ to } +125^{\circ}C$	0.08				%

LM4041-ADJ (Adjustable) Electrical Characteristics (Industrial Temperature Range)

Boldface limits apply for T_A = T_J = T_{MIN} to T_{MAX}; all other limits $T_J = 25^{\circ}C$ unless otherwise specified (SOT-23, see (Note 7)), $I_{RMIN} \le I_R \le 12$ mA, $V_{REF} \le V_{OUT} \le 10V$. The grades C and D designate initial Reference Voltage Tolerances of $\pm 0.5\%$ and $\pm 1\%$, respectively for $V_{OUT} = 5V$.

Symbol	Parameter	Conditions	Typical (Note 4)	LM4041CIM3 LM4041CIZ LM4041CIM7 (Note 5)	LM4041DIM3 LM4041DIZ LM4041DIM7 (Note 5)	Units (Limit)
V _{REF}	Reference Voltage	I _R = 100 μA, V _{OUT} = 5V	1.233			V
	Reference Voltage	I _B = 100 μA, V _{OUT} = 5V		±6.2	±12	mV (max)
	Tolerance (Note 8)			±14	±24	mV (max)
I _{RMIN}	Minimum Operating		45			μΑ
	Current			60	65	μA (max)
				65	70	μA (max)
$\Delta V_{REF}/\Delta I_{R}$	Reference Voltage	I _{RMIN} ≤ I _R ≤ 1 mA	0.7			mV
	Change with Operating	SOT-23: V _{OUT} ≥ 1.6V		1.5	2.0	mV (max)
	Current Change	(Note 7)		2.0	2.5	mV (max)
	(Note 9)	1 mA ≤ I _R ≤ 12 mA	2			mV
		SOT-23: V _{OUT} ≥ 1.6V (Note 7)		4	6	mV (max)
				6	8	mV (max)
$\Delta V_{REF}/\Delta V_{O}$	Reference Voltage	I _R = 1 mA	-1.55			mV/V
	Change			-2.0	-2.5	mV/V (max)
	with Output Voltage Change			-2.5	-3.0	mV/V (max)
I _{FB}	Feedback Current		60			nA
				100	150	nA (max)
				120	200	nA (max)
$\Delta V_{REF}/\Delta T$	Average Reference	$V_{OUT} = 5V$, $I_{R} = 10 \text{ mA}$	20			ppm/°C
	Voltage Temperature	$I_R = 1 \text{ mA}$	15	±100	±150	ppm/°C (max)
	Coefficient (Note 8)	$I_R = 100 \mu A$	15			ppm/°C
Z _{OUT}	Dynamic Output Impedance	$I_{R} = 1 \text{ mA, } f = 120 \text{ Hz,}$ $I_{AC} = 0.1 I_{R}$				
		$V_{OUT} = V_{REF}$	0.3			Ω
		V _{OUT} = 10V	2			Ω
e _N	Wideband Noise	$I_R = 100 \mu A$ $V_{OUT} = V_{REF}$ 10 Hz \leq f \leq 10 kHz	20			μV_{rms}
ΔV_{REF}	Reference Voltage Long Term Stability	$t = 1000 \text{ hrs}, I_R = 100 \mu A$ $T = 25^{\circ}C \pm 0.1^{\circ}C$	120			ppm
V _{HYST}	Thermal Hysteresis (Note 10)	$\Delta T = -40^{\circ}C \text{ to } +125^{\circ}C$	0.08			%

LM4041-ADJ (Adjustable) Electrical Characteristics (Extended Temperature Range)

Boldface limits apply for T_A = T_J = T_{MIN}to\ T_{MAX}; all other limits $T_J = 25^{\circ}C$ unless otherwise specified (SOT-23, see (Note 7)), $I_{RMIN} \le I_R \le 12$ mA, $V_{REF} \le V_{OUT} \le 10V$. The grades C and D designate initial Reference Voltage Tolerances of $\pm 0.5\%$ and $\pm 1\%$, respectively for $V_{OUT} = 5V$.

Symbol	Parameter	Conditions	Typical	LM4041CEM3	LM4041DEM3	Units
			(Note 4)	(Note 5)	(Note 5)	(Limit)
V_{REF}	Reference Voltage	$I_R = 100 \ \mu A, \ V_{OUT} = 5V$	1.233			V
	Reference Voltage	$I_R = 100 \ \mu A, \ V_{OUT} = 5V$		±6.2	±12	mV (max)
	Tolerance (Note 8)			±18	±30	mV (max)
I _{RMIN}	Minimum Operating		45			μA
	Current			60	65	μΑ (max)
				68	73	μA (max)
$\Delta V_{REF}/\Delta I_{R}$	Reference Voltage	$I_{RMIN} \le I_{R} \le 1 \text{ mA}$	0.7			mV
	Change with Operating	SOT-23: V _{OUT} ≥ 1.6V		1.5	2.0	mV (max)
	Current Change	(Note 7)		2.0	2.5	mV (max)
	(Note 9)	1 mA ≤ I _R ≤ 12 mA	2			mV
		SOT-23: V _{OUT} ≥ 1.6V(Note 7)		8	10	mV (max)
				6	8	mV (max)
$\Delta V_{REF}/\Delta V_{O}$	Reference Voltage	I _R = 1 mA	-1.55			mV/V
	Change			-2.0	-2.5	mV/V (max)
	with Output Voltage					, ,
	Change			-3.0	-4.0	mV/V (max)
I_{FB}	Feedback Current		60			nA
				100	150	nA (max)
				120	200	nA (max)
$\Delta V_{REF}/\Delta T$	Average Reference	$V_{OUT} = 5V$, $I_{R} = 10 \text{ mA}$	20			ppm/°C
	Voltage Temperature	$I_R = 1 \text{ mA}$	15	±100	±150	ppm/°C (max)
	Coefficient (Note 8)	I _R = 100 μA	15			ppm/°C
Z_{OUT}	Dynamic Output	$I_R = 1 \text{ mA}, f = 120 \text{ Hz},$				
	Impedance	I _{AC} = 0.1 I _R				
		$V_{OUT} = V_{REF}$	0.3			Ω
		V _{OUT} = 10V	2			Ω
e _N	Wideband Noise	$I_R = 100 \mu A$ $V_{OUT} = V_{REF}$	20			μV_{rms}
		10 Hz ≤ f ≤ 10 kHz				
ΔV_{REF}	Reference Voltage Long	$t = 1000 \text{ hrs}, I_R = 100 \mu A$	120			ppm
	Term Stability	T = 25°C ±0.1°C				
V _{HYST}	Thermal Hysteresis (Note 10)	$\Delta T = -40^{\circ}C \text{ to } +125^{\circ}C$	0.08			%

LM4041-ADJ (Adjustable) Electrical Characteristics (Extended Temperature Range) (Continued)

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions

Note 2: The maximum power dissipation must be derated at elevated temperatures and is dictated by T_{Jmax} (maximum junction temperature), θ_{JA} (junction to ambient thermal resistance), and T_A (ambient temperature). The maximum allowable power dissipation at any temperature is $PD_{max} = (T_{Jmax} - T_A)/\theta_{JA}$ or the number given in the Absolute Maximum Ratings, whichever is lower. For the LM4041, $T_{Jmax} = 125^{\circ}C$, and the typical thermal resistance (θ_{JA}) , when board mounted, is 326°C/W for the SOT-23 package, 415°C/W for the SC70 package and 180°C/W with 0.4" lead length and 170°C/W with 0.125" lead length for the TO-92 package.

Note 3: The human body model is a 100 pF capacitor discharged through a 1.5 k Ω resistor into each pin. The machine model is a 200 pF capacitor discharged directly into each pin. All pins are rated at 2kV for Human Body Model, but the feedback pin which is rated at 1kV.

Note 4: Typicals are at T_{.I} = 25°C and represent most likely parametric norm.

Note 5: Limits are 100% production tested at 25°C. Limits over temperature are guaranteed through correlation using Statistical Quality Control (SQC) methods. The limits are used to calculate National's AOQL.

Note 6: The boldface (over-temperature) limit for Reverse Breakdown Voltage Tolerance is defined as the room temperature Reverse Breakdown Voltage Tolerance $\pm [(\Delta V_R)^* \Delta T)(max \ \Delta T)(V_R)]$. Where, $\Delta V_R/\Delta T$ is the V_R temperature coefficient, $max\Delta T$ is the maximum difference in temperature from the reference point of 25 °C to $V_R/\Delta T$ to $V_R/\Delta T$ is the reverse breakdown voltage. The total over-temperature tolerance for the different grades in the industrial temperature range where $V_R/\Delta T$ is shown below:

A-grade: $\pm 0.75\% = \pm 0.1\% \pm 100 \text{ ppm/}^{\circ}\text{C x } 65^{\circ}\text{C}$ B-grade: $\pm 0.85\% = \pm 0.2\% \pm 100 \text{ ppm/}^{\circ}\text{C x } 65^{\circ}\text{C}$ C-grade: $\pm 1.15\% = \pm 0.5\% \pm 100 \text{ ppm/}^{\circ}\text{C x } 65^{\circ}\text{C}$ D-grade: $\pm 1.98\% = \pm 1.0\% \pm 150 \text{ ppm/}^{\circ}\text{C x } 65^{\circ}\text{C}$ E-grade: $\pm 2.98\% = \pm 2.0\% \pm 150 \text{ ppm/}^{\circ}\text{C x } 65^{\circ}\text{C}$

The total over-temperature tolerance for the different grades in the extended temperature range where max $\Delta T = 100$ °C is shown below:

B-grade: $\pm 1.2\% = \pm 0.2\% \pm 100 \text{ ppm/}^{\circ}\text{C x } 100^{\circ}\text{C}$ C-grade: $\pm 1.5\% = \pm 0.5\% \pm 100 \text{ ppm/}^{\circ}\text{C x } 100^{\circ}\text{C}$ D-grade: $\pm 2.5\% = \pm 1.0\% \pm 150 \text{ ppm/}^{\circ}\text{C x } 100^{\circ}\text{C}$ E-grade: $\pm 4.5\% = \pm 2.0\% \pm 150 \text{ ppm/}^{\circ}\text{C x } 100^{\circ}\text{C}$

Therefore, as an example, the A-grade LM4041-1.2 has an over-temperature Reverse Breakdown Voltage tolerance of ±1.2V x 0.75% = ±9.2 mV.

Note 7: When $V_{OUT} \le 1.6V$, the LM4041-ADJ in the SOT-23 package must operate at reduced I_R . This is caused by the series resistance of the die attach between the die (-) output and the package (-) output pin. See the Output Saturation (SOT-23 only) curve in the Typical Performance Characteristics section.

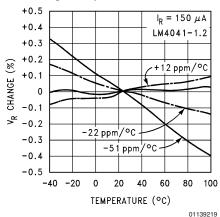
Note 8: Reference voltage and temperature coefficient will change with output voltage. See Typical Performance Characteristics curves.

Note 9: Load regulation is measured on pulse basis from no load to the specified load current. Outure changes due to die temperature change must be taken into account separately.-

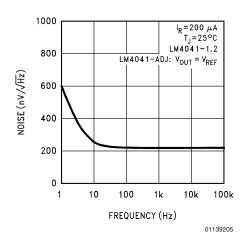
Note 10: Thermal hysteresis is defined as the difference in voltage measured at +25°C after cycling to temperature -40°C and the 25°C measurement after cycling to temperature +125°C.

Typical Performance Characteristics

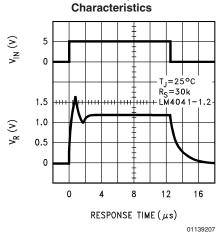
Temperature Drift for Different Average Temperature Coefficient



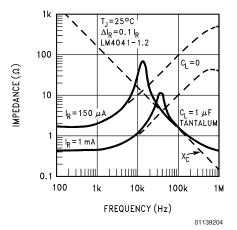
Noise Voltage



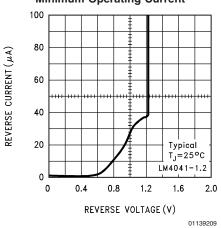
Start-Up

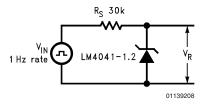


Output Impedance vs Frequency



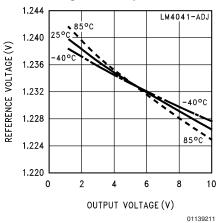
Reverse Characteristics and Minimum Operating Current

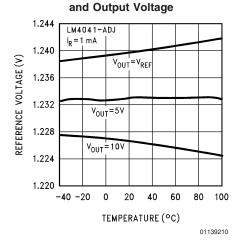




Typical Performance Characteristics (Continued)

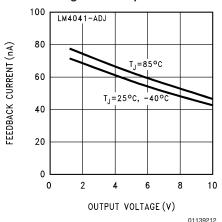
Reference Voltage vs Output Voltage and Temperature



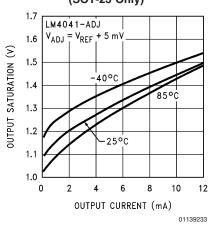


Reference Voltage vs Temperature

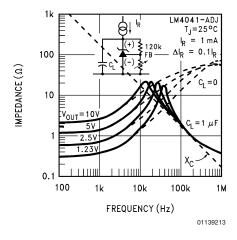
Feedback Current vs Output Voltage and Temperature



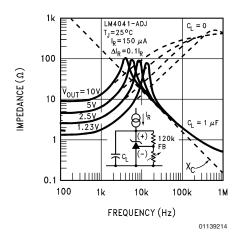
Output Saturation (SOT-23 Only)



Output Impedance vs Frequency

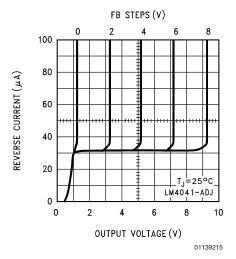


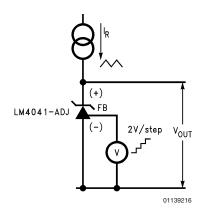
Output Impedance vs Frequency



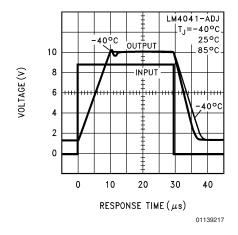
Typical Performance Characteristics (Continued)

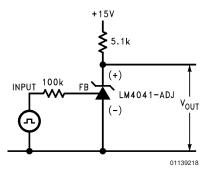
Reverse Characteristics



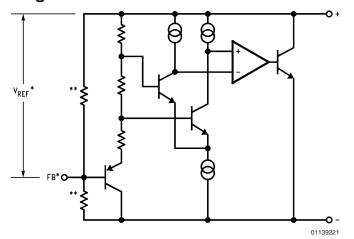


Large Signal Response





Functional Block Diagram



*LM4041-ADJ only

**LM4041-1.2 only

Applications Information

The LM4041 is a precision micro-power curvature-corrected bandgap shunt voltage reference. For space critical applications, the LM4041 is available in the sub-miniature SOT-23 and SC70 surface-mount package. The LM4041 has been designed for stable operation without the need of an external capacitor connected between the "+" pin and the "-" pin. If, however, a bypass capacitor is used, the LM4041 remains stable. Design effort is further reduced with the choice of either a fixed 1.2V or an adjustable reverse breakdown voltage. The minimum operating current is 60 μA for the LM4041-1.2 and the LM4041-ADJ. Both versions have a maximum operating current of 12 mA.

LM4041s using the SOT-23 package have pin 3 connected as the (-) output through the package's die attach interface. Therefore, the LM4041-1.2's pin 3 must be left floating or connected to pin 2 and the LM4041-ADJ's pin 3 is the (-) output.

LM4041s using the SC70 package have pin 2 connected as the (-) output through the packages' die attach interface. Therefore, the LM4041-1.2's pin 2 must be left floating or connected to pin 1, and the LM4041-ADJ's pin 2 is the (-) output.

The typical thermal hysteresis specification is defined as the change in +25°C voltage measured after thermal cycling. The device is thermal cycled to temperature -40°C and then measured at 25°C. Next the device is thermal cycled to temperature +125°C and again measured at 25°C. The resulting $V_{\rm OUT}$ delta shift between the 25°C measurements is thermal hysteresis. Thermal hysteresis is common in precision references and is induced by thermal-mechanical package stress. Changes in environmental storage temperature, operating temperature and board mounting temperature are all factors that can contribute to thermal hysteresis.

In a conventional shunt regulator application (Figure 1), an external series resistor ($R_{\rm S}$) is connected between the supply voltage and the LM4041. $R_{\rm S}$ determines the current that flows through the load ($I_{\rm L}$) and the LM4041 ($I_{\rm Q}$). Since load current and supply voltage may vary, $R_{\rm S}$ should be small enough to supply at least the minimum acceptable $I_{\rm Q}$ to the LM4041 even when the supply voltage is at its minimum and the load current is at its maximum value. When the supply voltage is at its maximum, $R_{\rm S}$ should be large enough so that the current flowing through the LM4041 is less than 12 mA.

 R_S should be selected based on the supply voltage, (V_S) , the desired load and operating current, $(I_L$ and $I_Q)$, and the LM4041's reverse breakdown voltage, V_B .

$$R_S = \frac{V_S - V_R}{I_L + I_Q}$$

The LM4041-ADJ's output voltage can be adjusted to any value in the range of 1.24V through 10V. It is a function of the internal reference voltage ($V_{\rm REF}$) and the ratio of the external

feedback resistors as shown in $\it Figure~2$. The output voltage is found using the equation

$$V_O = V_{REF}[(R2/R1) + 1]$$
 (1)

where $V_{\rm O}$ is the output voltage. The actual value of the internal $V_{\rm REF}$ is a function of $V_{\rm O}$. The "corrected" $V_{\rm REF}$ is determined by

$$V_{REF} = \Delta V_{O} (\Delta V_{REF} / \Delta V_{O}) + V_{Y}$$
 (2)

where

$$V_{V} = 1.240 \text{ V}$$

and

$$\Delta V_{O} = (V_{O} - V_{Y})$$

 $\Delta V_{REF}/\Delta V_O$ is found in the Electrical Characteristics and is typically –1.55 mV/V. You can get a more accurate indication of the output voltage by replacing the value of V_{REF} in equation (1) with the value found using equation (2).

Note that the actual output voltage can deviate from that predicted using the typical value of $\Delta V_{REF}/\Delta V_O$ in equation (2): for C-grade parts, the worst-case $\Delta V_{REF}/\Delta V_O$ is -2.5 mV/V. For D-grade parts, the worst-case $\Delta V_{REF}/\Delta V_O$ is -3.0 mV/V

Typical Applications

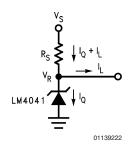


FIGURE 1. Shunt Regulator

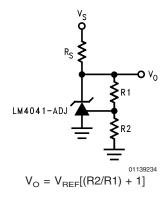


FIGURE 2. Adjustable Shunt Regulator

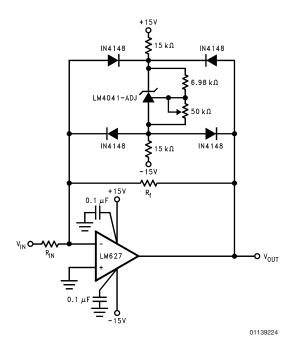


FIGURE 3. Bounded amplifier reduces saturation-induced delays and can prevent succeeding stage damage. Nominal clamping voltage is $\pm V_O$ (LM4041's reverse breakdown voltage) +2 diode V_F .

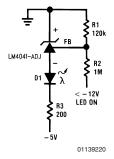


FIGURE 4. Voltage Level Detector

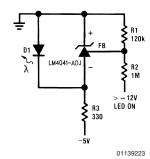


FIGURE 5. Voltage Level Detector

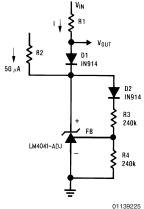
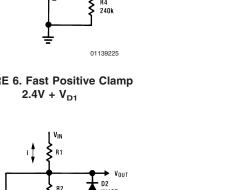


FIGURE 6. Fast Positive Clamp



01139226

FIGURE 7. Bidirectional Clamp ±2.4V

₹ R3 510k

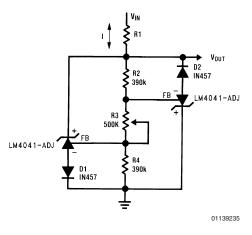


FIGURE 8. Bidirectional Adjustable Clamp ±18V to ±2.4V

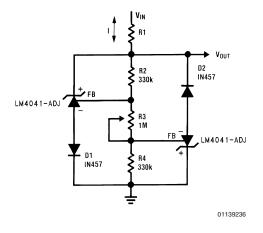


FIGURE 9. Bidirectional Adjustable Clamp ±2.4V to ±6V

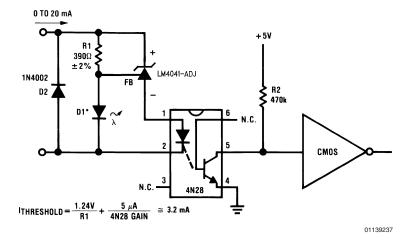


FIGURE 10. Simple Floating Current Detector

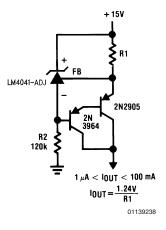


FIGURE 11. Current Source

Note 11: *D1 can be any LED, V_F = 1.5V to 2.2V at 3 mA. D1 may act as an indicator. D1 will be on if $I_{THRESHOLD}$ falls below the threshold current, except with I = 0.

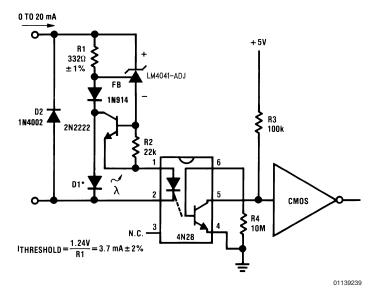


FIGURE 12. Precision Floating Current Detector

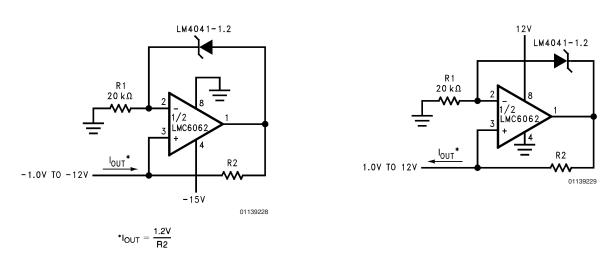


FIGURE 13. Precision 1 µA to 1 mA Current Sources

Physical Dimensions inches (millimeters) unless otherwise noted Α .115±.005 [2.92±0.12] В (.090) (3X .030 [0.76] .051 ± .004 .099+:005 [2.51+0:12] .030 [0.76] 2X [.0375 [0.953] RECOMENDED LAND PATTERN .018-.024 .075 [1.91] R.004 MIN TYP [0.1] .035-.044 R.004 MIN TYP Ç .005±.002 [0.13±0.05] △ .004 [0.1] Ø C .0005-.0040 [0.025-0.102] TYP 3X .0175±.0025 [0.445±0.063] ⊕ .008 [0.21∞ | C | A | B | S .024⁺:004 [0.61⁺⁰:10 [0.61⁺⁰:10] -SEATING PLANE .015±.002 TYP [0.39±0.05] CONTROLLING DIMENSION IS INCH VALUES IN [] ARE MILLIMETERS MF03A (Rev B) Plastic Surface Mount Package (M3) **NS Package Number MF03A** (JEDEC Registration TO-236AB) ___A **├** (0.65) 0.65 В (1.9) 1.25 ±0.1 (5X 0.4) - 5X 0.15-0.30 ⊕ 0.1 CBS AS LAND PATTERN RECOMMENDATION 1.3 RO. 025 MIN TYP GAGE PLANE R0.025 MIN TY 0.2 0.1 C SEATING PLANE-0-0.1 0.4±0.05 TYP

DIMENSIONS ARE IN MILLIMETERS

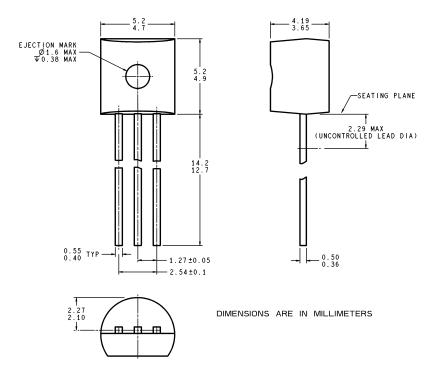
Molded Package (SC70) NS Package Number MAA05A

0.1-0.2

(0.515 TYP)

MAA05A (Rev C)

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



ZO3A (Rev G)

Plastic Package (Z) NS Package Number Z03A

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