

May 1999

# DS26S10 Quad Bus Transceiver

#### **General Description**

The DS26S10 is a quad Bus Transceiver consisting of 4 high speed bus drivers with open-collector outputs capable of sinking 100 mA at 0.8V and 4 high speed bus receivers. Each driver output is connected internally to the high speed bus receiver in addition to being connected to the package pin. The receiver has a Schottky TTL output capable of driving 10 Schottky TTL unit loads.

An active low enable gate controls the 4 drivers so that outputs of different device drivers can be connected together for party-line operation.

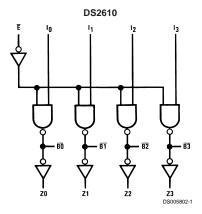
The bus output high-drive capability in the low state allows party-line operation with a line impedance as low as  $100\Omega$ . The line can be terminated at both ends, and still give considerable noise margin at the receiver. The receiver typical switching point is 2V.

The DS26S10 features advanced Schottky processing to minimize propagation delay. The device package also has 2 ground pins to improve ground current handling and allow close decoupling between  $\rm V_{CC}$  and ground at the package. Both GND 1 and GND 2 should be tied to the ground bus external to the device package.

#### **Features**

- Input to bus is inverting on DS26S10
- Quad high speed open-collector bus transceivers
- Driver outputs can sink 100 mA at 0.8V maximum
- Advanced Schottky processing
- PNP inputs to reduce input loading

#### **Logic Diagrams**



#### **Absolute Maximum Ratings** (Note \*NO

TARGET FOR FNXref NS859\*)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Storage Temperature -65°C to +150°C
Temperature (Ambient) Under Bias -55°C to +125°C
Supply Voltage to Ground Potential
DC Voltage Applied to Outputs for
High Output State
DC Input Voltage -0.5V to +5.5V

Output Current, Into Bus 200 mA Output Current, Into Outputs

Output Current, Into Outputs (Except Bus)

30 mA

DC Input Current -30 mA to +5 mA
Maximum Power Dissipation (Note 1) at 25°C
Molded Package 1362 mW

## **Operating Conditions**

	Min	Max	Units
Supply Voltage (V <sub>CC</sub> )			
DS26S10	4.75	5.25	V
Temperature (T <sub>A</sub> )			
DS26S10	0	+70	°C
N 4 A D 4 1 1 00	14/20 1	05:0 1 /	

Note 1: Derate cavity package 9.6 mW/°C above 25°C; derate molded package 10.9 mW/°C above 25°C, derate PLCC package TBD mW/°C above 25°C

#### **Electrical Characteristics**

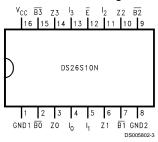
(Unless otherwise noted)

Symbol	Parameter	Conditions		Min	Тур	Max	Units
		(Note 2)			(Note 3)		
V <sub>OH</sub>	Output High Voltage	$V_{CC}$ = Min, $I_{OH}$ = -1 mA,	Military	2.5	3.4		V
	(Receiver Outputs)	$V_{IN} = V_{IL} \text{ or } V_{IH}$	Commercial	2.7	3.4		V
V <sub>OL</sub>	Output Low Voltage	V <sub>CC</sub> = Min, I <sub>OL</sub> = 20 mA,				0.5	V
	(Receiver Outputs)	$V_{IN} = V_{IL} \text{ or } V_{IH}$					
V <sub>IH</sub>	Input High Level	Guaranteed Input Logical High	n for	2.0			V
	(Except Bus)	All Inputs					
V <sub>IL</sub>	Input Low Level	Guaranteed Input Logical Low	for			0.8	V
	(Except Bus)	All Inputs					
V <sub>I</sub>	Input Clamp Voltage	V <sub>CC</sub> = Min, I <sub>IN</sub> = -18 mA				-1.2	V
	(Except Bus)						
I <sub>IL</sub>	Input Low Current	V <sub>CC</sub> = Max, V <sub>IN</sub> = 0.4V	Enable			-0.36	mA
	(Except Bus)		Data			-0.54	mA
I <sub>IH</sub>	Input High Current	$V_{CC} = Max, V_{IN} = 2.7V$	Enable			20	μΑ
	(Except Bus)		Data			30	μΑ
I <sub>I</sub>	Input High Current	V <sub>CC</sub> = Max, V <sub>IN</sub> = 5.5V				100	μΑ
	(Except Bus)						
I <sub>sc</sub>	Output Short-Circuit Current	V <sub>CC</sub> = Max, (Note 4)	Military	-20		-55	mA
	(Except Bus)		Commercial	-18		-60	mA
I <sub>CCL</sub>	Power Supply Current	V <sub>CC</sub> = Max, Enable = GND	DS26S10		45	70	mA
	(All Bus Outputs Low)		DS26S11			80	mA

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# **Connection Diagram**

#### **Dual-In-Line Package**



Top View Order Number DS26S10N See NS Package Number N16A Plastic Chip Carrier

# **Bus Input/Output Characteristics**

Symbol	Parameter	Conditions		Min	Typ(Note	Max	Units
		(Note 2)			3)		
$V_{OL}$	Output Low Voltage		$I_{OL}$ = 40 mA		0.33	0.5	
		V <sub>CC</sub> = Min	I <sub>OL</sub> = 70 mA		0.42	0.05	V
			I <sub>OL</sub> = 100 mA		0.51	0.8	
Io	Bus Leakage Current	)/ - M	V <sub>O</sub> = 0.8V			-50	
		V <sub>CC</sub> = Max	V <sub>O</sub> = 4.5V			100	μA
I <sub>OFF</sub>	Bus Leakage Currrent (power OFF)	V <sub>O</sub> = 4.5V				100	μА
V <sub>TH</sub>	Receiver Input High Threshold	Bus enable = 2.4V		2.25	2.0		V
		V <sub>CC</sub> = Max					V
V <sub>TL</sub>	Receiver Input Low Threshold	Bus enable =	: 2.4V		2.0	1.75	
		V <sub>CC</sub> = Min					

Note 2: For conditions shown as min or max, use the appropriate value specified under Electrical Characteristics for the applicable device type.

Note 3: Typical limits are at  $V_{CC}$  = 5V, 25°C ambient and maximum loading.

Note 4: Not more than one output should be shorted at a time. Duration of the short circuit test should not exceed one second.

## **Switching Characteristics**

 $(T_A = 25^{\circ}C, V_{CC} = 5V)$ 

Symbol	Parameter	Conditions		Min	Тур	Max	Units
t <sub>PLH</sub>	Data Input to Bus	$R_B = 50\Omega, C_B = 50 \text{ pF (Note 5)}$	DS26S10		10	15	ns
t <sub>PHL</sub>	Data Input to Bus		D326310		10	15	ns
t <sub>PLH</sub>	Enable Input to Bus		DS26S10		14	18	ns
t <sub>PHL</sub>	Enable Input to Bus		DS20510		13	18	ns
t <sub>PLH</sub>	Bus to Receiver Out	$R_{\rm B} = 50\Omega, R_{\rm L} = 280\Omega, C_{\rm B} = 50 \text{ pF(Note 5)}$			10	15	ns
t <sub>PHL</sub>	Bus to Receiver Out	C <sub>L</sub> = 15 pF			10	15	ns
t <sub>r</sub>	Bus	$R_{\rm B} = 50\Omega, C_{\rm B} = 50 \text{ pF (Note 5)}$		4.0	10		ns
t <sub>f</sub>	Bus			2.0	4.0		ns

Note 5: Includes probe and jig capacitance.

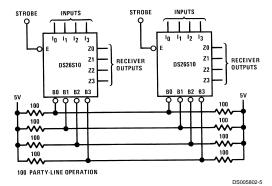
#### **Truth Tables**

#### DS26S10

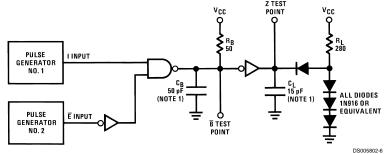
Inp	uts	Outputs		
Ē	I	B	Z	
L	L	Н	L	
L	Н	L	Н	
Н	X	Y	Y	

H = High voltage level
L = Low voltage level
X = Don't care
Y = Voltage level of bus (assumes control by another bus transceiver)

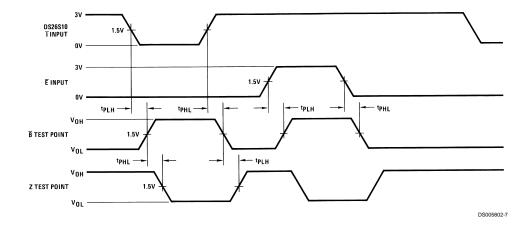
# **Typical Application**



## **AC Test Circuit and Switching Time Waveforms**

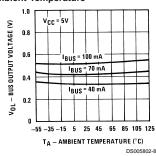


Note 1: Includes probe and jig capacitance.

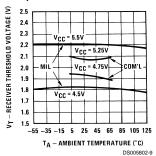


## **Typical Performance Characteristics**

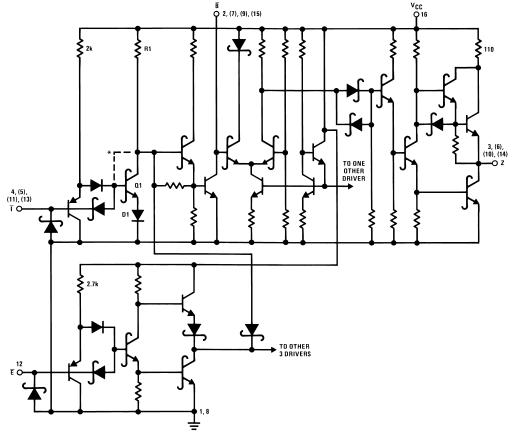
Typical Bus Output Low Voltage vs Ambient Temperature



# Receiver Threshold Variation vs Ambient Temperature



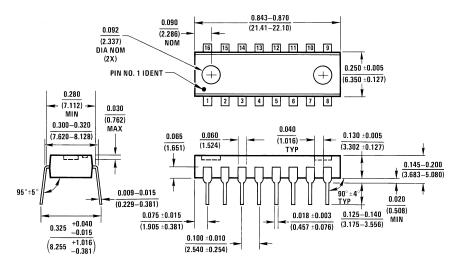
## **Schematic Diagram**



DS005802-10

V<sub>CC</sub> = Pin 16 GND 1 = Pin 1 GND 2 = Pin 8 Connect for DS26S10 \*Remove R1, Q1, D1 for DS26S10

#### Physical Dimensions inches (millimeters) unless otherwise noted



N16A (REV E)

Molded Dual-In-Line Package (N) Order Number DS26S10N NS Package Number N16A

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National Semiconductor Corporation Americas

Tel: 1-800-272-9959 Fax: 1-800-737-7018 Email: support@nsc.com

www.national.com

National Semiconductor Europe

Fax: +49 (0) 1 80-530 85 86 Email: europe.support@nsc.com Deutsch Tel: +49 (0) 1 80-530 85 85 English Tel: +49 (0) 1 80-532 78 32 Français Tel: +49 (0) 1 80-532 93 58 Italiano Tel: +49 (0) 1 80-534 16 80 National Semiconductor Asia Pacific Customer Response Group Tel: 65-2544466 Faxali: sea.support@nsc.com National Semiconductor Japan Ltd. Tel: 81-3-5639-7560 Fax: 81-3-5639-7507