

<b>Absolute Maximum Ratings</b>		<b>Values</b>	<b>Units</b>
<b>Symbol</b>	<b>Conditions<sup>1)</sup></b>		
$V_{CES}$		1700	V
$V_{CGR}$	$R_{GE} = 20 \text{ k}\Omega$	1700	V
$I_C; I_{CN}$	$T_{case} = 25/70^\circ\text{C}$	600 / 500	A
$I_{CM}$	$T_{case} = 25/70^\circ\text{C}; t_p = 1 \text{ ms}$	1200 / 1000	A
$V_{GES}$		± 20	V
$P_{tot}$	per IGBT, $T_{case} = 25^\circ\text{C}$	3000	W
$T_j, (T_{stg})$		-40 ... +150 (125)	°C
$V_{isol}$	AC, 1 min. <sup>4)</sup>	3400	V
humidity	DIN 40 040	Class F	
climate	DIN IEC 68 T.1	40/125/56	
Inverse Diode <sup>8)</sup>			
$I_F = -I_C$	$T_{case} = 25/70^\circ\text{C}$	550 / 420	A
$I_{FM} = -I_{CM}$	$T_{case} = 25/70^\circ\text{C}; t_p = 1 \text{ ms}$	1200 / 1000	A
$I_{FSM}$	$t_p = 10 \text{ ms}; \sin.; T_j = 150^\circ\text{C}$	4400	A
$I^2t$	$t_p = 10 \text{ ms}; T_j = 150^\circ\text{C}$	96800	A <sup>2</sup> s

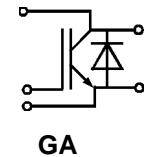
## SEMITRANS® M Low Loss IGBT Modules

### SKM 500 GA 174 D

#### Preliminary Data



SEMITRANS 4



#### Features

- MOS input (voltage controlled)
- N channel, homogeneous Silicon structure (NPT- Non punch-through IGBT)
- Low inductance case
- Low tail current with low temperature dependence
- High short circuit capability, self limiting to  $4 * I_{cnom}$
- Latch-up free
- Fast & soft inverse CAL diodes<sup>8)</sup>
- Isolated copper baseplate using DCB Direct Copper Bonding
- Large clearance (13 mm) and creepage distances (20 mm)

#### Typical Applications

- AC inverter drives on mains 575 - 750 V<sub>AC</sub>
- DC bus voltage 750 - 1200 V<sub>DC</sub>
- Public transport (auxiliary syst.)
- Switching (not for linear use)

<sup>1)</sup>  $T_{case} = 25^\circ\text{C}$ , unless otherwise specified

<sup>2)</sup>  $I_F = -I_C$ ,  $V_R = 1200 \text{ V}$ ,  $-\frac{dI_F}{dt} = 1500 \text{ A}/\mu\text{s}$ ,  $V_{GE} = 0 \text{ V}$

<sup>3)</sup> Use  $V_{GEoff} = -5 \dots -15 \text{ V}$

<sup>4)</sup> Option  $V_{isol} = 4000 \text{ V}/1 \text{ min}$  add suffix „H4“ - on request

<sup>8)</sup> CAL = Controlled Axial Lifetime Technology

Cases and mech. data → B6-280

<b>Characteristics</b>		<b>min.</b>	<b>typ.</b>	<b>max.</b>	<b>Units</b>
<b>Symbol</b>	<b>Conditions<sup>1)</sup></b>				
$V_{(BR)CES}$	$V_{GE} = 0$ , $I_C = 8 \text{ mA}$	≥ $V_{CES}$	—	—	V
$V_{GE(th)}$	$V_{GE} = V_{CE}$ , $I_C = 24 \text{ mA}$	4,8	5,5	6,2	V
$I_{CES}$	$V_{GE} = 0$ } $T_j = 25^\circ\text{C}$	—	0,1	0,8	mA
	$V_{CE} = V_{CES}$ } $T_j = 125^\circ\text{C}$	—	16	—	mA
$I_{GES}$	$V_{GE} = 20 \text{ V}$ , $V_{CE} = 0$	—	—	100	nA
$V_{CEsat}$	$I_C = 400 \text{ A}$ } $V_{GE} = 15 \text{ V}$	—	2,8(3,25)	3,3	V
	$I_C = 500 \text{ A}$ } $T_j = 25 \text{ (125)}^\circ\text{C}$	—	3,0(3,6)	—	V
$g_{fs}$	$V_{CE} = 20 \text{ V}$ , $I_C = 400 \text{ A}$	—	220	—	S
$C_{CHC}$	per IGBT	—	—	1,4	nF
$C_{ies}$	} $V_{GE} = 0$	—	20	—	nF
$C_{oes}$	} $V_{CE} = 25 \text{ V}$	—	3,8	—	nF
$C_{res}$	$f = 1 \text{ MHz}$	—	1,3	—	nF
$L_{CE}$		—	—	20	nH
$t_{d(on)}$	$V_{CC} = 1200 \text{ V}$	—	150	—	ns
$t_r$	$V_{GE} = -15 \text{ V} / +15 \text{ V}$ <sup>3)</sup>	—	120	—	ns
$t_{d(off)}$	$I_C = 400 \text{ A}$ , ind. load	—	1000	—	ns
$t_f$	$R_{Gon} = R_{Goff} = 4,7 \Omega$	—	150	—	ns
$E_{on}$	$T_j = 125^\circ\text{C}$	—	280	—	mWs
$E_{off}$	$L_S = 60 \text{ nH}$	—	160	—	mWs
Inverse Diode <sup>8)</sup>					
$V_F = V_{EC}$	$I_F = 400 \text{ A}$ } $V_{GE} = 0 \text{ V}$	—	2,2(1,9)	2,7(2,4)	V
	$I_F = 500 \text{ A}$ } $T_j = 25 \text{ (125)}^\circ\text{C}$	—	2,4(2,3)	—	V
$V_{TO}$	$T_j = 125^\circ\text{C}$	—	1,3	1,5	V
$r_t$	$T_j = 125^\circ\text{C}$	—	2	2,5	mΩ
$I_{RRM}$	$I_F = 400 \text{ A}$ ; $T_j = 25 \text{ (125)}^\circ\text{C}$ <sup>2)</sup>	—	200(300)	—	A
$Q_{rr}$	$I_F = 400 \text{ A}$ ; $T_j = 25 \text{ (125)}^\circ\text{C}$ <sup>2)</sup>	—	48(116)	—	μC
Thermal characteristics					
$R_{thjc}$	per IGBT	—	—	0,041	°C/W
$R_{thjc}$	per diode D	—	—	0,075	°C/W
$R_{thch}$	per module	—	—	0,038	°C/W

# SKM 500 GA 174 D

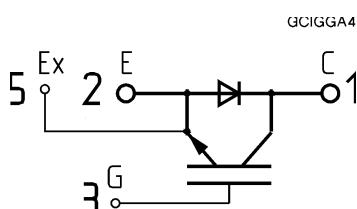
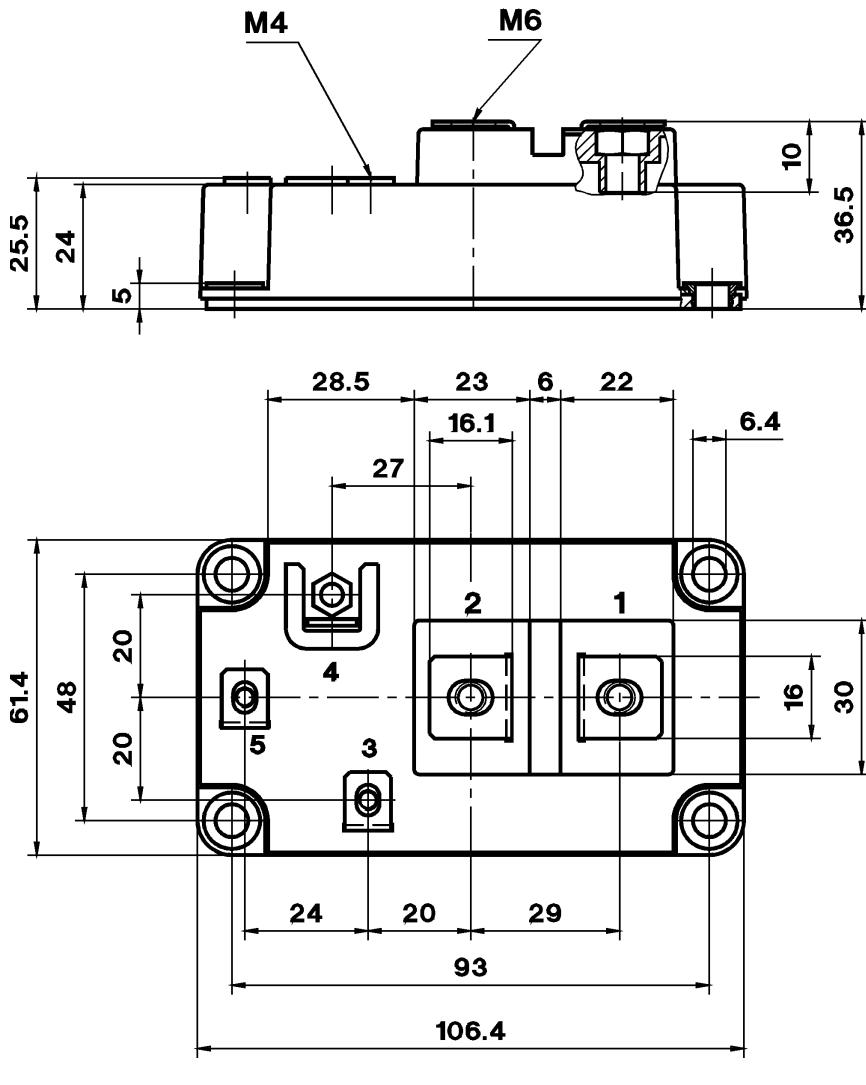
## SEMITRANS 4

### Case D 59

UL Recognition  
File no. E 63 532  
applied for

CASED59

SKM 500 GA 174 D



GCIGGA4

Dimensions in mm

Option SKM 500 GA 174 DS on request:  
Terminal 4 = collector sense  $V_{CE}$ , add suffix „S“. → B 6 – 212

Case outline and circuit diagram

Symbol	Conditions	Values			Units	
		min.	typ.	max.		
M <sub>1</sub>	to heatsink, SI Units	(M6)	3	–	5	Nm
M <sub>2</sub>	to heatsink, US Units		27	–	44	lb.in.
a	for terminals, SI Units	(M6/M4)	2,5/1,1	–	5/2	Nm
w	for terminals, US Units		22/10	–	44/18	lb.in.
		–	–	5x9,81	m/s <sup>2</sup>	
		–	–	330	g	

This is an electrostatic discharge sensitive device (ESDS). Please observe the international standard IEC 747-1, Chapter IX.

Three devices are supplied in one SEMIBOX B without mounting hardware, which can be ordered separately under Ident No. 33321100 (for 10 SEMITRANS 4)

Larger packing units of 12 or 20 pieces are used if suitable

Accessories → B 6 – 4

SEMIBOX → C – 1.