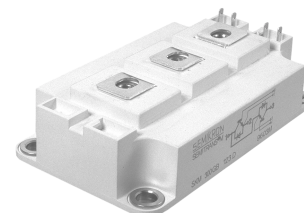
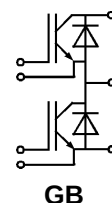


SEMITRANS® M Low Loss IGBT Modules

SKM 300 GB 174 D



SEMITRANS 3



Features

- MOS input (voltage controlled)
- N channel, homogeneous Silicon structure (NPT- Non punch-through IGBT)
- Low inductance case
- Low tail current with low temperature dependence
- High short circuit capability, self limiting
- Latch-up free
- Fast & soft inverse CAL diodes ⁸⁾
- Isolated copper baseplate using DCB Direct Copper Bonding
- Large clearance (13 mm) and creepage distances (20 mm)

Typical Applications

- AC inverter drives on mains 575 - 750 V_{AC}
- DC bus voltage 750 - 1200 V_{DC}
- Public transport (auxiliary syst.)
- Switching (not for linear use)

¹⁾ T_{case} = 25 °C, unless otherwise specified

²⁾ I_F = - I_C, V_R = 1200 V, -di_F/dt = 2000 A/μs, V_{GE} = 0 V

³⁾ Use V_{GEoff} = -5 ... -15 V

⁴⁾ Option V_{isol} = 4000V/1 min add suffix „H4“ - on request

⁸⁾ CAL = Controlled Axial Lifetime Technology

Cases and mech. data → page 6

Absolute Maximum Ratings		Values	Units
Symbol	Conditions ¹⁾		
V _{CES}		1700	V
V _{CGR}	R _{GE} = 20 kΩ	1700	V
I _C ; I _{CN}	T _{case} = 25/70 °C	370 / 285	A
I _{CM}	T _{case} = 25/70 °C; t _p = 1 ms	740 / 570	A
V _{GES}		± 20	V
P _{tot}	per IGBT, T _{case} = 25 °C	2100	W
T _J , (T _{stg})		-40 ... +150 (125)	°C
V _{isol}	AC, 1 min. ⁴⁾	3400	V
humidity	IEC 60721-3-3		
climate	DIN IEC 68 T.1	40/125/56	
Inverse Diode ⁸⁾			
I _F = -I _C	T _{case} = 25/70 °C	370 / 285	A
I _{FM} = -I _{CM}	T _{case} = 25/70 °C; t _p = 1 ms	740 / 570	A
I _{FSM}	t _p = 10 ms; sin.; T _J = 150 °C	2200	A
I ² t	t _p = 10 ms; T _J = 150 °C	24200	A ² s

Characteristics		min.	typ.	max.	Units
Symbol	Conditions ¹⁾				
V _{(BR)CES}	V _{GE} = 0, I _C = 6 mA	≥ V _{CES}	—	—	V
V _{GE(th)}	V _{GE} = V _{CE} , I _C = 9 mA	4,6	5,5	6,5	V
I _{CES}	V _{GE} = 0 } T _J = 25 °C	—	1	1,5	mA
	V _{CE} = V _{CES} } T _J = 125 °C	—	8	—	mA
I _{GES}	V _{GE} = 20 V, V _{CE} = 0	—	—	100	nA
V _{CESat}	I _C = 200 A } V _{GE} = 15 V;	—	2,8(3,25)	3,3(3,8)	V
	I _C = 300 A } T _J = 25 (125) °C }	—	3,3(3,8)	—	V
g _{fs}	V _{CE} = 20 V, I _C = 200 A	80	110	—	S
C _{CHC}	per IGBT	—	—	0,7	nF
C _{ies}	V _{GE} = 0	—	14	—	nF
C _{oes}	V _{CE} = 25 V	—	2,0	—	nF
C _{res}	f = 1 MHz	—	0,6	—	nF
L _{CE}		—	—	20	nH
t _{d(on)}	V _{CC} = 1200 V	—	100	—	ns
t _r	V _{GE} = -15 V / +15 V ³⁾	—	100	—	ns
t _{d(off)}	I _C = 200 A, ind. load	—	900	—	ns
t _f	R _{Gon} = R _{Goff} = 6,8 Ω	—	150	—	ns
E _{on}	T _J = 125 °C	—	125	—	mWs
E _{off}	L _S = 60 nH	—	95	—	mWs
Inverse Diode ⁸⁾					
V _F = V _{EC}	I _F = 200 A } V _{GE} = 0 V;	—	2,15(1,9)	2,4(2,25)	V
V _F = V _{EC}	I _F = 300 A } T _J = 25 (125) °C }	—	2,4(2,2)	2,75(2,5)	V
V _{TO}	T _J = 125 °C	—	1,3	1,5	V
r _t	T _J = 125 °C	—	3	4	mΩ
I _{RRM}	I _F = 200 A; T _J = 25 (125) °C ²⁾	—	100(200)	—	A
Q _{rr}	I _F = 200 A; T _J = 25 (125) °C ²⁾	—	24(60)	—	μC
E _{rr}	I _F = 200 A; T _J = 25 (125) °C ²⁾	—	10(20)	—	mWs
Thermal characteristics					
R _{thjc}	per IGBT	—	—	0,06	°C/W
R _{thjc}	per diode D	—	—	0,125	°C/W
R _{thch}	per module (50 μm grease)	—	—	0,038	°C/W

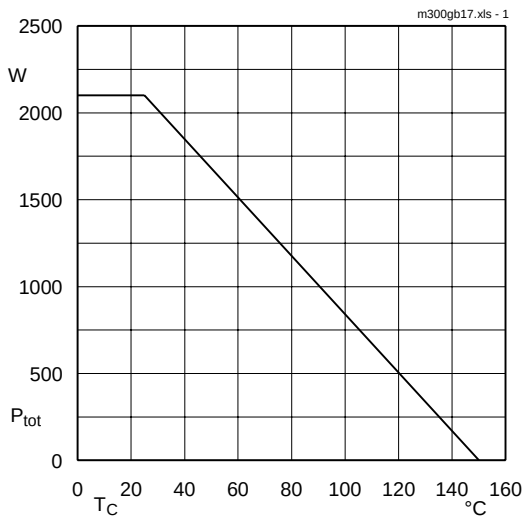


Fig. 1 Rated power dissipation $P_{tot} = f(T_C)$

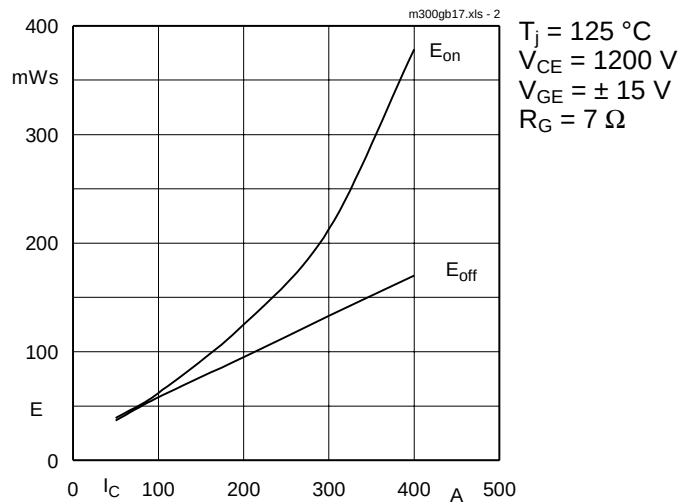


Fig. 2 Turn-on /off energy = $f(I_C)$

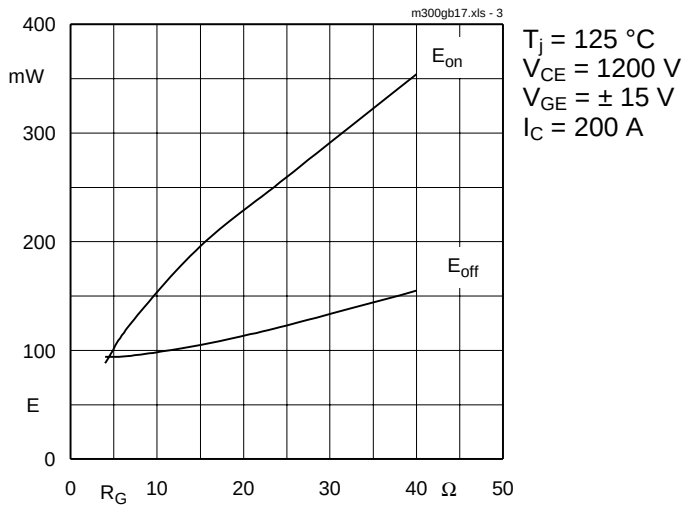


Fig. 3 Turn-on /off energy = $f(R_G)$

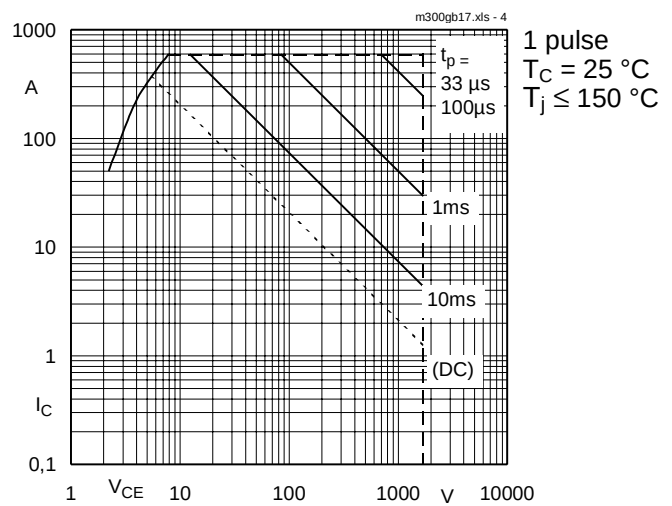


Fig. 4 Maximum safe operating area (SOA) $I_C = f(V_{CE})$

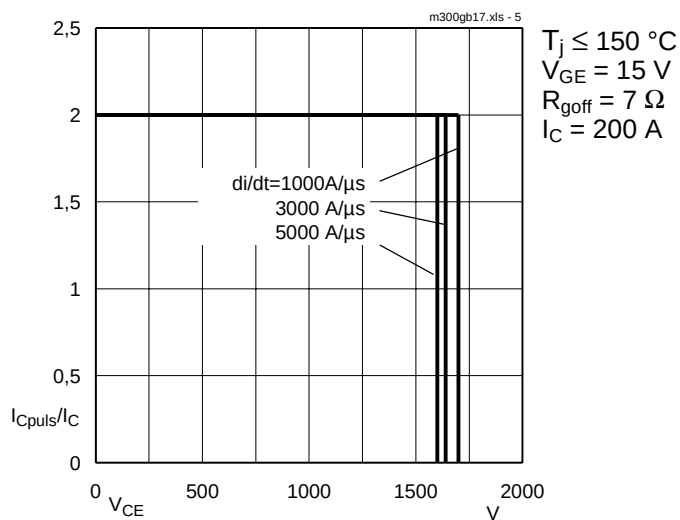


Fig. 5 Turn-off safe operating area (RBSOA)

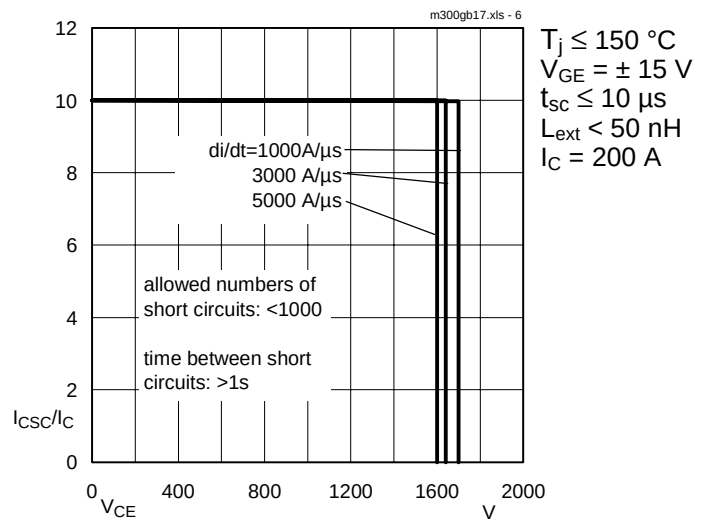


Fig. 6 Safe operating area at short circuit $I_C = f(V_{CE})$

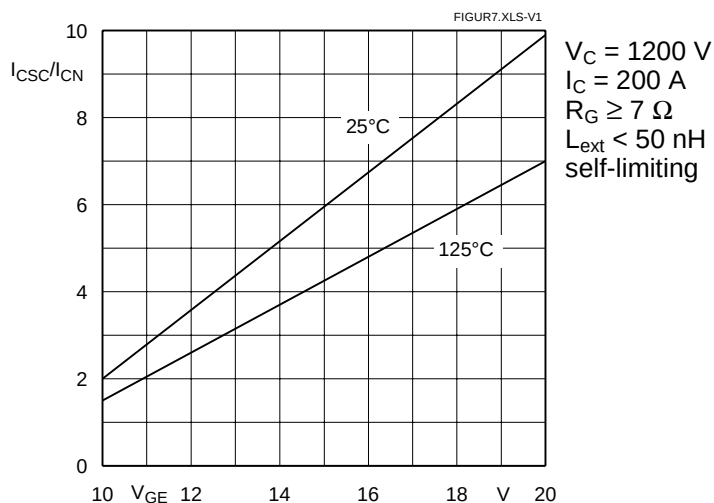


Fig. 7 Short circuit current vs. turn-on gate voltage

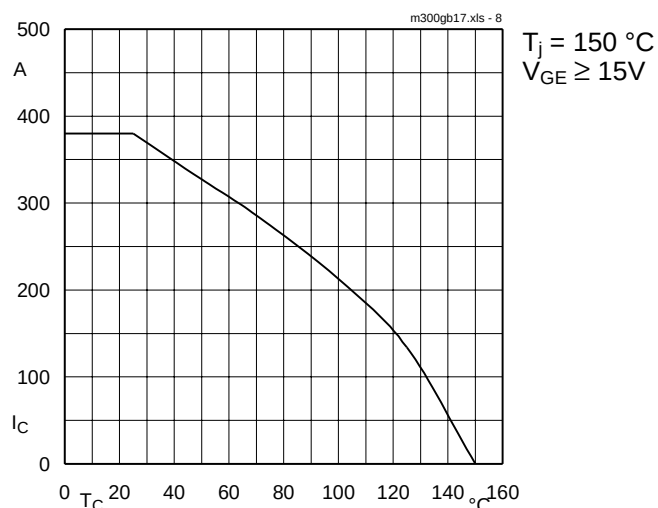


Fig. 8 Rated current vs. temperature $I_C = f(T_C)$

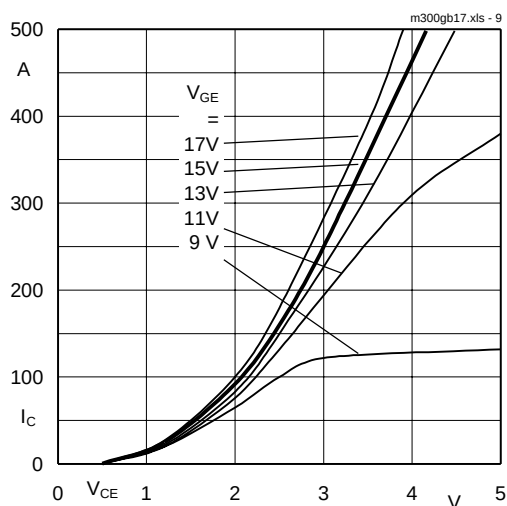


Fig. 9 Typ. output characteristic, $t_p = 250 \mu\text{s}$; $T_J = 25 \text{ °C}$

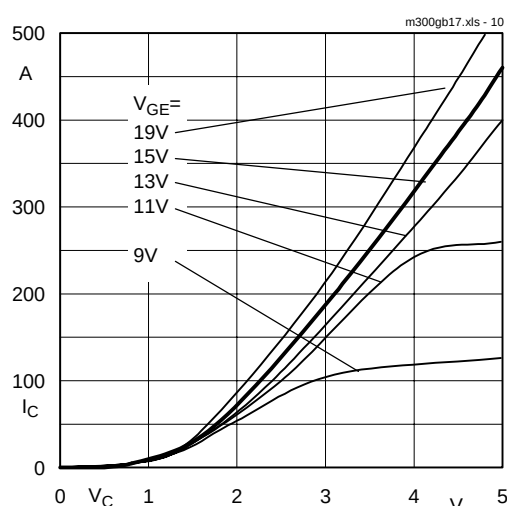


Fig. 10 Typ. output characteristic, $t_p = 250 \mu\text{s}$; $T_J = 125 \text{ °C}$

$$P_{\text{cond}(t)} = V_{\text{CEsat}(t)} \cdot I_{\text{C}(t)}$$

$$V_{\text{CEsat}(t)} = V_{\text{CE(TO)(T}_J)} + r_{\text{CE(T}_J)} \cdot I_{\text{C}(t)}$$

$$V_{\text{CE(TO)(T}_J)} \leq 1,6 + 0,001 (T_J - 25) [\text{V}]$$

$$\text{typ: } r_{\text{CE(T}_J)} = 0,0055 + 0,000023 (T_J - 25) [\Omega]$$

$$\text{max: } r_{\text{CE(T}_J)} \leq 0,0083 + 0,00002 (T_J - 25) [\Omega]$$

$$\text{valid for } V_{GE} \leq +15 \frac{+2}{-1} [\text{V}]; I_C \geq 0,3 I_{\text{Cnom}}$$

Fig. 11 Typ. saturation characteristic (IGBT)
 Calculation elements and equations

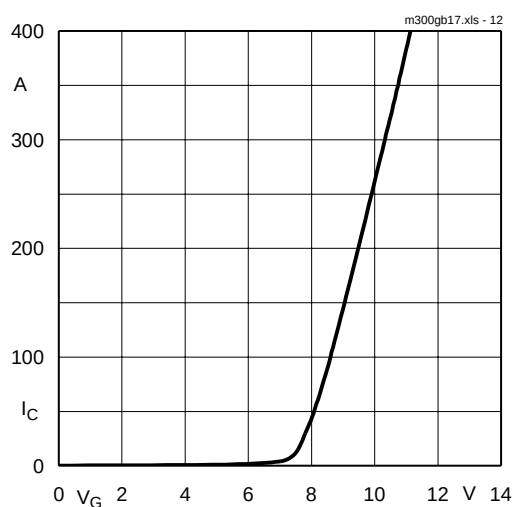


Fig. 12 Typ. transfer characteristic, $t_p = 250 \mu\text{s}$; $V_{CE} = 20 \text{ V}$

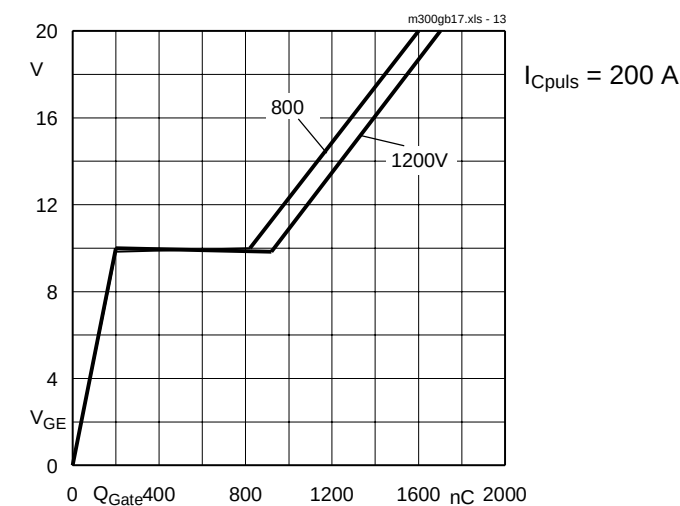


Fig. 13 Typ. gate charge characteristic

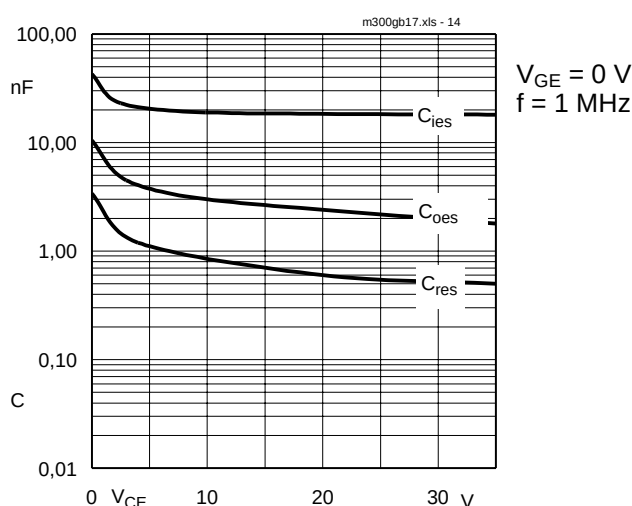


Fig. 14 Typ. capacitances vs. V_{CE}

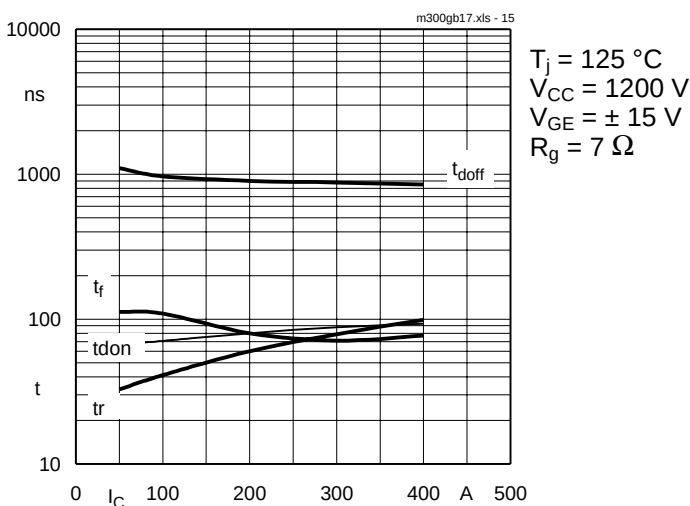


Fig. 15 Typ. switching times vs. I_C

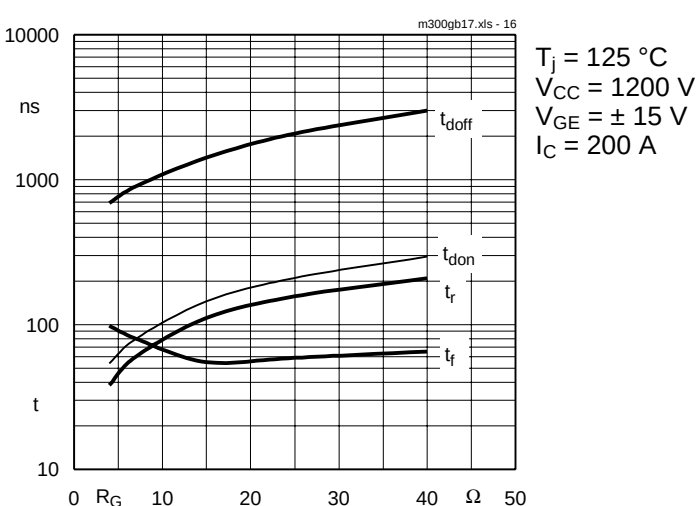


Fig. 16 Typ. switching times vs. gate resistor R_G

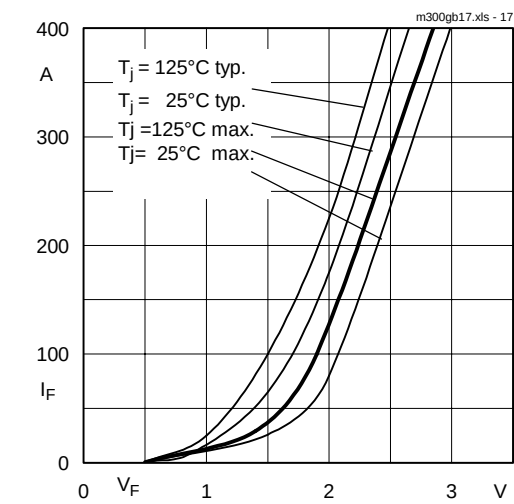


Fig. 17 Typ. CAL diode forward characteristic

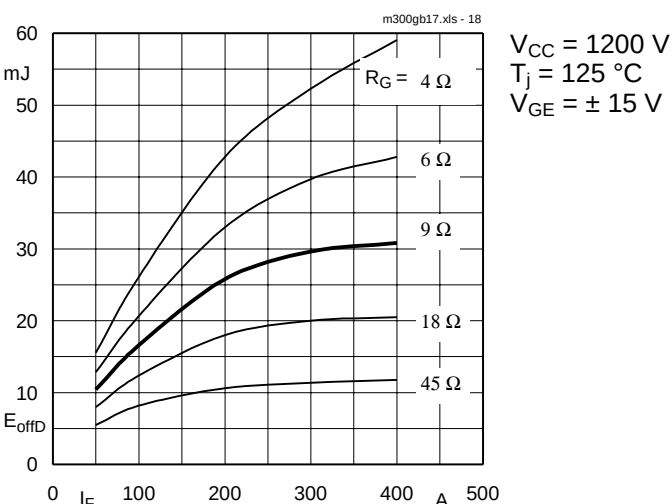


Fig. 18 Diode turn-off energy dissipation per pulse

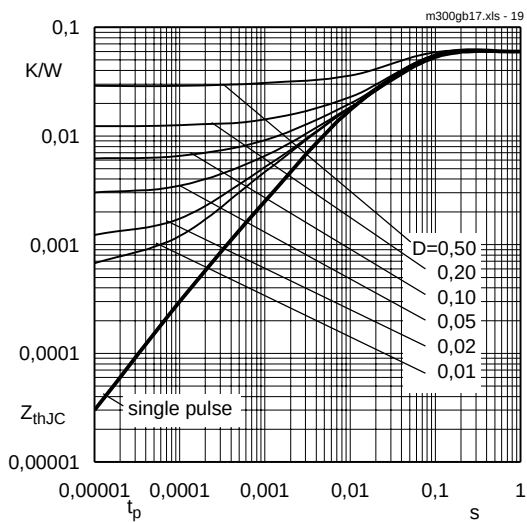


Fig. 19 Transient thermal impedance of IGBT
 $Z_{thJC} = f(t_p)$; $D = t_p / t_c = t_p \cdot f$

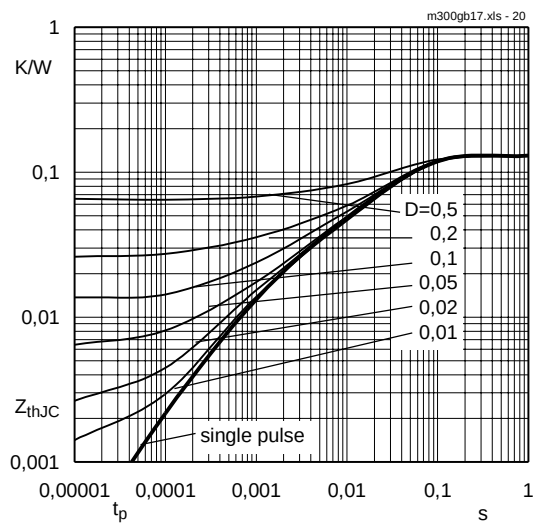


Fig. 20 Transient thermal impedance of inverse CAL diodes
 $Z_{thJC} = f(t_p)$; $D = t_p / t_c = t_p \cdot f$

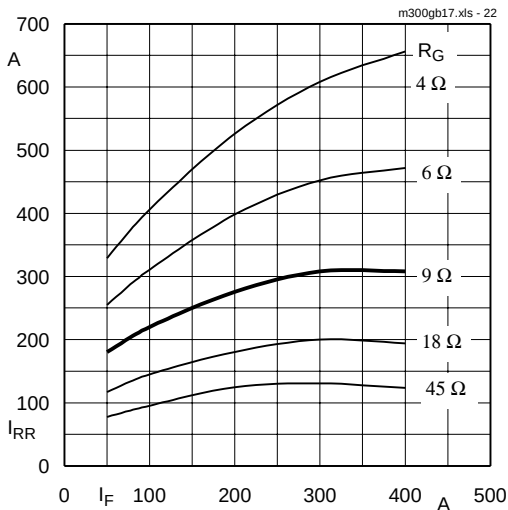


Fig. 22 Typ. CAL diode peak reverse recovery current $I_{RR} = f(I_F; R_G)$

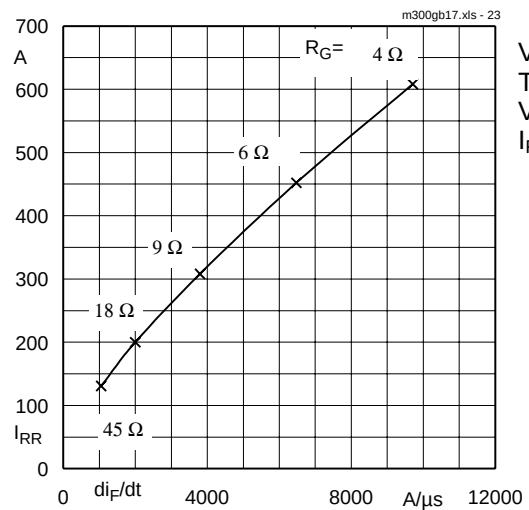


Fig. 23 Typ. CAL diode peak reverse recovery current $I_{RR} = f(di_F/dt)$

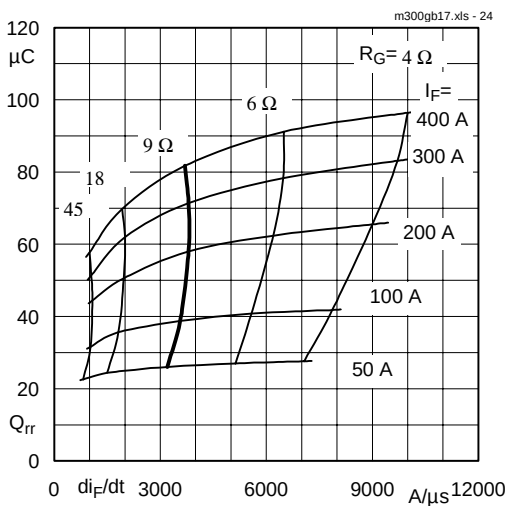
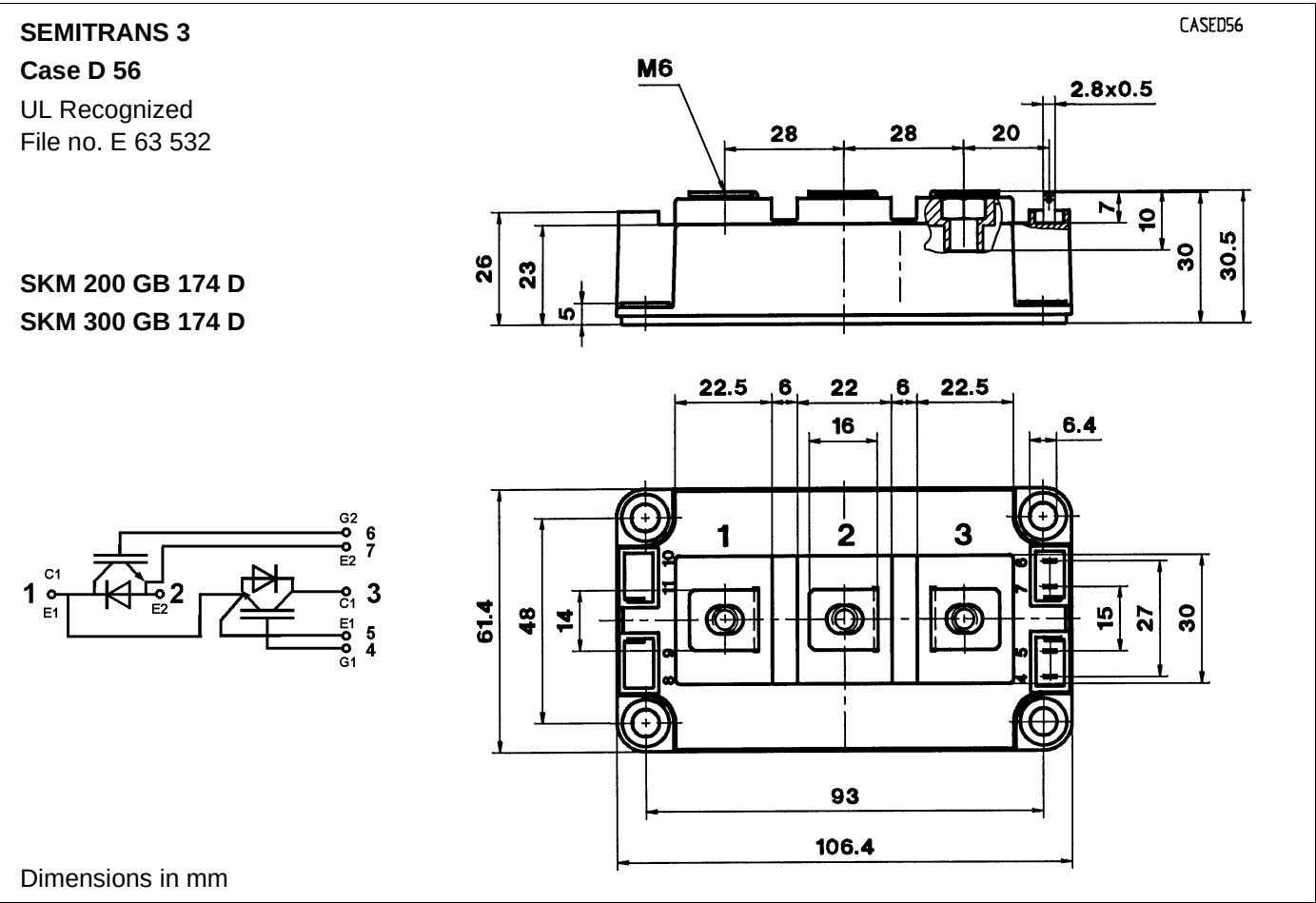


Fig. 24 Typ. CAL diode recovered charge



Case outline and circuit diagram

Mechanical Data		Values			Units
Symbol	Conditions	min.	typ.	max.	
M ₁	to heatsink, SI Units (M6)	3	—	5	Nm
	to heatsink, US Units	27	—	44	lb.in.
M ₂	for terminals, SI Units (M6)	2,5	—	5	Nm
	for terminals, US Units	22	—	44	lb.in.
a		—	—	5x9,81	m/s ²
w		—	—	325	g

This is an electrostatic discharge sensitive device (ESDS). Please observe the international standard IEC 747-1, Chapter IX.

Three devices are supplied in one SEMIBOX A without mounting hardware, which can be ordered separately under Ident No. 33321100 (for 10 SEMITRANS 3)

Larger packing units of 12 or 20 pieces are used if suitable

Accessories → see SEMIKRON Book '99 page B 6 – 4

SEMIBOX → see SEMIKRON Book '99 page C – 1.