

Absolute Maximum Ratings		Values		Units
Symbol	Conditions ¹⁾			
V_{CES}		1700		V
V_{CGR}	$R_{GE} = 20 \text{ k}\Omega$	1700		V
I_C	$T_{case} = 25/80 \text{ }^\circ\text{C}$	220 / 150		A
I_{CM}	$T_{case} = 25/80 \text{ }^\circ\text{C}; t_p = 1 \text{ ms}$	440 / 300		A
V_{GES}		± 20		V
P_{tot}	per IGBT, $T_{case} = 25 \text{ }^\circ\text{C}$	1250		W
$T_j, (T_{stg})$		-40 ... +150 (125)		$^\circ\text{C}$
V_{isol}	AC, 1 min.	4000		V
humidity	DIN 40 040	Class F		
climate	DIN IEC 68 T.1	40/125/56		
Diodes ⁸⁾		Inverse	Series ⁶⁾	
$I_F = -I_C$	$T_{case} = 25/80 \text{ }^\circ\text{C}$	150 / 100	230 / 150	A
$I_{FM} = -I_{CM}$	$T_{case} = 25/80 \text{ }^\circ\text{C}; t_p = 1 \text{ ms}$	400 / 300	440 / 300	A
I_{FSM}	$t_p = 10 \text{ ms}; \sin.; T_j = 150 \text{ }^\circ\text{C}$	1450	2200	A
I^2t	$t_p = 10 \text{ ms}; T_j = 150 \text{ }^\circ\text{C}$	10500	24000	A^2s

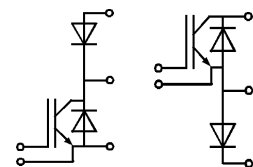
Characteristics		min.	typ.	max.	Units
Symbol	Conditions ¹⁾				
$V_{(BR)CES}$	$V_{GE} = 0, I_C = 3 \text{ mA}$	$\geq V_{CES}$	—	—	V
$V_{GE(th)}$	$V_{GE} = V_{CE}, I_C = 10 \text{ mA}$	4,8	5,5	6,2	V
I_{CES}	$V_{GE} = 0 \quad \left. \begin{array}{l} T_j = 25 \text{ }^\circ\text{C} \\ T_j = 125 \text{ }^\circ\text{C} \end{array} \right\}$	—	—	1,5	mA
	$V_{CE} = V_{CES}$	—	—	4,5	mA
I_{GES}	$V_{GE} = 20 \text{ V}, V_{CE} = 0 \text{ V}$	—	—	400	nA
V_{CESat}	$I_C = 150 \text{ A} \quad \left. \begin{array}{l} V_{GE} = 15 \text{ V;} \\ T_j = 25 (125) \text{ }^\circ\text{C} \end{array} \right\}$	—	3,4(4,5)	3,9(5)	V
V_{CESat}	$I_C = 200 \text{ A} \quad \left. \begin{array}{l} V_{GE} = 15 \text{ V;} \\ T_j = 25 (125) \text{ }^\circ\text{C} \end{array} \right\}$	—	3,8(5,5)	—	V
g_{fs}	$V_{CE} = 20 \text{ V}, I_C = 150 \text{ A}$	54	—	—	S
C_{CHC}	per IGBT	—	—	200	pF
C_{ies}	$V_{GE} = 0$	—	20	—	nF
C_{oes}	$V_{CE} = 25 \text{ V}$	—	2	—	nF
C_{res}	$f = 1 \text{ MHz}$	—	0,55	—	nF
L_{CE}		—	—	20	nH
$t_{d(on)}$	$V_{CC} = 1200 \text{ V}$	—	580	—	ns
t_r	$V_{GE} = +15 \text{ V} / -15 \text{ V}$	—	100	—	ns
$t_{d(off)}$	$I_C = 150 \text{ A, ind. load}$	—	750	—	ns
t_f	$R_{Gon} = R_{Goff} = 4 \text{ } \Omega$	—	40	—	ns
E_{on}	$T_j = 125 \text{ }^\circ\text{C}$	—	95	—	mWs
E_{off}		—	45	—	mWs
Inverse Diode ⁸⁾					
$V_F = V_{EC}$	$I_F = 150 \text{ A} \quad \left. \begin{array}{l} V_{GE} = 0 \text{ V;} \\ T_j = 25 (125) \text{ }^\circ\text{C} \end{array} \right\}$	—	2,2(1,9)	2,7(2,3)	V
$V_F = V_{EC}$	$I_F = 200 \text{ A} \quad \left. \begin{array}{l} V_{GE} = 0 \text{ V;} \\ T_j = 25 (125) \text{ }^\circ\text{C} \end{array} \right\}$	—	2,4(2,2)	—	V
V_{TO}	$T_j = 125 \text{ }^\circ\text{C}$	—	1,3	1,5	V
r_T	$T_j = 125 \text{ }^\circ\text{C}$	—	4,5	6,2	m Ω
I_{RR}	$I_F = 150 \text{ A}; T_j = 25 (125) \text{ }^\circ\text{C} \text{ } ^2)$	—	60(85)	—	A
Q_{rr}	$I_F = 150 \text{ A}; T_j = 25 (125) \text{ }^\circ\text{C} \text{ } ^2)$	—	15(38)	—	μC
Series Diode ^{8) 6)}					
$V_F = V_{EC}$	$I_F = 200 \text{ A} \quad \left. \begin{array}{l} V_{GE} = 0 \text{ V;} \\ T_j = 25 (125) \text{ }^\circ\text{C} \end{array} \right\}$	—	2,2(1,9)	2,7(2,3)	V
$V_F = V_{EC}$	$I_F = 300 \text{ A} \quad \left. \begin{array}{l} V_{GE} = 0 \text{ V;} \\ T_j = 25 (125) \text{ }^\circ\text{C} \end{array} \right\}$	—	2,4(2,2)	—	V
V_{TO}	$T_j = 125 \text{ }^\circ\text{C}$	—	1,3	1,5	V
r_T	$T_j = 125 \text{ }^\circ\text{C}$	—	4	4,5	m Ω
I_{RR}	$I_F = 200 \text{ A}; T_j = 25 (125) \text{ }^\circ\text{C} \text{ } ^2)$	—	100(150)	—	A
Q_{rr}	$I_F = 200 \text{ A}; T_j = 25 (125) \text{ }^\circ\text{C} \text{ } ^2)$	—	24(58)	—	μC
Thermal Characteristics					
R_{thjc}	per IGBT	—	—	0,1	$^\circ\text{C/W}$
R_{thjc}	per inverse/series diode	—	—	0,32/0,20	$^\circ\text{C/W}$
R_{thch}	per module	—	—	0,038	$^\circ\text{C/W}$

SEMITRANS® M IGBT Modules

SKM 200 GAX 173 D ⁶⁾
SKM 200 GAY 173 D ⁶⁾



SEMITRANS 3



GAX GAY

Features

- N channel, Homogeneous Silicon structure (NPT-IGBT)
- Low inductance case
- Very low tail current with low temperature dependence
- High short circuit capability, self limiting to $6 \cdot I_{cnom}$
- Latch-up free
- Fast & soft inverse CAL diodes ⁸⁾
- Isolated copper baseplate using DCB Direct Copper Bonding
- Large clearance (13 mm) and creepage distances (20 mm).

Typical Applications

- Bidirectional switches as "reverse blocking" IGBT
- Regenerative braking
- Quasi resonant inverters
- DC bus voltage 750 - 1200 V_{DC}
- Public transport (auxiliary syst.)
- Switching (not for linear use)

¹⁾ $T_{case} = 25 \text{ }^\circ\text{C}$, unless otherwise specified

²⁾ $I_F = -I_C, V_R = 1200 \text{ V}, di_F/dt = 1000 \text{ A}/\mu\text{s}, V_{GE} = 0 \text{ V}$

⁶⁾ The series diodes have the data of the inverse diodes of SKM 300 GA 173 D

⁸⁾ CAL = Controlled Axial Lifetime Technology.

Cases and mech. data

→ B 6 - 260

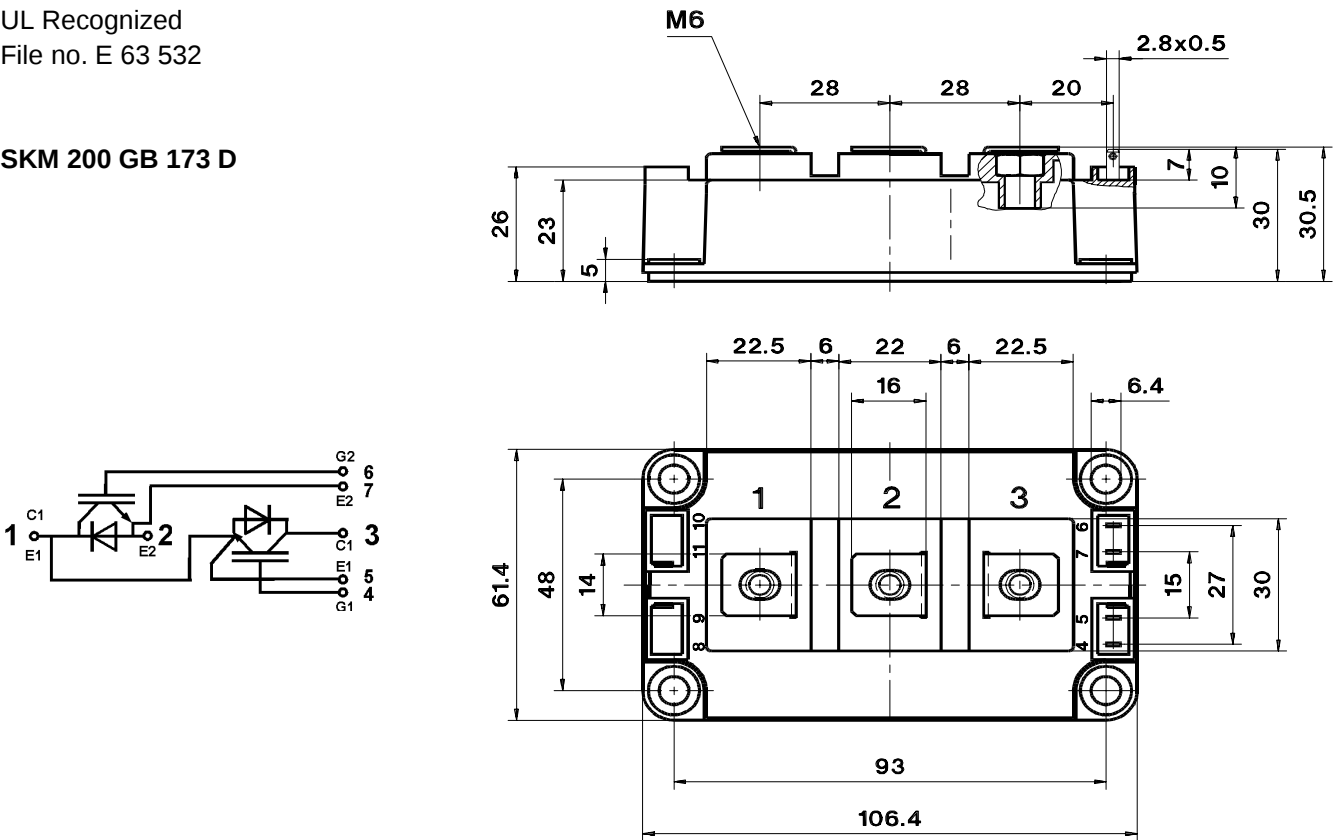
Diagrams of IGBT → B 6 - 254...
of series diode → B 6 - 266
fig. 17, 18, 20 to 24

SEMITRANS 3

Case D 56
UL Recognized
File no. E 63 532

CASED56

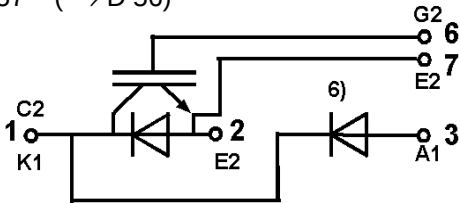
SKM 200 GB 173 D



Dimensions in mm

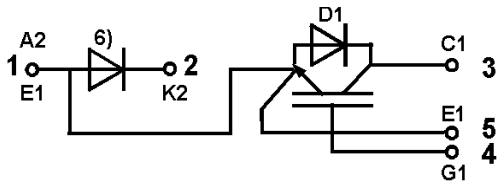
SKM 200 GAX 173 D

Case D 57 (→ D 56)



SKM 200 GAY 173 D

Case D 58 (→ D 56)



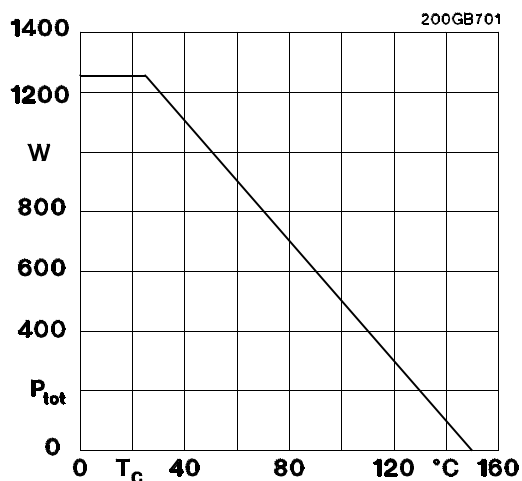
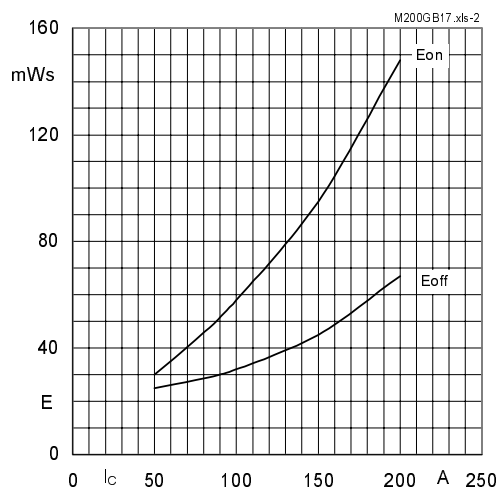
Case outline and circuit diagrams

Mechanical Data			Values			Units
Symbol	Conditions		min.	typ.	max.	
M ₁	to heatsink, SI Units	(M6)	3	—	5	Nm
	to heatsink, US Units		27	—	44	lb.in.
M ₂	for terminals, SI Units	(M5)	2,5	—	5	Nm
	for terminals, US Units		22	—	44	lb.in.
a			—	—	5x9,81	m/s ²
w			—	—	325	g

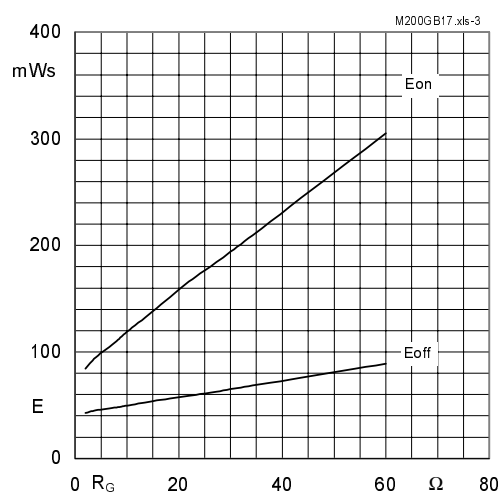
6) Series diode → B 6 – 259, remark 6.

This is an electrostatic discharge sensitive device (ESDS). Please observe the international standard IEC 747-1, Chapter IX.

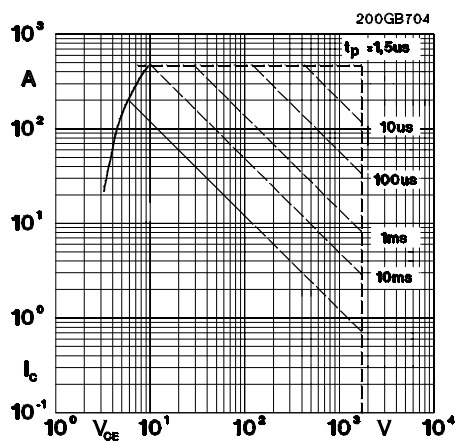
Three devices are supplied in one SEMIBOX A without mounting hardware, which can be ordered separately under Ident No. 33321100 (for 10 SEMITRANS 3). Larger packing units of 12 and 20 pieces are used if suitable
Accessories → B 6 – 4.
SEMIBOX → C – 1.


Fig. 1 Rated power dissipation $P_{tot} = f(T_c)$

Fig. 2 Turn-on /-off energy = $f(I_c)$

$T_j = 125^\circ\text{C}$
 $V_{CE} = 1200\text{ V}$
 $V_{GE} = \pm 15\text{ V}$
 $R_G = 4\ \Omega$


Fig. 3 Turn-on /-off energy = $f(R_G)$

$T_j = 125^\circ\text{C}$
 $V_{CE} = 1200\text{ V}$
 $V_{GE} = \pm 15\text{ V}$
 $I_c = 150\text{ A}$


Fig. 4 Maximum safe operating area (SOA) $I_c = f(V_{CE})$

1 pulse
 $T_c = 25^\circ\text{C}$
 $T_j \leq 150^\circ\text{C}$

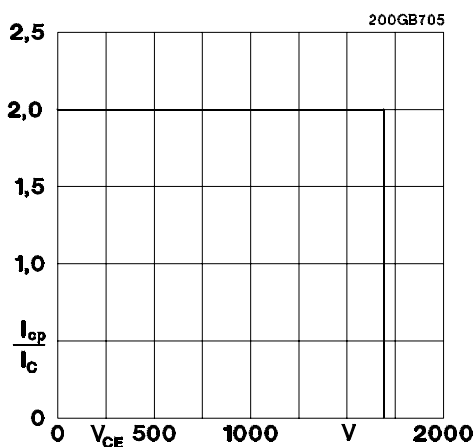
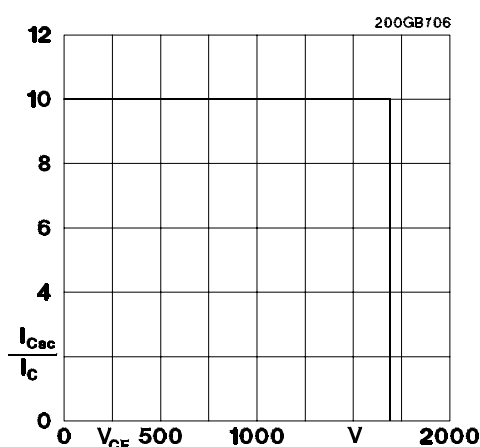
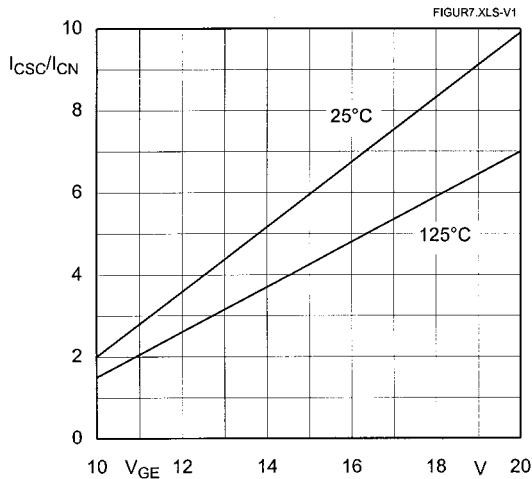


Fig. 5 Turn-off safe operating area (RBSOA)

$T_j \leq 150^\circ\text{C}$
 $V_{GE} = \pm 15\text{ V}$
 $R_{Goff} = 4\ \Omega$
 $I_c = 150\text{ A}$

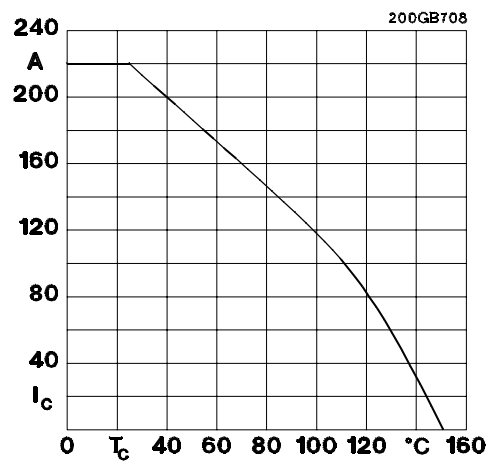

Fig. 6 Safe operating area at short circuit $I_c = f(V_{CE})$

$T_j \leq 150^\circ\text{C}$
 $V_{GE} = \pm 15\text{ V}$
 $t_{sc} \leq 10\ \mu\text{s}$
 $L_{ext} < 50\text{ nH}$
 $I_c = 150\text{ A}$



$V_{CC} = 1200 \text{ V}$
 $I_C = 150 \text{ A}$
 $R_G = 4 \Omega$
 $L_{ext} \leq 50 \text{ nH}$
self-limiting
 $t_p = 10 \mu\text{s}$

Fig. 7 Short circuit current vs. turn-on gate voltage



$T_j = 150 \text{ °C}$
 $V_{GE} \geq 15 \text{ V}$

Fig. 8 Rated current vs. temperature $I_C = f(T_C)$

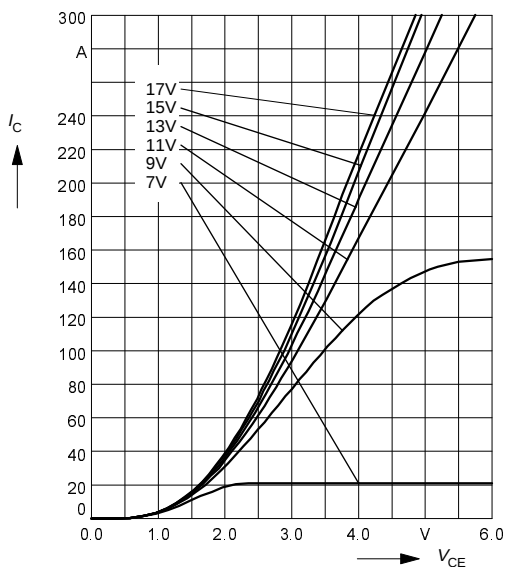


Fig. 9 Typ. output characteristic, $t_p = 80 \mu\text{s}$; $T_j = 25 \text{ °C}$

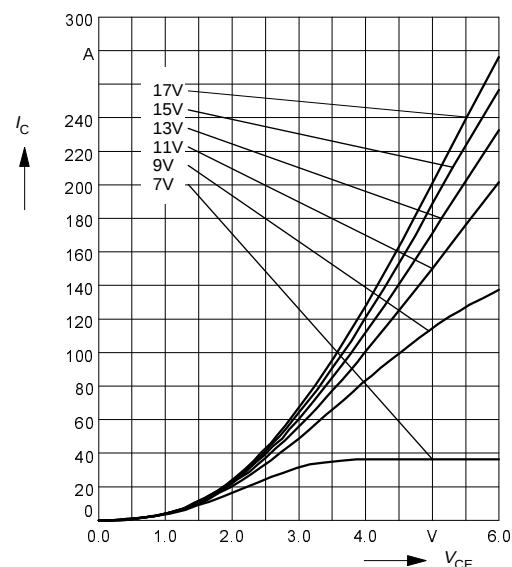


Fig. 10 Typ. output characteristic, $t_p = 80 \mu\text{s}$; $T_j = 125 \text{ °C}$

$$P_{cond}(t) = V_{CEsat}(t) \cdot I_C(t)$$

$$V_{CEsat}(t) = V_{CE(TO)(T_j)} + r_{CE(T_j)} \cdot I_C(t)$$

$$V_{CE(TO)(T_j)} \leq 1,9 + 0,003 (T_j - 25) [\text{V}]$$

$$r_{CE(T_j)} = 0,011 + 0,00004 (T_j - 25) [\Omega]$$

$$\text{valid for } V_{GE} = +15 \frac{+2}{-1} [\text{V}]; I_C \geq 0,3 I_{Cnom}$$

Fig. 11 Typ. saturation characteristic (IGBT)
Calculation elements and equations

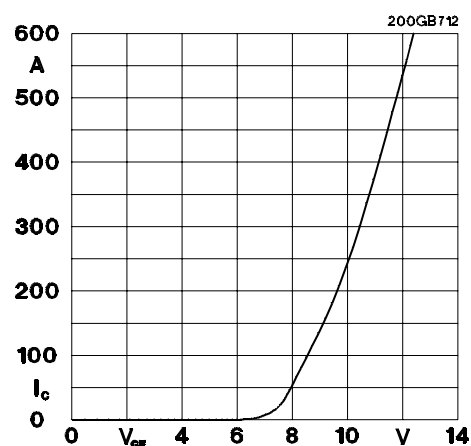


Fig. 12 Typ. transfer characteristic, $t_p = 80 \mu\text{s}$; $V_{CE} = 20 \text{ V}$

$I_{Cpul} = 150 \text{ A}$

$V_{GE} = 0 \text{ V}$
 $f = 1 \text{ MHz}$

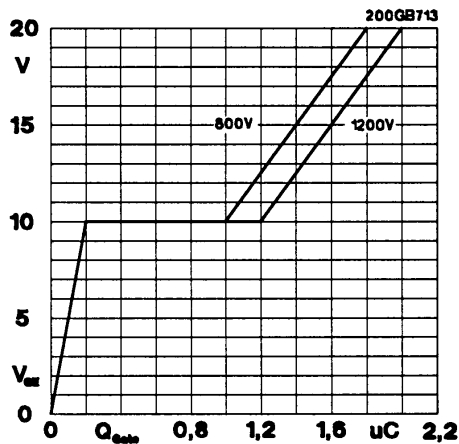


Fig. 13 Typ. gate charge characteristic

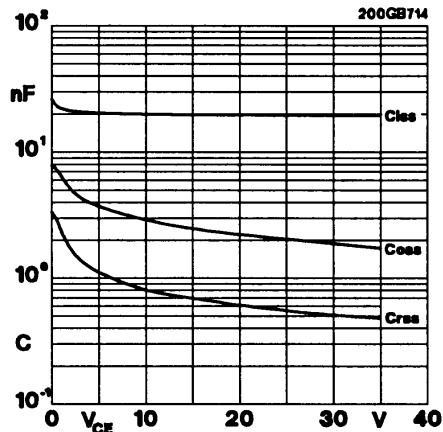


Fig. 14 Typ. capacitances vs. V_{CE}

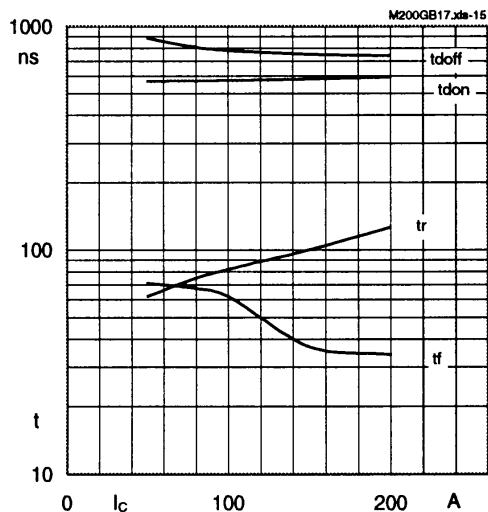


Fig. 15 Typ. switching times vs. I_C

$T_J = 125 \text{ }^\circ\text{C}$
 $V_{CC} = 1200 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_g = 4 \text{ } \Omega$

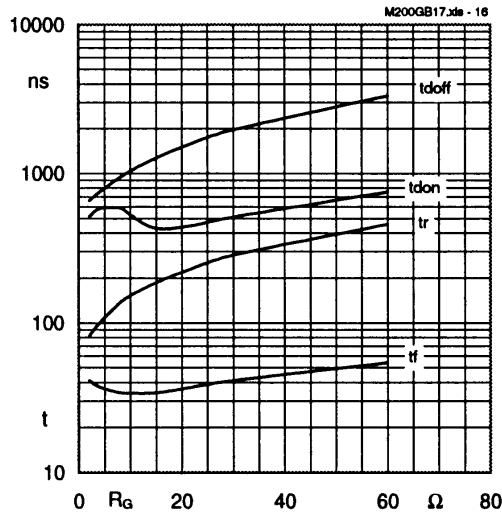


Fig. 16 Typ. switching times vs. R_G

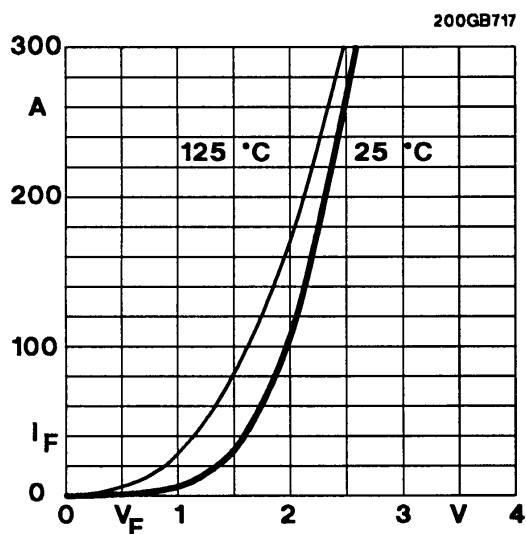


Fig. 17 Typ. CAL diode forward characteristic

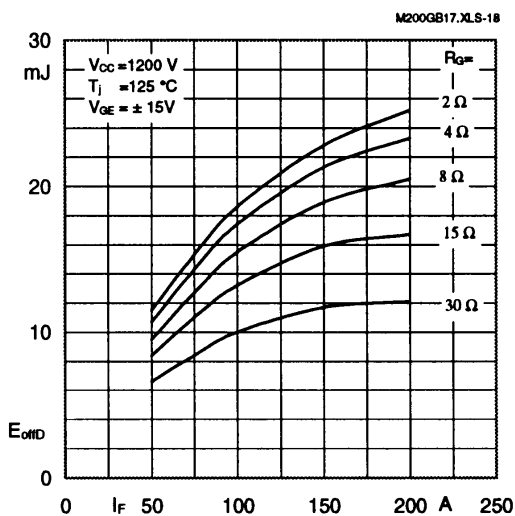


Fig. 18 Typ. Diode turn-off energy dissipation per pulse

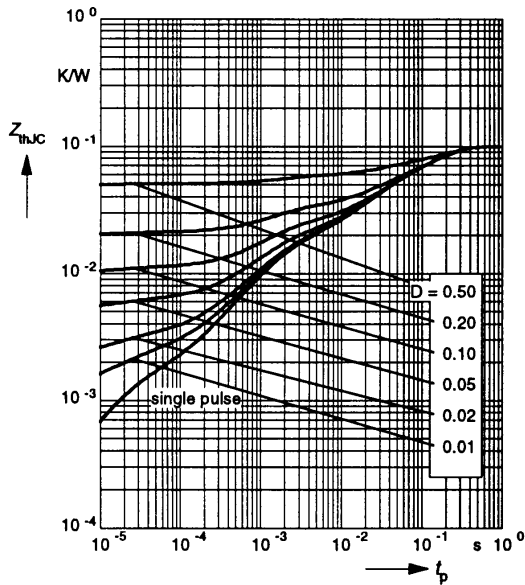


Fig. 19 Transient thermal impedance of IGBT: $Z_{thJC} = f(t_p)$; $D = t_p / t_c = t_p \cdot f$

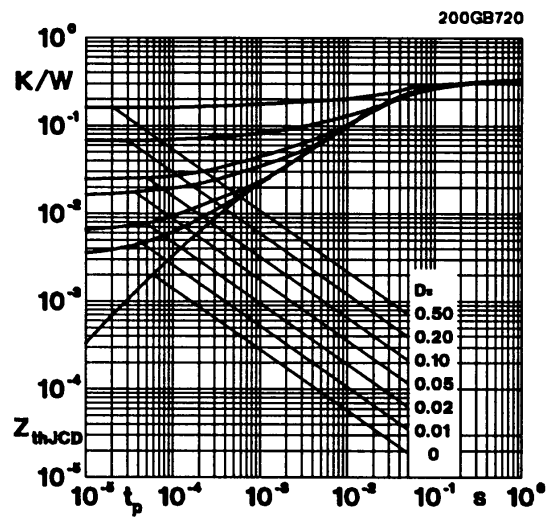


Fig. 20 Transient thermal impedance of inverse diode: $Z_{thJC} = f(t_p)$

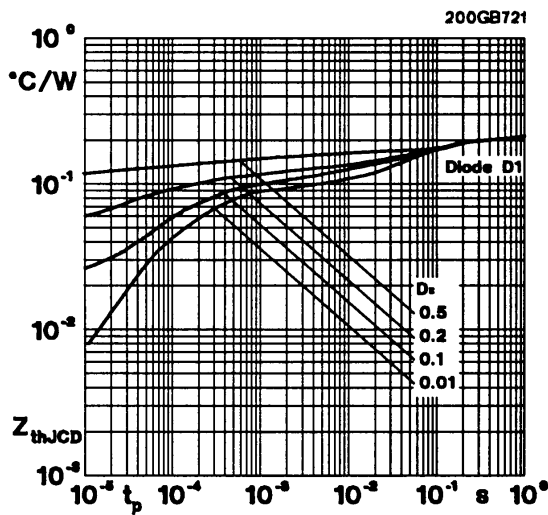


Fig. 21 Transient thermal impedance of FWD of SKM 200GAL173D: $Z_{thJC} = f(t_p)$

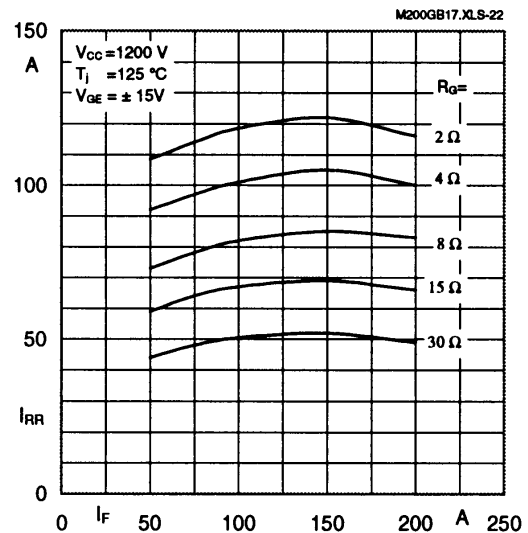


Fig. 22 Typ. CAL diode peak reverse recovery current of inverse diode $I_{RR} = f(I_F; R_G)$

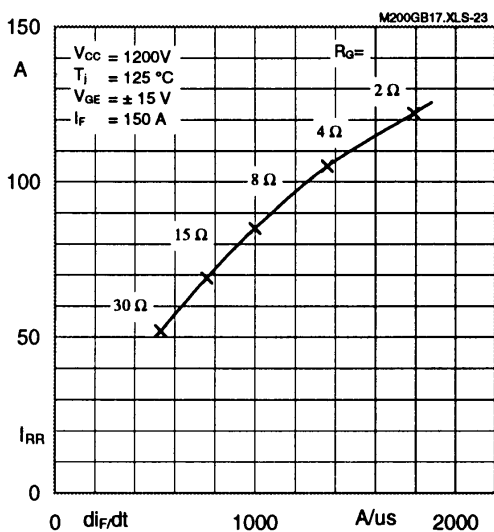


Fig. 23 Typ. CAL diode peak reverse recovery current of inverse diode: $I_{RR} = f(di_F/dt)$

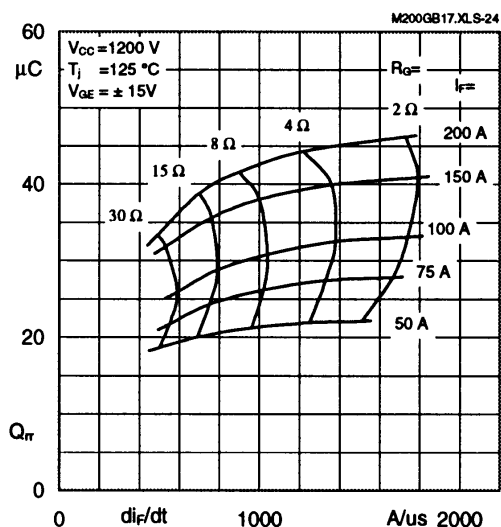


Fig. 24 Typ. CAL diode recovered charge Q_{rr} of inverse diode

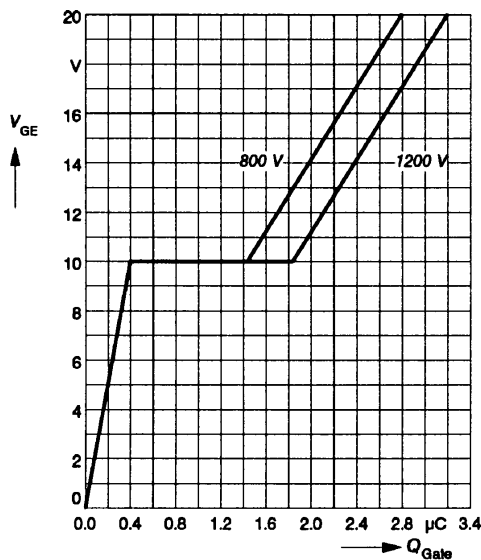


Fig. 13 Typ. gate charge characteristic

$I_{Cpuls} = 200 \text{ A}$

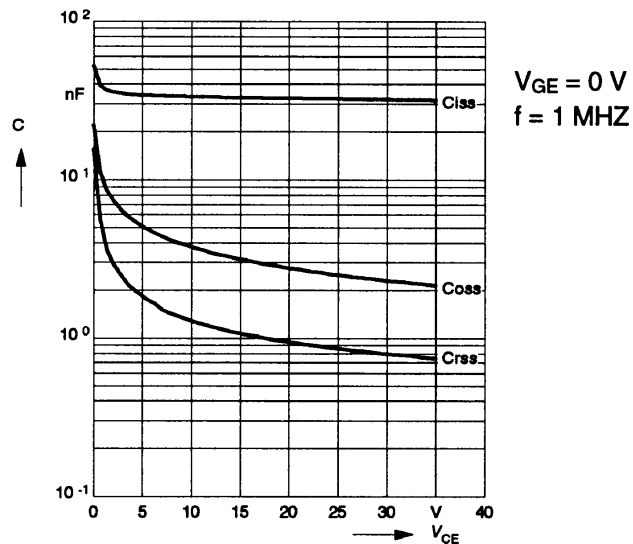


Fig. 14 Typ. capacitances vs. V_{CE}

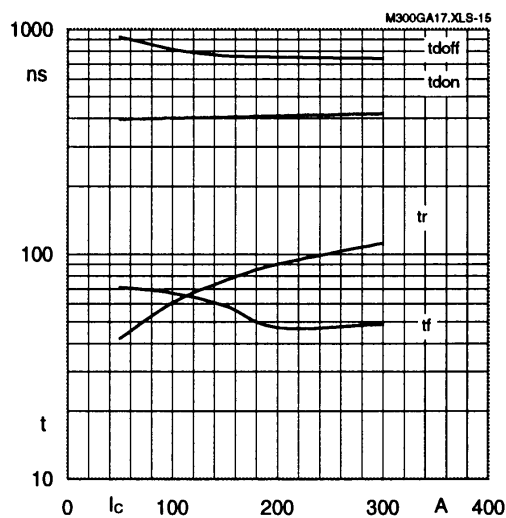


Fig. 15 Typ. switching times vs. I_C

$T_j = 125 \text{ }^{\circ}\text{C}$
 $V_{CC} = 1200 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_g = 3 \text{ } \Omega$

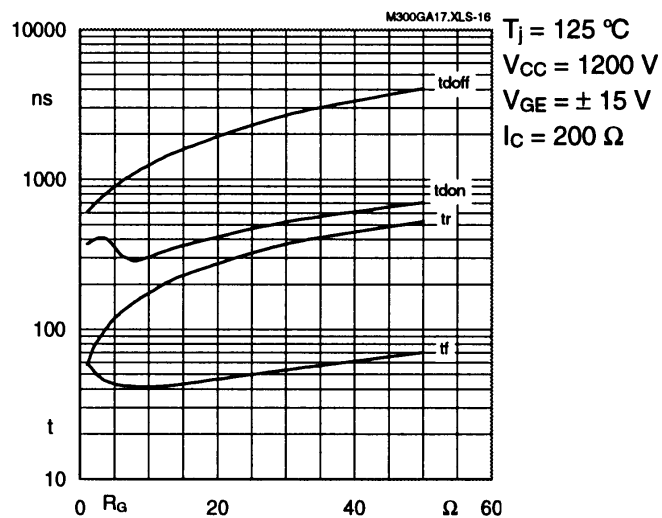


Fig. 16 Typ. switching times vs. R_G

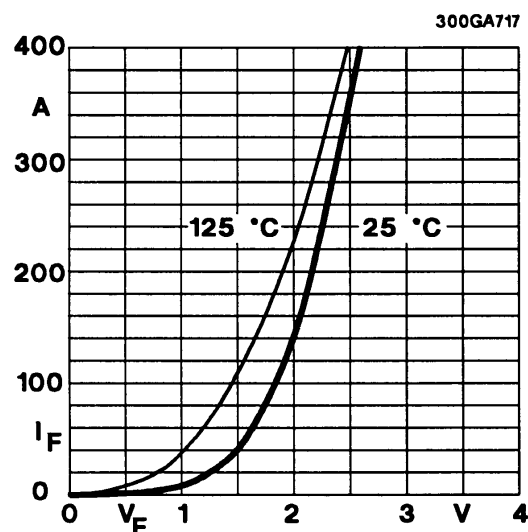


Fig. 17 Typ. CAL diode forward characteristic

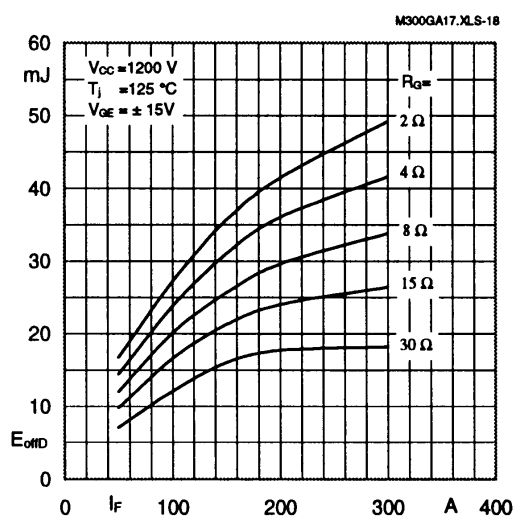


Fig. 18 Typ. Diode turn-off energy dissipation per pulse

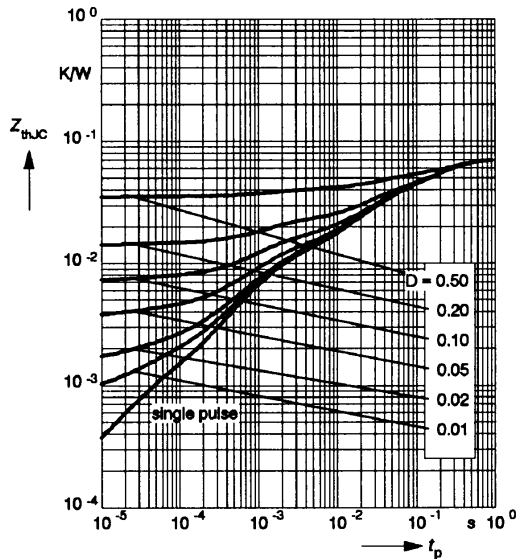


Fig. 19 Transient thermal impedance of IGBT: $Z_{thjc} = f(t_p)$; $D = t_p / t_c = t_p \cdot f$

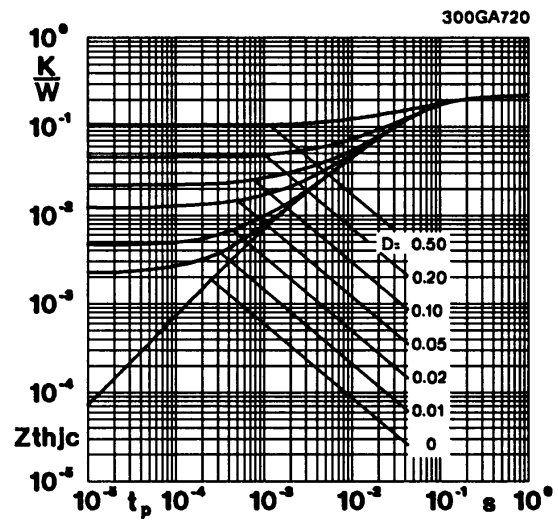


Fig. 20 Transient thermal impedance of inverse diode: $Z_{thjcD} = f(t_p)$

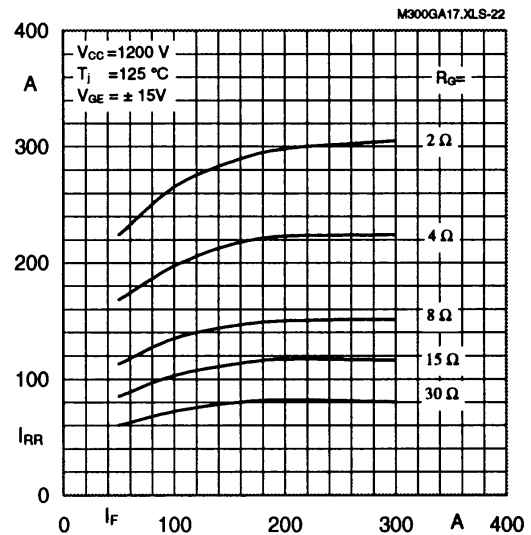


Fig. 22 Typ. CAL diode peak reverse recovery current of inverse diode $I_{RR} = f(I_F; R_G)$

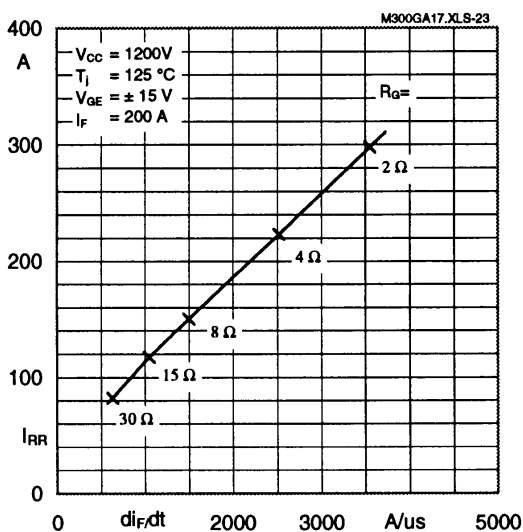


Fig. 23 Typ. CAL diode peak reverse recovery current of inverse diode: $I_{RR} = f(di_F/dt)$

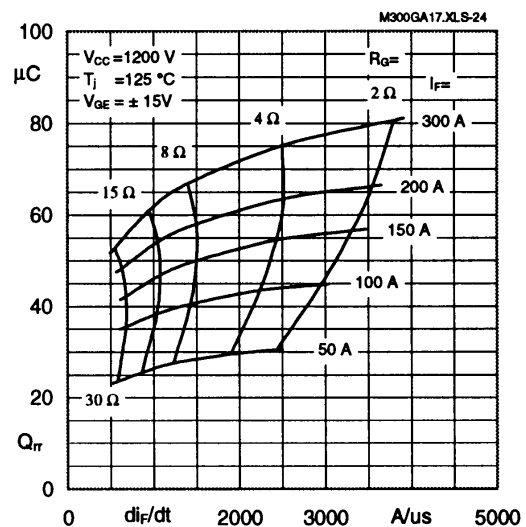


Fig. 24 Typ. CAL diode recovered charge Q_{rr} of inverse diode