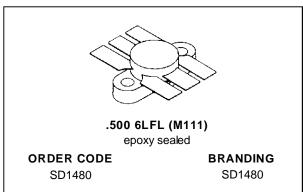


SD1480

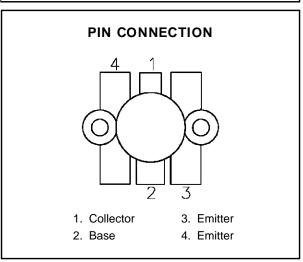
RF & MICROWAVE TRANSISTORS VHF APPLICATIONS

- 136 175 MHz
- 28 VOLTS
- EFFICIENCY 55%
- COMMON EMITTER
- GOLD METALLIZATION
- INTERNAL INPUT MATCHING
- Pout = 125 W MIN. WITH 9.2 dB GAIN



DESCRIPTION

The SD1480 is a common emitter 28 V Class C epitaxial silicon NPN planar transistor designed primarily for VHF communications applications. This internally matched device incorporates diffused emitter ballasting resistors nad provides high gain and stable operation across the entire 136 - 175 MHz VHF communications band.



ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^{\circ}C$)

Symbol	Parameter Value		Unit	
V _{CBO}	Collector-Base Voltage	65	V	
V _{CEO}	Collector-Emitter Voltage	36	V	
V _{CES}	Collector-Emitter Voltage	65	V	
V _{EBO}	Emitter-Base Voltage	4.0	V	
lc	Device Current	20	А	
P _{DISS}	Power Dissipation	270	W	
TJ	Junction Temperature	+200	°C	
T _{STG}	Storage Temperature	- 65 to +150	°C	

THERMAL DATA

R _{TH(j-c)} Junction-Case Thermal Resistance	0.65	°C/W
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ELECTRICAL SPECIFICATIONS (Tcase = 25°C)

STATIC

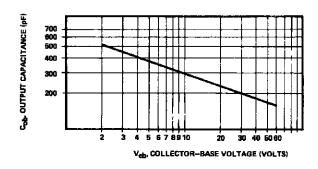
Symbol	Test Conditions	Value			Unit		
		Min.	Тур.	Max.	Oiiit		
ВУсво	I _C = 100 mA	$I_E = 0 \text{ mA}$		65	_	_	V
BVces	I _C = 100 mA	V _{BE} = 0 V		65	_	_	V
BV _{CEO}	I _C = 100 mA	$I_B = 0 \text{ mA}$		35	_	_	V
BV _{EBO}	I _E = 10 mA	$I_C = 0 \text{ mA}$		4.0	_	_	V
ICES	V _{CE} = 30 V	I _E = 0 mA		_	_	15	mA
h _{FE}	Vce = 5 V	Ic = 5 A		20	_	200	_

DYNAMIC

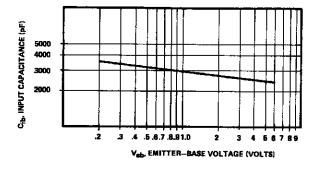
Symbol	Test Conditions		Value		Unit		
Symbol	rest Conditions			Min.	Тур.	Max.	Oiiit
Роит	f = 150 MHz	$P_{IN} = 15 W$	$V_{CE} = 28 V$	125		_	W
Pg	f = 150 MHz	Pout = 125 W	$V_{CE} = 28 V$	9.2	_	_	dB
ης	f = 150 MHz	P _{OUT} = 125 W	$V_{CE} = 28 \text{ V}$	55	_	_	%
СОВ	f = 1 MHz	$V_{CB} = 28 \text{ V}$		_	_	250	pF
Load Mismatch	f = 150 MHz	P _{IN} = 15 W	V _{CE} = 28 V	20:1	_	_	VSWR

TYPICAL PERFORMANCE

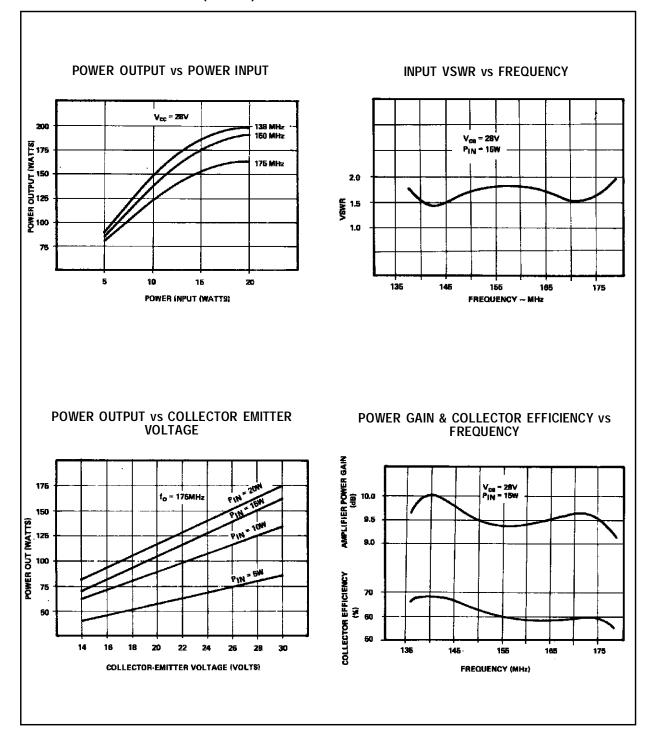
OUTPUT CAPACITANCE vs COLLECTOR BASE VOLTAGE



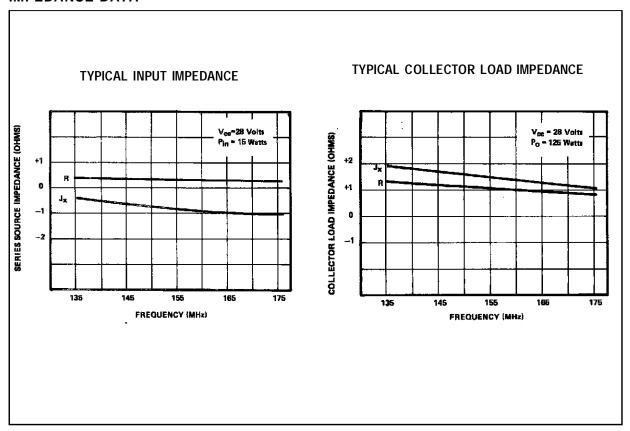
INPUT CAPACITANCE vs EMITTER BASE VOLTAGE



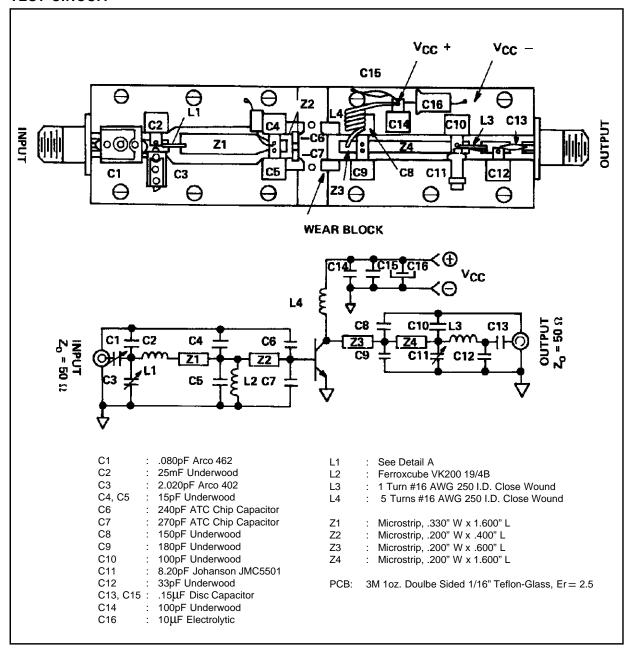
TYPICAL PERFORMANCE (cont'd)



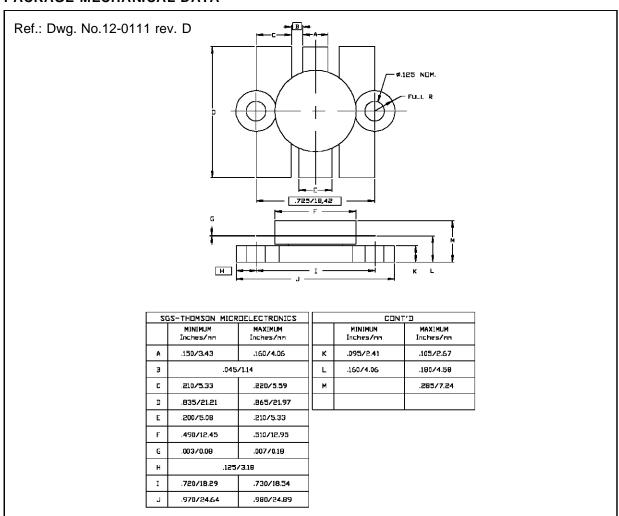
IMPEDANCE DATA



TEST CIRCUIT



PACKAGE MECHANICAL DATA



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