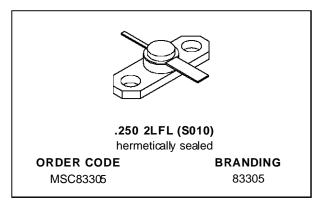


### MSC83305

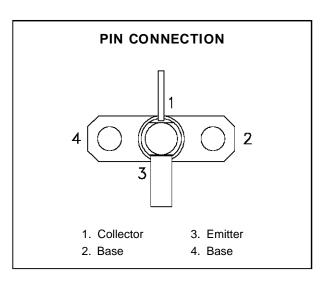
# RF & MICROWAVE TRANSISTORS GENERAL PURPOSE AMPLIFIER APPLICATIONS

- REFRACTORY/GOLD METALLIZATION
- EMITTER BALLASTED
- VSWR CAPABILITY ∞:1 @ RATED CONDITIONS
- HERMETIC STRIPAC® PACKAGE
- P<sub>OUT</sub> = 4.5 W MIN. WITH 4.5 dB GAIN @ 3.0 GHz



#### **DESCRIPTION**

The MSC83305 is a common base hermetically sealed silicon NPN microwave power transistor utilizing an emitter site ballasted geometry with a refractory gold metallization system. This device is capable of withstanding an infinite load VSWR at any phase angle under rated conditions. The MSC83305 was designed for Class C amplifier/oscillator applications in the 1.0 - 3.0 GHz frequency range.



#### **ABSOLUTE MAXIMUM RATINGS** $(T_{case} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit
Poiss	Power Dissipation* (T <sub>C</sub> ≤ 50°C)	17.6	W
Ic	Device Current*	700	mA
Vcc	Collector-Supply Voltage*	30	V
TJ	Junction Temperature	200	°C
T <sub>STG</sub>	Storage Temperature	- 65 to +200	°C

#### THERMAL DATA

_				
	R <sub>TH(j-c)</sub>	Junction-Case Thermal Resistance*	8.5	°C/W

 $<sup>^*</sup>$ Applies only to rated RF amplifier operation

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### **ELECTRICAL SPECIFICATIONS** (T<sub>case</sub> = 25°C)

#### **STATIC**

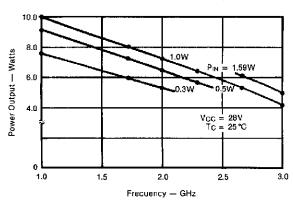
Symbol	Test Conditions	Value			IIm:4		
		rest Conditions		Min.	Тур.	Max.	Unit
BV <sub>CBO</sub>	$I_C = 1mA$	$I_E = 0mA$		45			V
BV <sub>EBO</sub>	I <sub>E</sub> = 1mA	I <sub>C</sub> = 0mA		3.5		_	V
BVcer	IC = 5mA	$R_{BE} = 10\Omega$		45			V
Ісво	$V_{CB} = 28V$			_		0.5	mA
h <sub>FE</sub>	$V_{CE} = 5V$	I <sub>C</sub> = 500mA		30	_	300	_

#### **DYNAMIC**

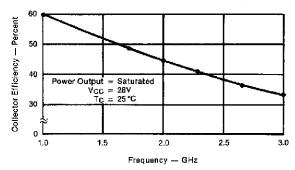
Sumb al		Test Conditions		Value		IImi4	
Symbol		Test Conditions			Тур.	Max.	Unit
Роит	f = 3.0 GHz	$P_{IN} = 1.59 W$	$V_{CC} = 28 \text{ V}$	4.5	5.0	_	W
ης	f = 3.0 GHz	$P_{IN}=1.59\;W$	$V_{CC} = 28 \text{ V}$	30	33	_	%
G <sub>P</sub>	f = 3.0 GHz	$P_{IN} = 1.59 \text{ W}$	$V_{CC} = 28 \text{ V}$	4.5	5.0	_	dB
Сов	f = 1 MHz	V <sub>CB</sub> = 28 V		_	_	7.5	pF

#### TYPICAL PERFORMANCE

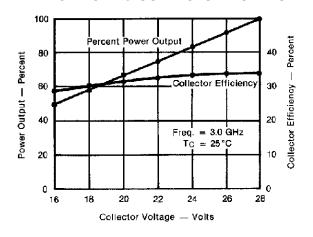
#### **POWER OUTPUT vs FREQUENCY**



#### **COLLECTOR EFFICIENCY vs FREQUENCY**

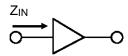


### PERCENT POWER OUTPUT & COLLECTOR EFFICIENCY vs COLLECTOR VOLTAGE

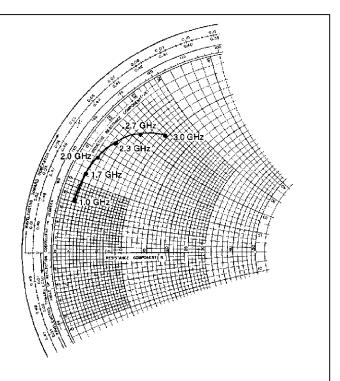


#### **IMPEDANCE DATA**

## TYPICAL INPUT IMPEDANCE

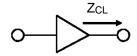


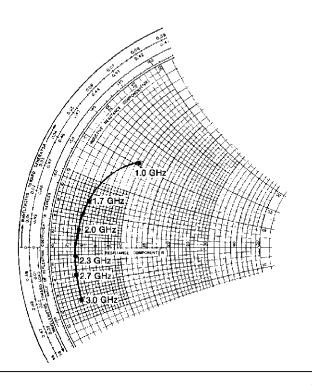
FREQ.	Z <sub>IN</sub> (Ω)	Z <sub>CL</sub> (Ω)
1.0 GHz	1.7 + j 7.2	9.5 + j 15.5
1.7 GHz	2.0 + j 11.2	4.2 + j 6.7
2.0 GHz	2.4 + j 14.0	3.5 + j 2.5
2.3 GHz	3.6 + j 17.4	3.1 – j 1.2
2.7 GHz	6.0 + j 21.0	3.0 – j 3.8
3.0 GHz	95 + i240	30-i72



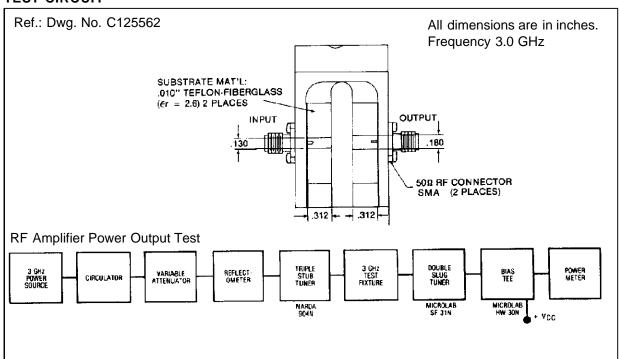
 $P_{OUT} = Saturated$   $V_{CC} = 28V$ Normalized to 50 ohms

# TYPICAL COLLECTOR LOAD IMPEDANCE

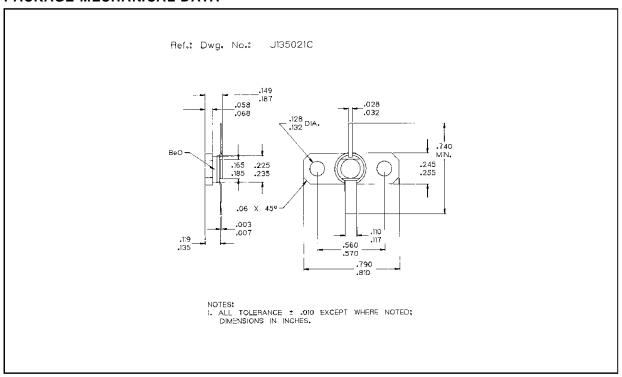




#### **TEST CIRCUIT**



#### PACKAGE MECHANICAL DATA



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