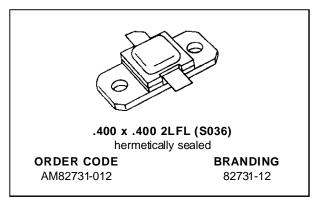


AM82731-012

RF & MICROWAVE TRANSISTORS S-BAND RADAR APPLICATIONS

PRELIMINARY DATA

- REFRACTORY/GOLD METALLIZATION
- EMITTER SITE BALLASTED
- LOW THERMAL RESISTANCE
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- P_{OUT} = 12 W MIN. WITH 6.0 dB GAIN

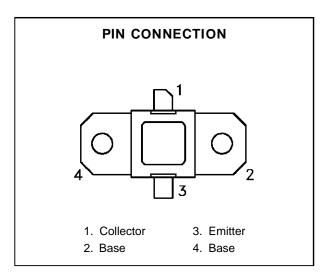


DESCRIPTION

The AM82731-012 device is a high power silicon bipolar NPN transistor specifically designed for S-Band radar pulsed output and driver applications.

This device is capable of operaion over a wide range of pulse widths, duty cycles, and temperatures and can withstand a 3:1 output VSWR with a + 1 dB input overdrive. Low RF thermal resistance, refractory/gold metallization, and automatic wire bonding techniques ensure high reliability and product consistency (including phase characteristics).

The AM82731-012 is supplied in the Hermetic Metal/Ceramic package with internal Input/Output impedance matching sircuitry, and is intended for military and other high reliability applications.



ABSOLUTE MAXIMUM RATINGS $(T_{case} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit	
P _{DISS}	Power Dissipation* (T _C ≤ 50°C)	50	W	
Ic	Device Current*	2.0	А	
Vcc	Collector-Supply Voltage*	46	V	
TJ	Junction Temperature (Pulsed RF Operation)	250	°C	
T _{STG}	Storage Temperature	- 65 to +200	°C	

THERMAL DATA

R _{TH(j-c)}	Junction-Case Thermal Resistance*	4.0	°C/W

^{*}Applies only to rated RF amplifier operation

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ELECTRICAL SPECIFICATIONS (Tcase = 25°C)

STATIC

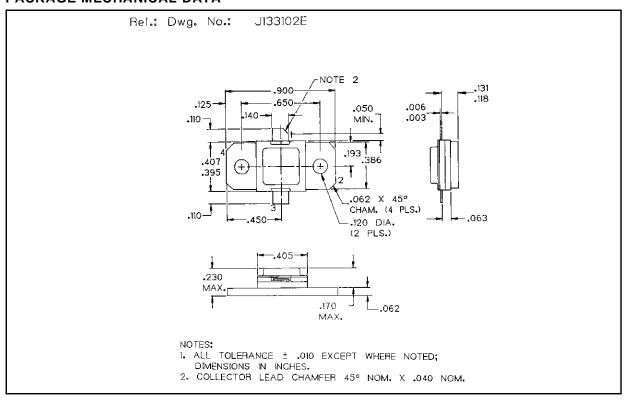
			Value			
Symbol		Test Conditions	Min.	Тур.	Max.	Unit
ВУсво	I _C = 7mA	$I_E = 0mA$	55	_		V
BV _{EBO}	I _E = 1mA	$I_C = 0mA$	3.5	_		V
BV _{CER}	IC = 7mA	$R_{BE} = 10\Omega$	55	_	_	V
Ices	V _{CE} = 40V			_	5	mA
h _{FE}	Vce = V	$I_C = 600 mA$	30	_	300	_

DYNAMIC

				Value			
Symbol	Test Conditions			Min.	Тур.	Max.	Unit
Pout	f = 2700 —3100 MHz	$P_{IN}=3.0W$	$V_{CC} = 40V$	12	_	_	W
ης	f = 2700 —3100 MHz	$P_{IN}=3.0W$	$V_{CC} = 40V$	30	_	_	%
G _P	f = 2700 —3100 MHz	$P_{IN} = 3.0W$	$V_{CC} = 40V$	6.0	_	_	dB

Note: Pulse Width = $100 \mu S$ Duty Cycle = 10%

PACKAGE MECHANICAL DATA



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