

START405 NPN Silicon RF Transistor

TARGET DATA

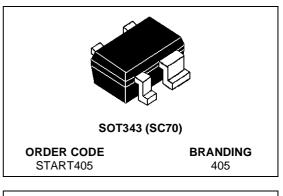
LOW NOISE FIGURE: NFmin = 1.15dB @ 1.8GHz, 2mA, 2V

- COMPRESSION P1dB = 5dBm
 @ 1.8GHz, 5mA, 2V
- TRANSITION FREQUENCY 42GHz
- LOW CURRENT CONSUMPTION
- ULTRA MINIATURE SOT343 PACKAGE

DESCRIPTION

The START405 is a member of the START family that provide the state of the art of RF silicon process to the market. Manufacturated in the third generation of ST proprietary bipolar process, it offers the best mix of gain and NF for given breakdown voltage(BVceo).

It offers performance level only archived with GaAs products before.



APPLICATIONS

- LNA FOR GSM/DCS, DECT, PCS, PCN, CDMA, W-CDMA
- GENERAL PURPOSE 500MHz-5GHz

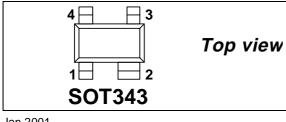
Symbol	Parameter	Value	Unit V	
V _{ceo}	Collector emitter voltage	4.5		
V _{cbo}	Collector base voltage	15	V	
V _{ebo}	Emitter base voltage	1.5	V	
I _c	Collector current	10	mA mA	
۱ _b	Base current	1		
Ptot	Total dissipation, $T_s = TBD$		mW	
T _{stg}	Storage temperature	-65 to 150	°C	
Tj	Max. operating junction temperature	150	°C	

ABSOLUTE MAXIMUM RATINGS

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R _{thjs}	Thermal Resistance Junction soldering point	MAX	≤ TBD	°C/W	
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PINOUT



PIN CONNECTION

Pin No.	Description
1	BASE
3	COLLECTOR
2,4	EMITTER

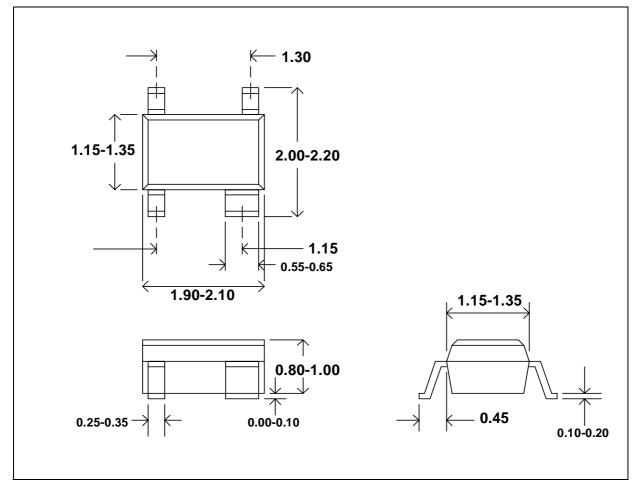
Jan 2001

ELECTRICAL CHARACTERISTICS (T _i =25 °C, unless otherwise	rwise specified)
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Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I _{cbo}	Collector cutoff current	Vcb = 3V, Ie = 0A			150	nA
I _{ebo}	Emitter-base cutoff current	Veb = 1.5V, Ic = 0A			15	μΑ
Hfe	DC current gain	Ic = 5mA, Vce = 4V	50	90		
NFmin	Minimim noise figure	lc = 2mA, Vce = 2V, f = 1.8GHz		1.15		dB
Ga	NFmin associated gain	Ic = 2mA, Vce = 2V, f = 1.8GHz		19		dB
S21 ²	Insertion power gain	Ic = 5mA, Vce = 2V, f = 1.8GHz		17		dB
Gms ⁽¹⁾	Maximum stable gain	Ic = 5mA, Vce = 2V, f = 1.8GHz		22		dB
P-1dB	1dB compression point	Ic = 5mA,Vce = 2V, f = 1.8GHz		5		dBm
OIP3	Ouput third order intercept point	lc = 5mA,Vce = 2V, f = 1.8GHz		15		dBm

Note(1): Gms = $|S_{21} / S_{12}|$

PACKAGE DIMENSIONS SOT343 (SC-70 4 leads)



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