QRFL0001
QUALIFICATION REPORT

M29F080A T6X-U35:
8 Mbit (x8) Single Supply Flash Memory

INTRODUCTION

The M29F080A is an 8 Mbit Single Supply (5V) Flash memory with Uniform Block partitioning and organized as 1 MByte of 8 bits each. It can be programmed and erased in-system or in standard EPROM programmers.

The M29F080A is manufactured with the STMicroelectronics advanced CMOS 0.35 micron T6X-U35 process, especially developed for Flash memory products. The memory features a fast access time, low power consumption in all operations (Standby, Read, Erase and Program) and an endurance of 100,000 Program/Erase cycles per block.

Qualification Report History

– December 1999: Catania M5 Diffusion Line, TSOP40 (10 x 20mm) package
– December 1999: Catania M5 Diffusion Line, SO44 package

ST recognises that the quality of a product must be built-in during the design, material procurement, manufacturing and testing. Also that the reliability must be demonstrated before the product is released to full mass production. The qualification of new products and the certification of new processes is a rigorous task undertaken by Quality and Reliability professionals, to ensure stable products and processes capable of fully meeting customer requirements.

A key step of this activity is the Design Review where we assure that,

– adequate and realistic product specifications have been developed;
– design and layout rules, as documented in the Design Rules Manual, have been respected;
– critical performance parameters and process variables have been identified;
– previously untested design techniques or manufacturing processes are recognised;
– manufacturability concerns are identified;
– comprehensive and efficient qualification programs are defined.

Product Qualification is made on all new products and on new packages. Qualification is also remade on existing products when there are major changes to the design or manufacturing. The tests performed are tailored to the parameters affected by the major change or the combinations of new die or new package to be evaluated.

The results of the tests for this Flash memory are on the attached pages of this qualification report.

Director of
Memory Products Group
Quality Control & Reliability

Attilio PANCHIERI

Table 1. Revision History

Date	Revision Details
February 2000	First Issue
06/16/00	TSOP package related tests conditions change to be compliant to JEDEC 020A SO package related tests conditions change to be compliant to JEDEC 020A

Table 2. TSOP40 (10 x 20mm) Plastic Package Related Tests

Sub-group	Test Procedure	MIL-STD-883 Procedure	Test Conditions	Result			Note
				Lots	Samp.	Fail	
1	Phisical Dimensions Coplanarity TSOP40 Package	2016	Published Data Published Data				2 2
2	Bond Strenght	2011		1	10	0	
3	Die Attach Strength	2019 or 2027		1	5	0	
4	Radiography	2012					2
5	Internal Visual and Mechanical	2014		1	5	0	
6	Solderability TSOP40 Package	2003	215°C, 3 sec, Precondition, 8 hrs, Steam aging				2
7	Resistance to Solvents	2015	4 Solvent Solutions				2
8	Solder Coating Thickness and Compositions	(Note 1)	5 µm min Sn/Pb 85/15				2
9	Resistance to Surface Mounting TSOP40 Package	JEDEC 020A	MSL 3	1	15	0	

Note: 1. According to ST specification.
2. Results for similarity, from standard production monitor.

Table 3. TSOP40 (10 x 20mm) Plastic Package - Die Related Tests

Sub-group	Test Procedure	MIL-STD-883 Procedure	Test Conditions	Results			Note
				Lots	Samp.	Fail	
1	Operating Life Test	1005	140°C, V_{CC} = 6V, – 168 hrs – 500 hrs – 1000 hrs	1	 76 76 76	 0 0 0	2
2	Low Temperature Operating Life Test	1005	–40°C, V_{CC} = 6V, – 168 hrs – 500 hrs – 1000 hrs	1	 15 15 15	 0 0 0	2
3	Retention Bake	1008	150°C – 168 hrs – 500 hrs – 1000 hrs	1	 60 60 60	 0 0 0	3
4	Retention Bake	1008	250°C – 168 hrs – 500 hrs – 1000 hrs	3	 180 180 180	 0 0 0	3
			monitoring 250°C – 168 hrs – 500 hrs – 1000 hrs	1	 60 60 60	 0 0 0	2
5	Write/Erase Cycling		100,000 cycles	4	257	0	3
			10,000 cycles 20,000 cycles	1	55 55	0 0	2
6	Retention Bake (after 100k cycles)		250°C – 168 hrs – 500 hrs – 1000 hrs	3	 135 135 135	 0 0 0	3
7	Temperature, Humidity, Bias	CECC 90,000	85°C, RH = 85%, V_{CC} = 3.6V, – 168 hrs – 500 hrs – 1000 hrs – 2000 hrs	1	 40 40 40 40	 0 0 0 0	3
8	Temperature Cycling	1010C	–65 to 150°C, – 100 cycles – 500 cycles – 1000 cycles	1	 60 60 60	 0 0 0	1
9	Pressure Pot		121°C, 2 Atm, RH = 100%, – 96 hrs – 168 hrs	1	 60 60	 0 0	1
10	HAST	CECC 90,000	130°C, RH = 85%, V_{CC} = 3.6V, – 96 hrs – 168 hrs	1	 15 15	 0 0	3

Note: 1. Samples previously submitted to preconditioning flow for Surface Mounting devices according to ST specification.
2. Test performed on CDIP48 package.
3. Data applies by similarity from the M29W008A. Results come from the relevant Qualification Report.

Table 4. SO44 Plastic Package Related Tests

Sub-group	Test Procedure	MIL-STD-883 Procedure	Test Conditions	Result			Note
				Lots	Samp.	Fail	
1	Phisical Dimensions Coplanarity SO44 Package	2016	Published Data Published Data				2 2
2	Bond Strenght	2011					2
3	Die Attach Strength	2019 or 2027		1	5	0	
4	Radiography	2012					2
5	Internal Visual and Mechanical	2014		1	5	0	
6	Solderability SO44 Package	2003	215°C, 3 sec, Precondition, 8 hrs, Steam aging				2
7	Resistance to Solvents	2015	4 Solvent Solutions				2
8	Solder Coating Thickness and Compositions	(Note 1)	5 µm min Sn/Pb 85/15				2
9	Resistance to Surface Mounting SO44 Package	JEDEC 020A	MSL 3	1	15	0	

Note: 1. According to ST specification.
2. Results for similarity, from standard production monitor.

Table 5. SO44 Plastic Package - Die Related Tests

Sub-group	Test Procedure	MIL-STD-883 Procedure	Test Conditions	Results			Note
				Lots	Samp.	Fail	
1	Operating Life Test	1005	140°C, V_{CC} = 6V, – 168 hrs – 500 hrs – 1000 hrs	1	 76 76 76	 0 0 0	2
2	Low Temperature Operating Life Test	1005	–40°C, V_{CC} = 6V, – 168 hrs – 500 hrs – 1000 hrs	1	 15 15 15	 0 0 0	2
3	Retention Bake	1008	150°C – 168 hrs – 500 hrs – 1000 hrs	1	 60 60 60	 0 0 0	3
4	Retention Bake	1008	250°C – 168 hrs – 500 hrs – 1000 hrs	3	 180 180 180	 0 0 0	3
			monitoring 250°C – 168 hrs – 500 hrs – 1000 hrs	1	 60 60 60	 0 0 0	2
5	Write/Erase Cycling		100,000 cycles	4	257	0	3
			10,000 cycles 50,000 cycles	1	55 55	0 0	2
6	Retention Bake (after 100k cycles)		250°C – 168 hrs – 500 hrs – 1000 hrs	3	 135 135 135	 0 0 0	3
7	Temperature, Humidity, Bias	CECC 90,000	85°C, RH = 85%, V_{CC} = 3.6V, – 168 hrs – 500 hrs – 1000 hrs – 2000 hrs	1	 40 40 40 40	 0 0 0 0	3
8	Temperature Cycling	1010C	–65 to 150°C, – 100 cycles – 500 cycles – 1000 cycles	1	 60 60 60	 0 0 0	1
9	Pressure Pot		121°C, 2 Atm, RH = 100%, – 96 hrs – 168 hrs	1	 60 60	 0 0	1
10	HAST	CECC 90,000	130°C, RH = 85%, V_{CC} = 3.6V, – 96 hrs – 168 hrs	1	 15 15	 0 0	3

Note: 1. Samples previously submitted to preconditioning flow for Surface Mounting devices according to the Package Related tests (previous page).
2. Test performed on CDIP48 package.
3. Data applies by similarity from the M29W800A. Results come from the relevant Qualification Report.

If you have any questions or suggestion concerning the matters raised in this document please send them to the following electronic mail address:

ask.memory@st.com (for general enquiries)

Please remember to include your name, company, location, telephone number and fax number

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