



# STGB20NB32LZ

## N-CHANNEL CLAMPED 20A D<sup>2</sup>PAK INTERNALLY CLAMPED PowerMESH™ IGBT

PRELIMINARY DATA

TYPE	V <sub>CES</sub>	V <sub>CE(sat)</sub>	I <sub>C</sub>
STGB20NB32LZ	CLAMPED	< 2.0 V	20 A

- POLYSILICON GATE VOLTAGE DRIVEN
- LOW THRESHOLD VOLTAGE
- LOW ON-VOLTAGE DROP
- HIGH CURRENT CAPABILITY
- HIGH VOLTAGE CLAMPING FEATURE
- SURFACE-MOUNTING D<sup>2</sup>PAK (TO-263)  
POWER PACKAGE IN TUBE (NO SUFFIX)  
OR IN TAPE & REEL (SUFFIX "T4")

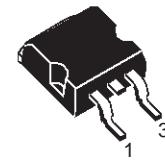
### DESCRIPTION

Using the latest high voltage technology based on patented strip layout, STMicroelectronics has designed an advanced family of IGBTs with outstanding performances.

The built in collector-gate zener exhibits a very precise active clamping while the gate-emitter zener supplies an ESD protection.

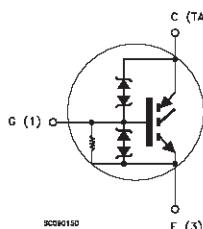
### APPLICATIONS

- AUTOMOTIVE IGNITION



D<sup>2</sup>PAK  
TO-263

### INTERNAL SCHEMATIC DIAGRAM



### ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V <sub>CES</sub>	Collector-Emitter Voltage (V <sub>GS</sub> = 0)	CLAMPED	V
V <sub>ECR</sub>	Reverse Battery Protection	20	V
V <sub>GE</sub>	Gate-Emitter Voltage	CLAMPED	V
I <sub>C</sub>	Collector Current (continuous) at T <sub>c</sub> = 25 °C	40	A
I <sub>C</sub>	Collector Current (continuous) at T <sub>c</sub> = 100 °C	30	A
I <sub>CM(•)</sub>	Collector Current (pulsed)	80	A
E <sub>AS</sub>	Single Pulse Energy T <sub>c</sub> = 25 °C	700	mJ
P <sub>tot</sub>	Total Dissipation at T <sub>c</sub> = 25 °C	150	W
	Derating Factor	1	W/°C
E <sub>SD</sub>	ESD (Human Body Model)	4	kV
T <sub>stg</sub>	Storage Temperature	-65 to 175	°C
T <sub>j</sub>	Max. Operating Junction Temperature	175	°C

(•) Pulse width limited by safe operating area

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### THERMAL DATA

R <sub>thj-case</sub>	Thermal Resistance Junction-case	Max	1	°C/W
R <sub>thj-amb</sub>	Thermal Resistance Junction-ambient	Max	62.5	°C/W
R <sub>thc-sink</sub>	Thermal Resistance Case-sink	Typ	0.2	°C/W

### ELECTRICAL CHARACTERISTICS ( $T_j = 25^\circ\text{C}$ unless otherwise specified)

OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
BV <sub>(CES)</sub>	Clamped Voltage	I <sub>C</sub> = 2mA V <sub>GE</sub> = 0 T <sub>C</sub> = - 40°C	330	355	380	V
		I <sub>C</sub> = 2mA V <sub>GE</sub> = 0 T <sub>C</sub> = 25°C	325	350	375	V
		I <sub>C</sub> = 2mA V <sub>GE</sub> = 0 T <sub>C</sub> = 150°C	320	345	370	V
BV <sub>(ECR)</sub>	Emitter Collector Break-down Voltage	I <sub>C</sub> = 75 mA T <sub>C</sub> = 25°C	20	28		V
BV <sub>GE</sub>	Gate Emitter Break-down Voltage	I <sub>G</sub> = ± 2 mA	12	14	16	V
I <sub>CES</sub>	Collector cut-off Current (V <sub>GE</sub> = 0)	V <sub>CE</sub> = 15 V V <sub>GE</sub> = 0 T <sub>C</sub> = 150 °C V <sub>CE</sub> = 200 V V <sub>GE</sub> = 0 T <sub>C</sub> = 150 °C			10 100	μA μA
I <sub>GES</sub>	Gate-Emitter Leakage Current (V <sub>CE</sub> = 0)	V <sub>GE</sub> = ± 10 V V <sub>CE</sub> = 0	± 300	± 660	± 1000	μA
R <sub>GE</sub>	Gate Emitter Resistance		10	15	30	KΩ

ON (\*)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V <sub>GE(th)</sub>	Gate Threshold Voltage	V <sub>CE</sub> = V <sub>GE</sub> I <sub>C</sub> = 250μA T <sub>C</sub> = - 40°C	1.2			V
		V <sub>CE</sub> = V <sub>GE</sub> I <sub>C</sub> = 250μA T <sub>C</sub> = 25°C	1.0	1.4	2	V
		V <sub>CE</sub> = V <sub>GE</sub> I <sub>C</sub> = 250μA T <sub>C</sub> = 150°C	0.6			V
V <sub>CE(SAT)</sub>	Collector-Emitter Saturation Voltage	V <sub>GE</sub> = 4.5 V I <sub>C</sub> = 10 A T <sub>C</sub> = 25°C			1.1	V
		V <sub>GE</sub> = 4.5 V I <sub>C</sub> = 10 A T <sub>C</sub> = 150 °C			1.0	V
		V <sub>GE</sub> = 4.5 V I <sub>C</sub> = 20 A T <sub>C</sub> = 25°C			1.35	V
		V <sub>GE</sub> = 4.5 V I <sub>C</sub> = 20 A T <sub>C</sub> = 150 °C			1.25	V

### DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
g <sub>fs</sub>	Forward Transconductance	V <sub>CE</sub> = 25 V I <sub>C</sub> = 20 A		35		S
C <sub>ies</sub> C <sub>oes</sub> C <sub>res</sub>	Input Capacitance Output Capacitance Reverse Transfer Capacitance	V <sub>CE</sub> = 25 V f = 1 MHz V <sub>GE</sub> = 0		2300 165 28		pF pF pF
Q <sub>G</sub>	Gate Charge	V <sub>CE</sub> = 280 V I <sub>C</sub> = 20 A V <sub>GE</sub> = 5 V		51		nC

## FUNCTIONAL CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
II	Latching Current	$V_{CLAMP} = 250 \text{ V}$ $V_{GE} = 4.5 \text{ V}$ $R_{GOFF} = 1 \text{ k}\Omega$ $T_C = 150 \text{ }^\circ\text{C}$	80			A
U.I.S.	Functional Test Open Secondary Coil	$R_{GOFF}=1 \text{ k}\Omega$ $L=3 \text{ mH}$ $T_C = 25 \text{ }^\circ\text{C}$ $R_{GOFF}=1 \text{ k}\Omega$ $L=3 \text{ mH}$ $T_C = 150 \text{ }^\circ\text{C}$	21.6 15	26 18		A A

## SWITCHING ON

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$ $t_r$	Delay Time Rise Time	$V_{CC} = 250 \text{ V}$ $I_C = 20 \text{ A}$ $V_{GE} = 4.5 \text{ V}$ $R_G = 1 \text{ k}\Omega$		2.3 0.6		$\mu\text{s}$ $\mu\text{s}$
$(di/dt)_{on}$	Turn-on Current Slope	$V_{CC} = 250 \text{ V}$ $I_C = 20 \text{ A}$ $R_G = 1 \text{ k}\Omega$ $V_{GE} = 4.5 \text{ V}$		550		$\text{A}/\mu\text{s}$
$E_{on}$	Turn-on Switching Losses	$V_{CC}=250\text{V}$ $I_C = 20\text{A}$ $T_C = 25 \text{ }^\circ\text{C}$ $R_G = 1 \text{ k}\Omega$ $V_{GE} = 4.5 \text{ V}$ $T_C = 150 \text{ }^\circ\text{C}$		8.8 9.2		$\text{mJ}$ $\text{mJ}$

## SWITCHING OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_c$ $t_r(V_{off})$ $t_f$ $t_d(off)$ $E_{off}^{(**)}$	Cross-Over Time Off Voltage Rise Time Fall Time Off Voltage Delay Time Turn-off Switching Loss	$V_{CC} = 250 \text{ V}$ $I_C = 20 \text{ A}$ $R_{GE} = 1 \text{ k}\Omega$ $V_{GE} = 4.5 \text{ V}$		4.8 2.6 2.0 11.5 11.8		$\mu\text{s}$ $\mu\text{s}$ $\mu\text{s}$ $\mu\text{s}$ $\text{mJ}$
$t_c$ $t_r(V_{off})$ $t_f$ $t_d(off)$ $E_{off}^{(**)}$	Cross-Over Time Off Voltage Rise Time Fall Time Off Voltage Delay Time Turn-off Switching Loss	$V_{CC} = 250 \text{ V}$ $I_C = 20 \text{ A}$ $R_{GE} = 1 \text{ k}\Omega$ $V_{GE} = 4.5 \text{ V}$ $T_C = 150 \text{ }^\circ\text{C}$		7.8 3.5 3.9 12.0 17.8		$\mu\text{s}$ $\mu\text{s}$ $\mu\text{s}$ $\mu\text{s}$ $\text{mJ}$

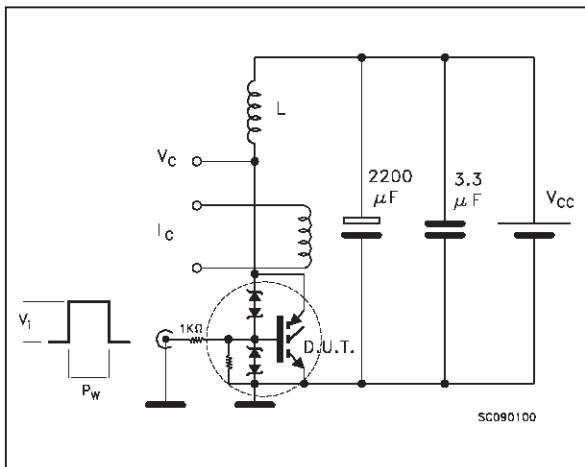
(•) Pulse width limited by safe operating area

(\*) Pulsed: Pulse duration = 300 ms, duty cycle 1.5 %

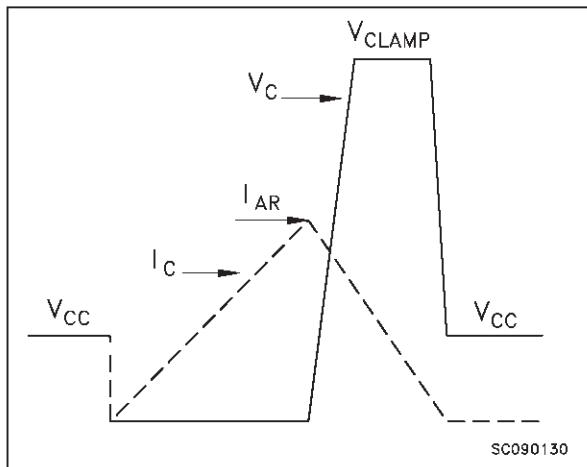
(\*\*)Losses Include Also The Tail (jedec Standardization)

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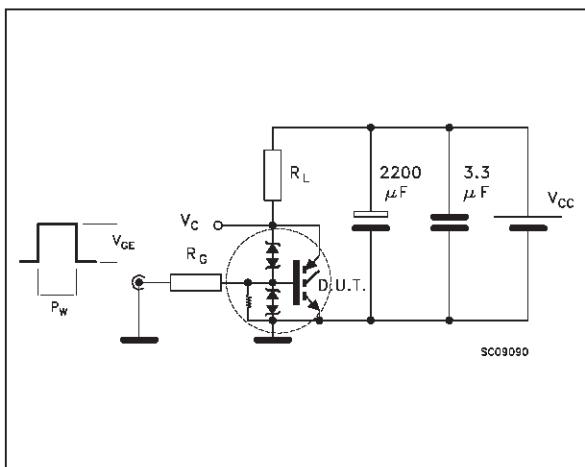
**Fig. 1:** Unclamped Inductive Load Test Circuit



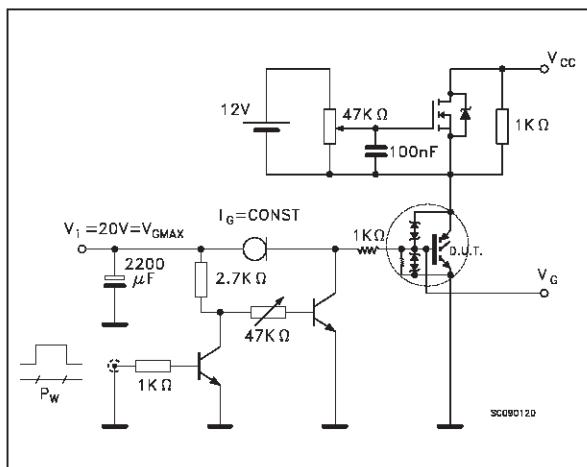
**Fig. 2:** Unclamped Inductive Waveform



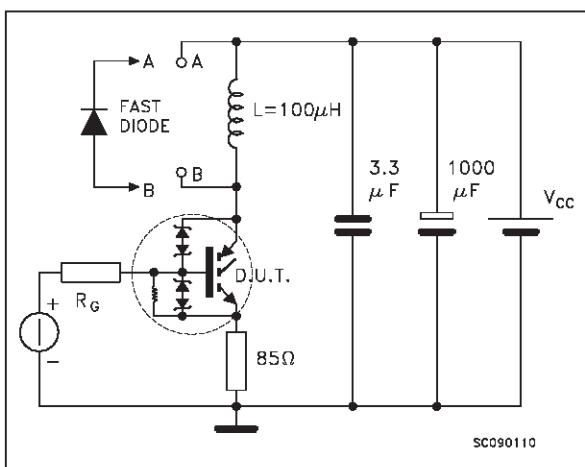
**Fig. 3:** Switching Times Test Circuits For Resistive Load



**Fig. 4:** Gate Charge test Circuit

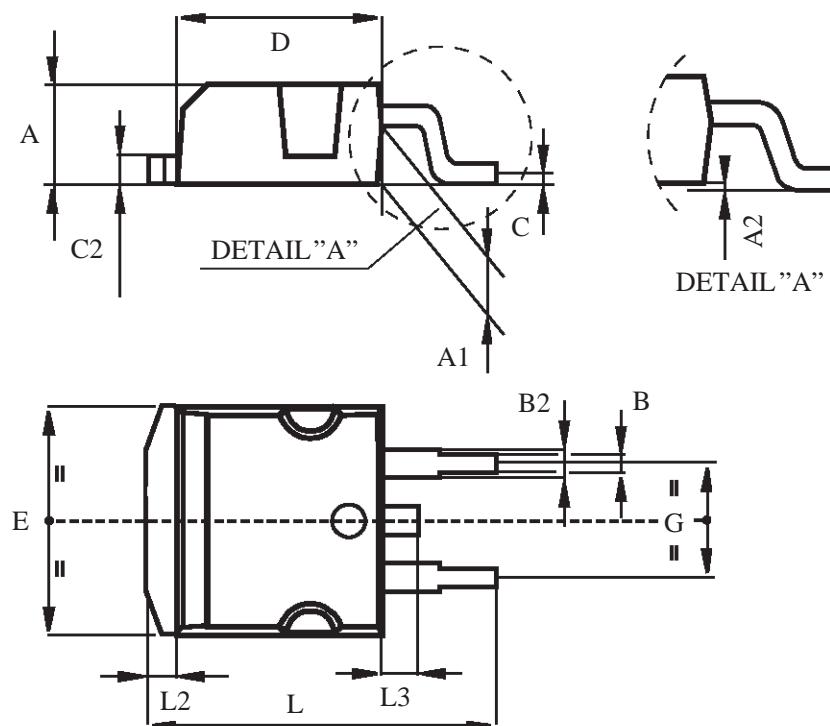


**Fig. 5:** Test Circuit For Inductive Load Switching And Diode Recovery Times



TO-263 (D<sup>2</sup>PAK) MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.4		4.6	0.173		0.181
A1	2.49		2.69	0.098		0.106
B	0.7		0.93	0.027		0.036
B2	1.14		1.7	0.044		0.067
C	0.45		0.6	0.017		0.023
C2	1.21		1.36	0.047		0.053
D	8.95		9.35	0.352		0.368
E	10		10.4	0.393		0.409
G	4.88		5.28	0.192		0.208
L	15		15.85	0.590		0.624
L2	1.27		1.4	0.050		0.055
L3	1.4		1.75	0.055		0.068



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