



STE10/100A Bill of Materials

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STE10/100A Evaluation Board Revised: Thursday, February 10, 2000
Revision: 1.0f

WITH 7705 FOR POWER ON RESET
WITH 2N3904 TO INSULATE PME# BETWEEN PCI & CHIP

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Item	Quantity	Reference	Part
1	27	C1,C2,C3,C4,C8,C9,C10, C11,C12,C13,C14,C15,C16, C17,C18,C19,C20,C21,C23, C25,C26,C30,C38,C39,C41, C42,C44	0.1UF
2	5	C5,C28,C29,C50,C51	22UF
3	2	C7,C6	22PF
4	3	C36,C37,C46	10PF
5	1	C40	0.001UF
6	1	C43	1000PF
7	1	C45	0.01UF
8	3	D1,D2,D3	LED
9	1	JP1	HEADER
10	1	J1	RJ45
11	5	L1,L2,L3,L4,L5	27ohm, 100MHz BEAD
12	1	OSC	25MHZ oscillator
13	1	Q2	NDS352P
14	1	Q3	NDS351N
15	1	Q4	2N3904
16	2	R1,R13	4.99K
17	3	R2,R3,R4	330
18	3	R5,R22,R40	0
19	1	R6	33
20	2	R9,R14	20K
21	3	R10,R39,R41	10k
22	1	R11	5.1K
23	1	R12	2.2K
24	2	R21,R20	50
25	1	R26	100
26	3	R28,R30,R34	49.9
27	2	R33,R32	75
28	1	R36	133k
29	1	R38	4.7K
30	1	U1	32PIN
31	1	U2	PCISLOT_0
32	1	U3	93C46
33	1	U4	STE10/100A
34	1	U5	LM393M
35	1	U7	HB626-1 Transpower
36	1	U8	74VHC123A
37	1	U9	3.3V Regulator
38	1	X1	25MHz crystal

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