



## VN330SP(8932)

### QUAD HIGH SIDE SMART POWER SOLID STATE RELAY

| TYPE    | V <sub>demag</sub> * | R <sub>DS(on)</sub> * | I <sub>OUT</sub> * | V <sub>CC</sub> |
|---------|----------------------|-----------------------|--------------------|-----------------|
| VN330SP | V <sub>CC</sub> -55V | 0.32 Ω**              | 1 A                | 36 V            |

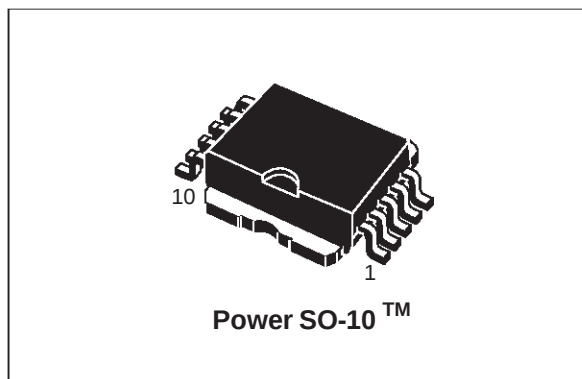
\* per Channel

\*\* at T<sub>J</sub> = 85 °C

- OUTPUT CURRENT: 1 A PER CHANNEL
- DIGITAL INPUTS CLAMPED AT 32V MINIMUM VOLTAGE
- SHORTED LOAD AND OVERTEMPERATURE PROTECTIONS
- BUILT-IN CURRENT LIMITER
- UNDER VOLTAGE SHUT DOWN
- OPEN DRAIN DIAGNOSTIC OUTPUT
- FAST DEMAGNETIZATION OF INDUCTIVE LOADS

#### DESCRIPTION

The VN330SP(8932) is a monolithic device made using STMicroelectronics VIPower Technology, intended for driving four independent resistive or inductive loads with one side connected to ground.

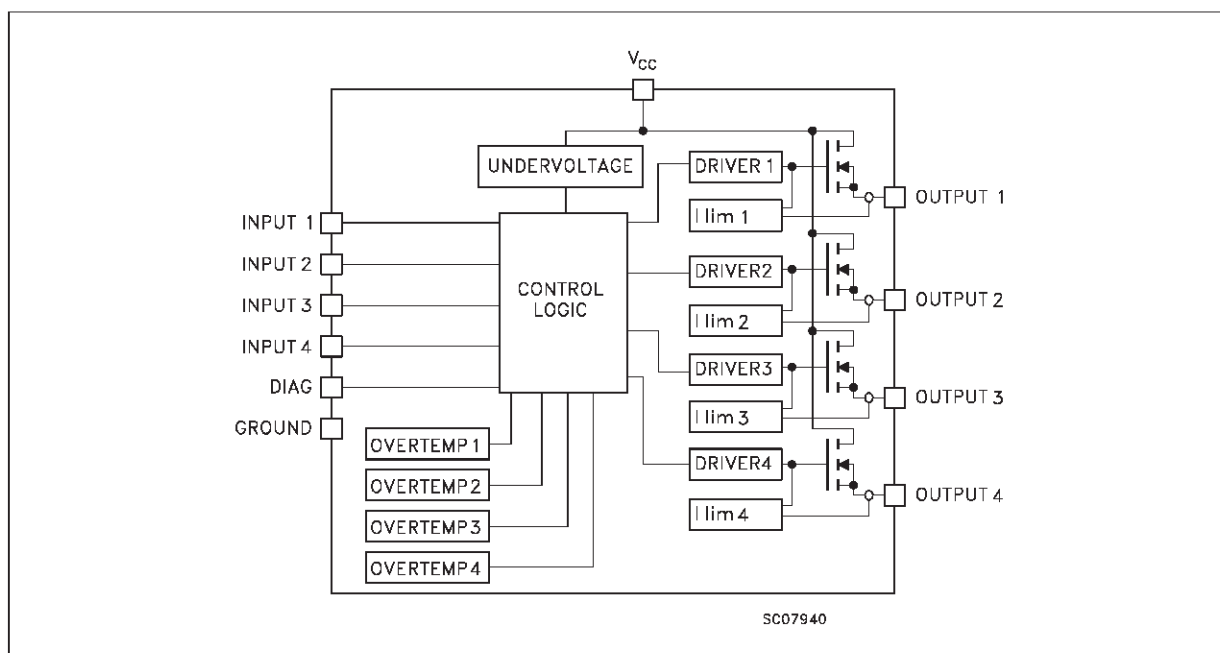


Active current limitation avoids dropping the system power supply in case of shorted load.

Built-in thermal shut-down protects the chip from over temperature and short circuit.

The open drain diagnostic output indicates short circuit and overtemperature conditions.

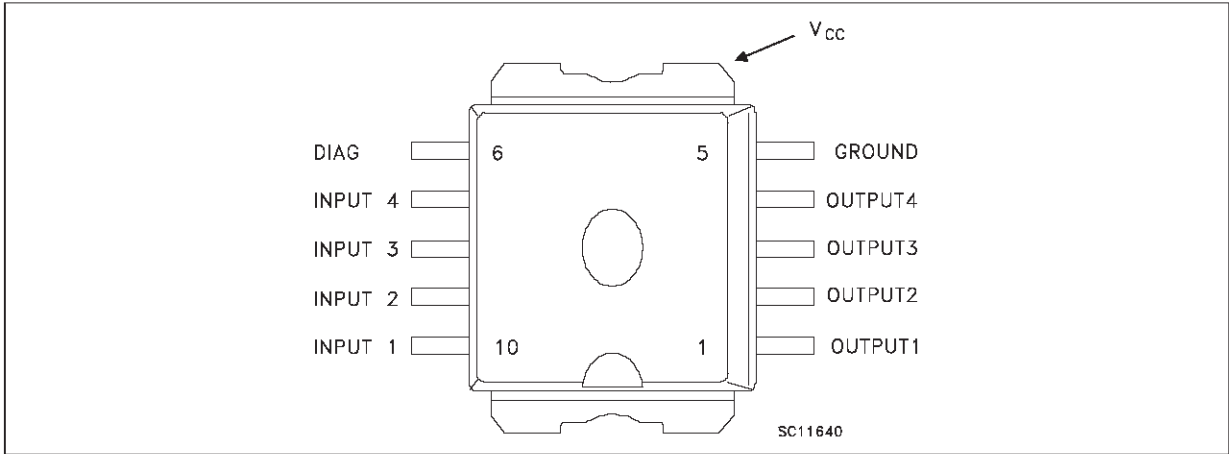
#### BLOCK DIAGRAM



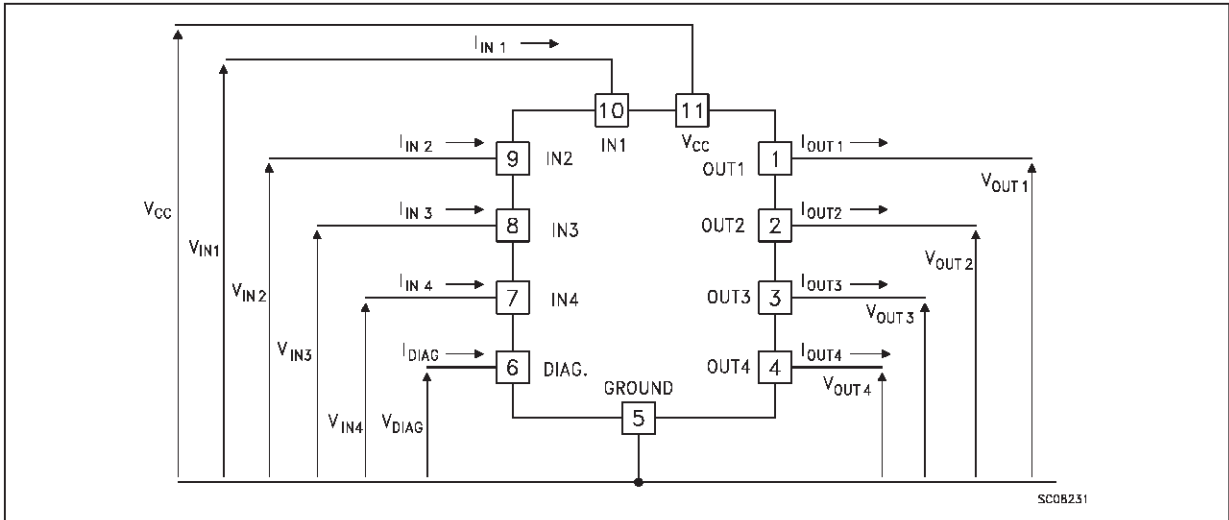
ABSOLUTE MAXIMUM RATING

| Symbol     | Parameter                                                                   | Value              | Unit               |
|------------|-----------------------------------------------------------------------------|--------------------|--------------------|
| $V_{CC}$   | Power Supply Voltage                                                        | 45                 | V                  |
| $-V_{CC}$  | Reverse Supply Voltage                                                      | -0.3               | V                  |
| $I_{OUT}$  | Output Current (cont.)                                                      | Internally Limited | A                  |
| $I_R$      | Reverse Output Current (per channel)                                        | -6                 | A                  |
| $I_{IN}$   | Input Current (per channel)                                                 | $\pm 10$           | mA                 |
| $I_{DIAG}$ | DIAG Pin Current                                                            | $\pm 10$           | mA                 |
| $V_{ESD}$  | Electrostatic Discharge (1.5 k $\Omega$ , 100 pF)                           | 2000               | V                  |
| $E_{AS}$   | Single Pulse Avalanche Energy per Channel Not Symultaneously (see figure 1) | 400                | mJ                 |
| $P_{tot}$  | Power Dissipation at $T_c \leq 25\text{ }^{\circ}\text{C}$                  | Internally Limited | W                  |
| $T_j$      | Junction Operating Temperature                                              | Internally Limited | $^{\circ}\text{C}$ |
| $T_{stg}$  | Storage Temperature                                                         | -55 to 150         | $^{\circ}\text{C}$ |

CONNECTION DIAGRAM



CURRENT AND VOLTAGE CONVENTIONS



**THERMAL DATA**

|                       |                                          |     |    |      |
|-----------------------|------------------------------------------|-----|----|------|
| R <sub>thj-case</sub> | Thermal Resistance Junction-case (1)     | Max | 2  | °C/W |
| R <sub>thj-amb</sub>  | Thermal Resistance Junction-ambient (\$) | Max | 50 | °C/W |

(1) All channels ON

(\$) When mounted using minimum recommended pad size on FR-4 board

**ELECTRICAL CHARACTERISTICS** (10 V < V<sub>CC</sub> < 36 V; -25 °C < T<sub>J</sub> < 125 °C unless otherwise specified)

**POWER**

| Symbol             | Parameter                  | Test Conditions                                                                                                             | Min.                | Typ.                | Max.                | Unit     |
|--------------------|----------------------------|-----------------------------------------------------------------------------------------------------------------------------|---------------------|---------------------|---------------------|----------|
| V <sub>CC</sub>    | Supply Voltage             |                                                                                                                             | 10                  |                     | 36                  | V        |
| R <sub>on</sub>    | On State Resistance        | I <sub>OUT</sub> = 0.5 A      T <sub>J</sub> = 125 °C<br>I <sub>OUT</sub> = 0.5 A                                           |                     |                     | 0.4<br>0.32         | Ω<br>Ω   |
| I <sub>S</sub>     | Supply Current             | All Channels Off    V <sub>IN</sub> = 30 V<br>On State (T <sub>J</sub> = 125 °C) I <sub>out1</sub> .. I <sub>out4</sub> = 0 |                     |                     | 1<br>10             | mA<br>mA |
| V <sub>demag</sub> | Output Voltage at Turn-Off | I <sub>out</sub> = 0.5 A    L <sub>LOAD</sub> = 1 mH                                                                        | V <sub>CC</sub> -65 | V <sub>CC</sub> -55 | V <sub>CC</sub> -45 | V        |

**SWITCHING** (V<sub>CC</sub> = 24 V)

| Symbol                 | Parameter                             | Test Conditions                                                                                                          | Min. | Typ. | Max.       | Unit         |
|------------------------|---------------------------------------|--------------------------------------------------------------------------------------------------------------------------|------|------|------------|--------------|
| t <sub>d(on)</sub>     | Turn-on Delay Time Of Output Current  | I <sub>OUT</sub> = 0.5 A Resistive Load<br>Input Rise Time < 0.1 μs<br>T <sub>J</sub> = 25 °C<br>T <sub>J</sub> = 125 °C |      | 30   | 40<br>60   | μs<br>μs     |
| t <sub>r</sub>         | Rise Time Of Output Current           | I <sub>OUT</sub> = 0.5 A Resistive Load<br>Input Rise Time < 0.1 μs<br>T <sub>J</sub> = 25 °C<br>T <sub>J</sub> = 125 °C |      | 50   | 100<br>115 | μs<br>μs     |
| t <sub>d(off)</sub>    | Turn-off Delay Time Of Output Current | I <sub>OUT</sub> = 0.5 A Resistive Load<br>Input Rise Time < 0.1 μs<br>T <sub>J</sub> = 25 °C<br>T <sub>J</sub> = 125 °C |      | 20   | 30<br>40   | μs<br>μs     |
| t <sub>f</sub>         | Fall Time Of Output Current           | I <sub>OUT</sub> = 0.5 A Resistive Load<br>Input Rise Time < 0.1 μs<br>T <sub>J</sub> = 25 °C<br>T <sub>J</sub> = 125 °C |      | 8    | 15<br>20   | μs<br>μs     |
| (di/dt) <sub>on</sub>  | Turn-on Current Slope                 | I <sub>OUT</sub> = 0.5 A<br>I <sub>OUT</sub> = I <sub>lim</sub> T <sub>J</sub> = 25 °C                                   |      |      | 0.5<br>2   | A/μs<br>A/μs |
| (di/dt) <sub>off</sub> | Turn-off Current Slope                | I <sub>OUT</sub> = 0.5 A<br>I <sub>OUT</sub> = I <sub>lim</sub> T <sub>J</sub> = 25 °C                                   |      |      | 2<br>4     | A/μs<br>A/μs |

**ELECTRICAL CHARACTERISTICS** (continued)**LOGIC INPUT** (Each Channel)

| Symbol         | Parameter                              | Test Conditions                                         | Min. | Typ.       | Max. | Unit               |
|----------------|----------------------------------------|---------------------------------------------------------|------|------------|------|--------------------|
| $V_{IL}$       | Input Low Level Voltage                |                                                         |      |            | 2    | V                  |
| $V_{IH}$       | Input High Level Voltage (see note 1)  |                                                         | 3.5  |            |      | V                  |
| $V_{I(hyst.)}$ | Input Hysteresis Voltage               |                                                         |      | 0.5        |      | V                  |
| $I_{IN}$       | Input Current                          | $V_{IN} = 0$ to 30 V<br>$V_{IN} = 0$ to 2 V             | 25   |            | 600  | $\mu A$<br>$\mu A$ |
| $I_{LGND}$     | Output Current in Ground Disconnection | $V_{CC} = V_{INX} = GND = DIAG = 24$ V<br>$T_J = 25$ °C |      |            | 25   | mA                 |
| $V_{ICL}$      | Input Clamp Voltage (see note 1)       | $I_{IN} = 1$ mA<br>$I_{IN} = -1$ mA                     | 32   | 36<br>-0.7 |      | V<br>V             |

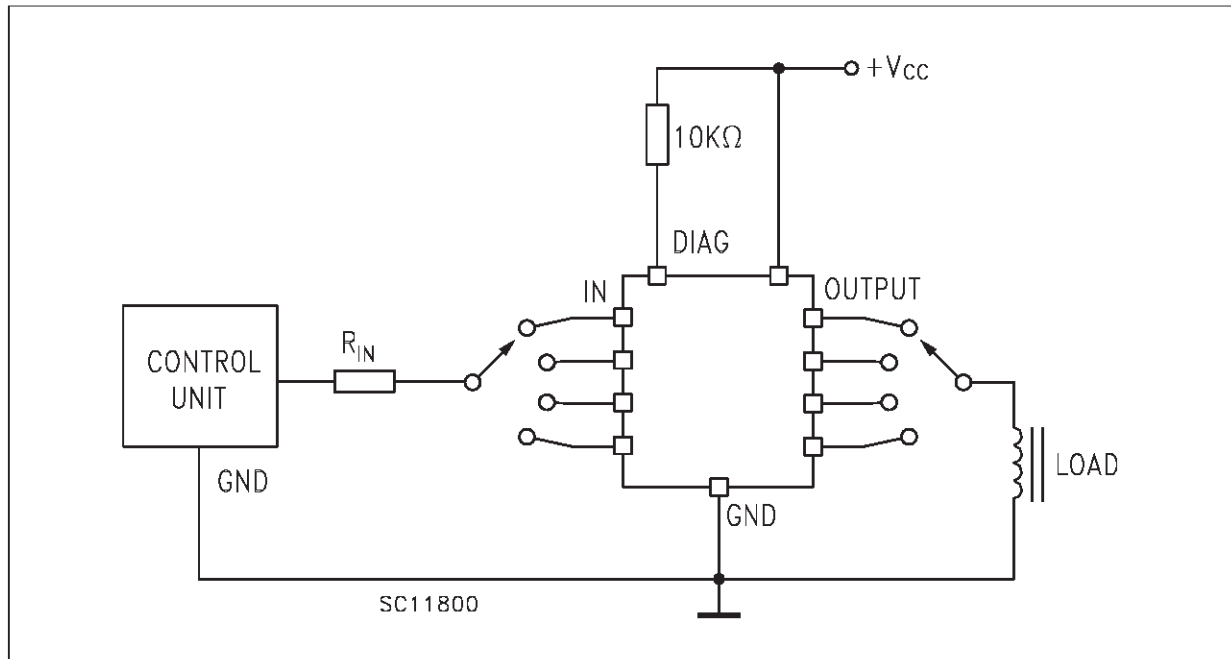
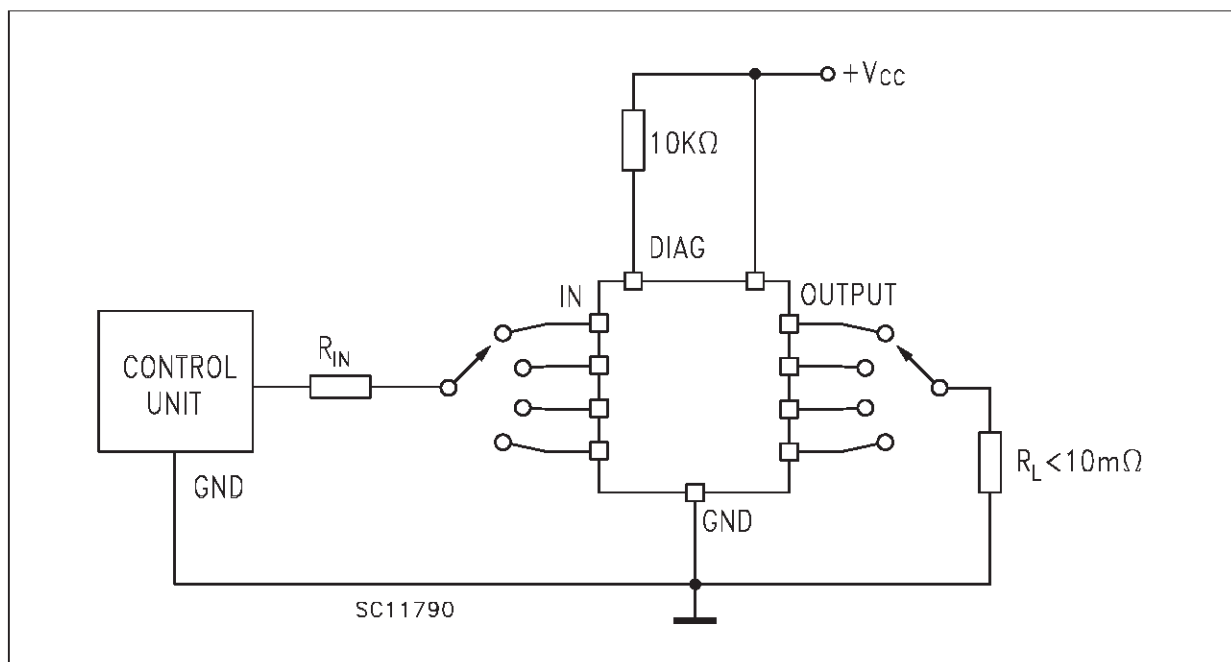
note 1 : The input voltage is internally clamped at 32V minimum, it is possible to connect the input pins to an higher voltage via an external resistor calculate to not exceed 10 mA.

**PROTECTION AND DIAGNOSTICS**

| Symbol               | Parameter                         | Test Conditions                                                              | Min. | Typ.       | Max. | Unit    |
|----------------------|-----------------------------------|------------------------------------------------------------------------------|------|------------|------|---------|
| $V_{STAT} (\bullet)$ | Status Voltage Output Low         | $I_{STAT} = 5$ mA<br>(Fault Condition)                                       |      |            | 1    | V       |
| $V_{SCL} (\bullet)$  | Status Clamp Voltage              | $I_{STAT} = 1$ mA<br>$I_{STAT} = -1$ mA                                      | 32   | 36<br>-0.7 |      | V<br>V  |
| $V_{USD}$            | Under Voltage Shut Down           |                                                                              | 5    |            | 8    | V       |
| $I_{LIM}$            | DC Short Circuit Current          | $V_{CC} = 24$ V $R_{LOAD} < 10$ m $\Omega$                                   | 1    |            | 2.5  | A       |
| $I_{OVPK}$           | Peak Short Circuit Current        | $V_{CC} = 24$ V $V_{in} = 30$ V<br>$R_{LOAD} < 10$ m $\Omega$ (see figure 2) |      |            | 4    | A       |
| $I_{DIAGH}$          | Leakage on diag pin in high state | $V_{DIAG} = 24$ V                                                            |      |            | 100  | $\mu A$ |
| $I_{LOAD}$           | Output Leakage Current            | $V_{CC} = 10$ to 36V $V_{IN} = 0$ V<br>4 Channel in Parallel                 |      |            | 25   | $\mu A$ |
| $t_{SC}$             | Delay Time of Current Limiter     |                                                                              |      |            | 100  | $\mu s$ |
| $T_{TSD}$            | Thermal Shut-down Temperature     |                                                                              | 150  | 170        |      | °C      |
| $T_R$                | Reset Temperature                 |                                                                              | 135  | 155        |      | °C      |

( $\bullet$ ) Status determination > 100  $\mu s$  after the switching edge.

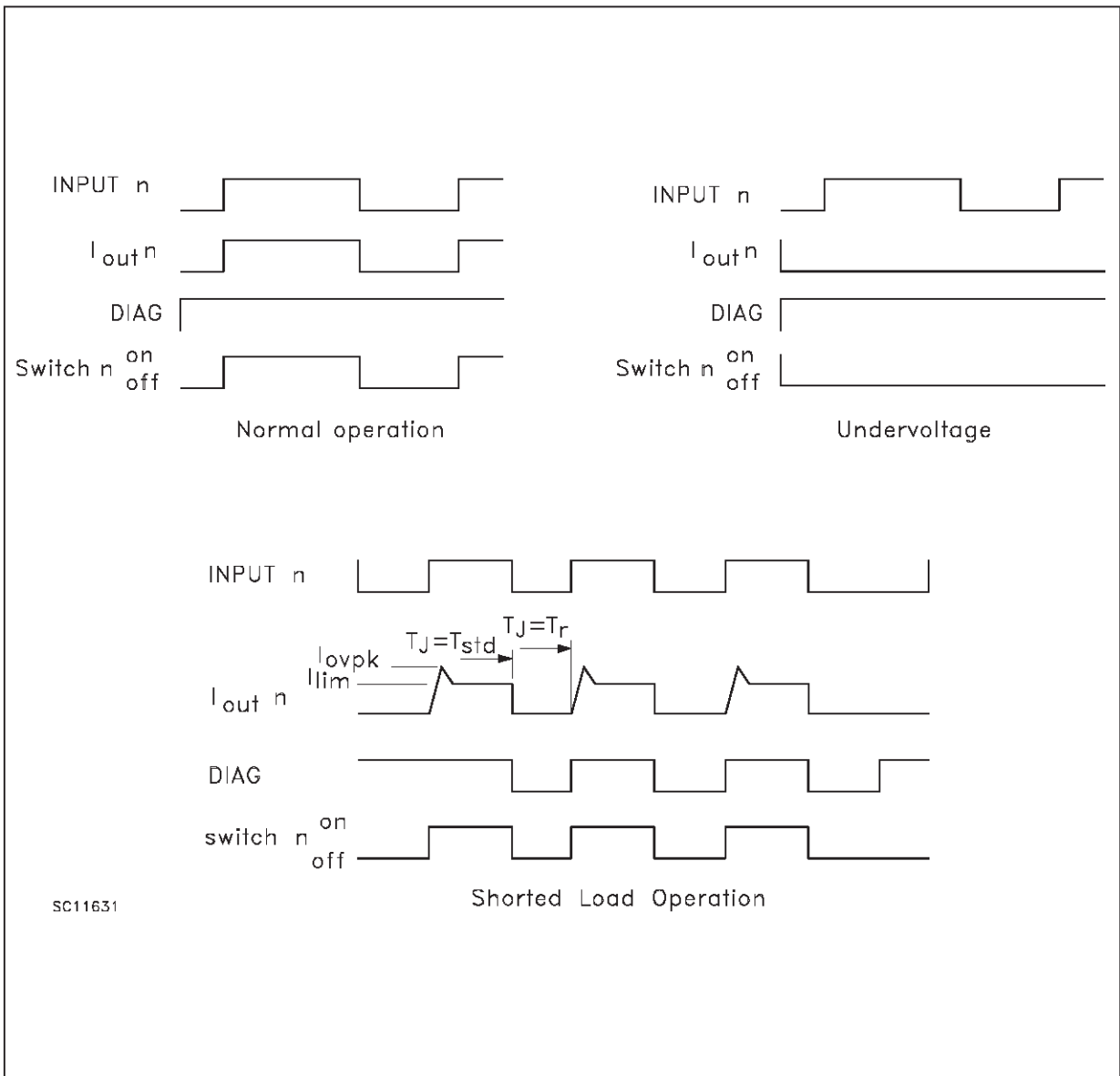
Note: If INPUTn pin is left floating the corresponding channel will automatically switch off. If GND pin is disconnected, all channels will switch off provided  $V_{CC}$  does not exceed 36V

**FIGURE 1:** Avalanche Energy Test Circuit**FIGURE 2:** Peak Short Circuit Current Test Circuit

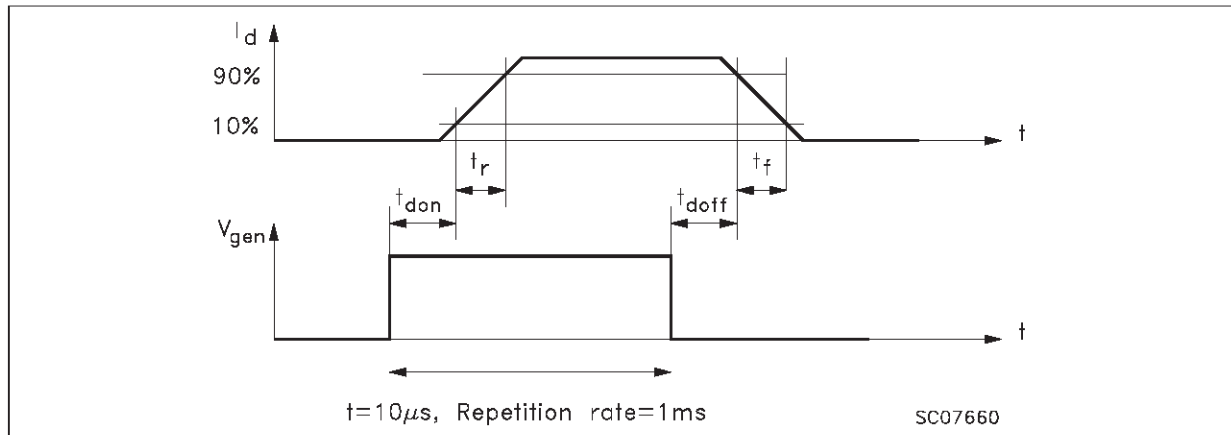
TRUTH TABLE

|                                      | INPUTn | OUTPUTn | DIAGNOSTIC |
|--------------------------------------|--------|---------|------------|
| Normal Operation                     | L      | L       | H          |
|                                      | H      | H       | H          |
| Over-temperature                     | L      | L       | H          |
|                                      | H      | L       | L          |
| Under-voltage                        | L      | L       | H          |
|                                      | H      | L       | H          |
| Shorted Load<br>(current limitation) | L      | L       | H          |
|                                      | H      | H       | H          |

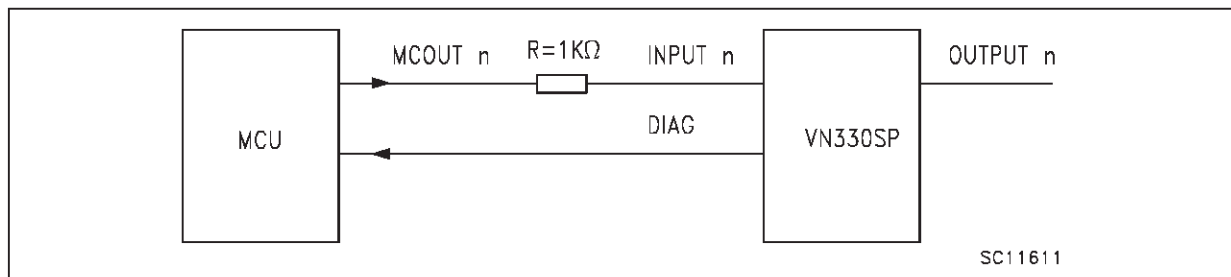
FIGURE 3: Switching Waveforms



## SWITCHING PARAMETERS TEST CONDITIONS

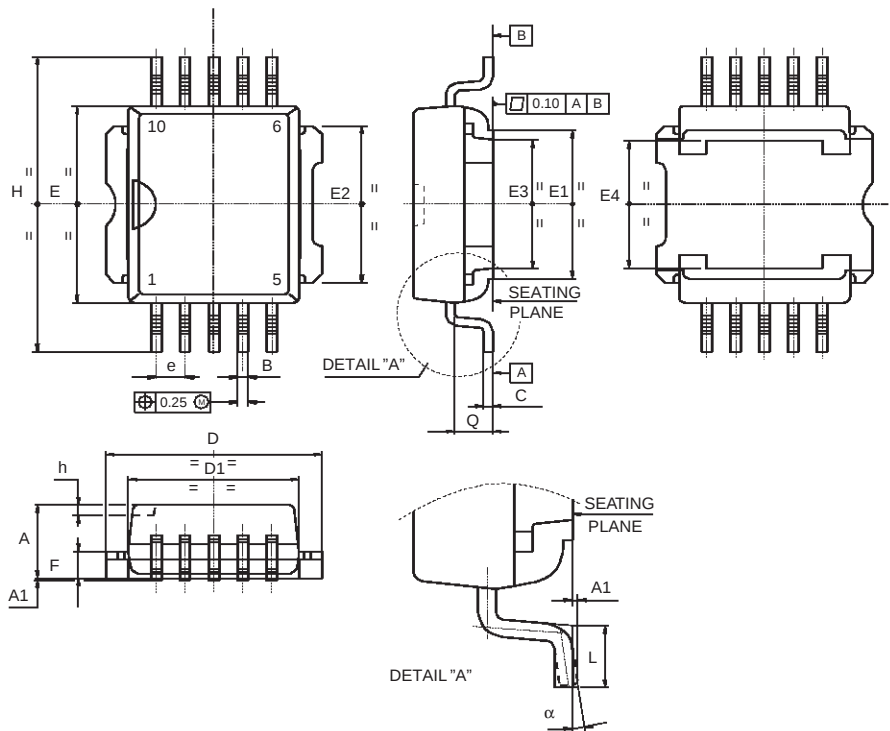


## DRIVING CIRCUIT



PowerSO-10 MECHANICAL DATA

| DIM.     | mm    |      |       | inch  |       |       |
|----------|-------|------|-------|-------|-------|-------|
|          | MIN.  | TYP. | MAX.  | MIN.  | TYP.  | MAX.  |
| A        | 3.35  |      | 3.65  | 0.132 |       | 0.144 |
| A1       | 0.00  |      | 0.10  | 0.000 |       | 0.004 |
| B        | 0.40  |      | 0.60  | 0.016 |       | 0.024 |
| c        | 0.35  |      | 0.55  | 0.013 |       | 0.022 |
| D        | 9.40  |      | 9.60  | 0.370 |       | 0.378 |
| D1       | 7.40  |      | 7.60  | 0.291 |       | 0.300 |
| E        | 9.30  |      | 9.50  | 0.366 |       | 0.374 |
| E1       | 7.20  |      | 7.40  | 0.283 |       | 0.291 |
| E2       | 7.20  |      | 7.60  | 0.283 |       | 0.300 |
| E3       | 6.10  |      | 6.35  | 0.240 |       | 0.250 |
| E4       | 5.90  |      | 6.10  | 0.232 |       | 0.240 |
| e        |       | 1.27 |       |       | 0.050 |       |
| F        | 1.25  |      | 1.35  | 0.049 |       | 0.053 |
| H        | 13.80 |      | 14.40 | 0.543 |       | 0.567 |
| h        |       | 0.50 |       |       | 0.002 |       |
| L        | 1.20  |      | 1.80  | 0.047 |       | 0.071 |
| q        |       | 1.70 |       |       | 0.067 |       |
| $\alpha$ | 0°    |      | 8°    |       |       |       |



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