

**STD4NB25****N - CHANNEL 250V - 0.95Ω - 4A - DPAK/IPAK
PowerMESH™ MOSFET****PRELIMINARY DATA**

TYPE	V _{DSS}	R _{DS(on)}	I _D
STD4NB25	250 V	< 1.1 Ω	4 A

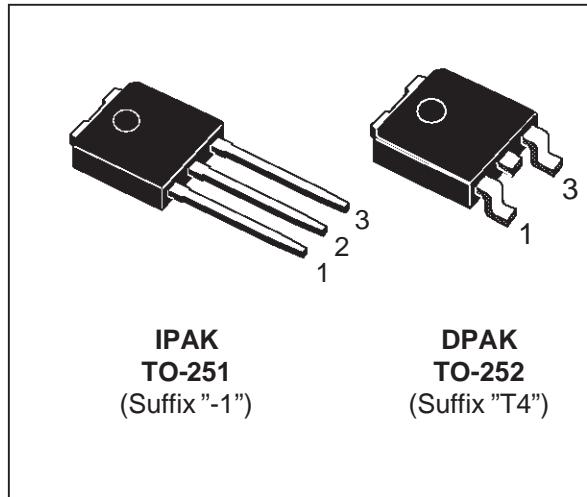
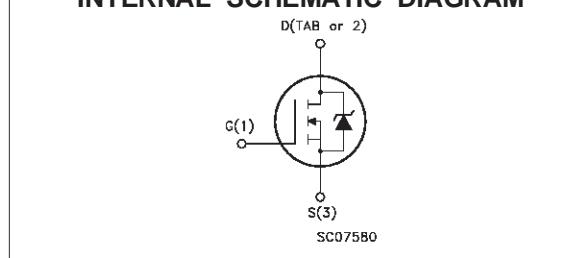
- ✓ TYPICAL R_{DS(on)} = 0.95 Ω
- ✓ EXTREMELY HIGH dv/dt CAPABILITY
- ✓ 100% AVALANCHE TESTED
- ✓ VERY LOW INTRINSIC CAPACITANCES
- ✓ GATE CHARGE MINIMIZED
- ✓ FOR TROUGH-HOLE VERSION CONTACT SALES OFFICE

DESCRIPTION

Using the latest high voltage MESH OVERLAY™ process, STMicroelectronics has designed an advanced family of power MOSFETs with outstanding performances. The new patent pending strip layout coupled with the Company's proprietary edge termination structure, gives the lowest R_{DS(on)} per area, exceptional avalanche and dv/dt capabilities and unrivalled gate charge and switching characteristics.

APPLICATIONS

- ✓ SWITCH MODE POWER SUPPLIES (SMPS)
- ✓ DC-AC CONVERTERS FOR WELDING EQUIPMENT AND UNINTERRUPTIBLE POWER SUPPLIES AND MOTOR DRIVE

**INTERNAL SCHEMATIC DIAGRAM****ABSOLUTE MAXIMUM RATINGS**

Symbol	Parameter	Value	Unit
V _{DS}	Drain-source Voltage (V _{GS} = 0)	250	V
V _{DGR}	Drain-gate Voltage (R _{GS} = 20 kΩ)	250	V
V _{GS}	Gate-source Voltage	± 30	V
I _D	Drain Current (continuous) at T _c = 25 °C	4	A
I _D	Drain Current (continuous) at T _c = 100 °C	2.5	A
I _{DM(•)}	Drain Current (pulsed)	16	A
P _{tot}	Total Dissipation at T _c = 25 °C	40	W
	Derating Factor	0.32	W/°C
dv/dt(1)	Peak Diode Recovery voltage slope	5.5	V/ns
T _{stg}	Storage Temperature	-65 to 150	°C
T _j	Max. Operating Junction Temperature	150	°C

(•) Pulse width limited by safe operating area

(1) I_{SD} ≤ 4 A, di/dt ≤ 200 A/μs, V_{DD} ≤ V_{(BR)DSS}, T_j ≤ T_{JMAX}

STD4NB25

THERMAL DATA

$R_{thj\text{-case}}$	Thermal Resistance Junction-case	Max	3.12	$^{\circ}\text{C}/\text{W}$
$R_{thj\text{-amb}}$	Thermal Resistance Junction-ambient	Max	100	$^{\circ}\text{C}/\text{W}$
$R_{thc\text{-sink}}$	Thermal Resistance Case-sink	Typ	1.5	$^{\circ}\text{C}/\text{W}$
T_L	Maximum Lead Temperature For Soldering Purpose		275	$^{\circ}\text{C}$

AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I_{AR}	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T_j max)	4	A
E_{AS}	Single Pulse Avalanche Energy (starting $T_j = 25^{\circ}\text{C}$, $I_D = I_{AR}$, $V_{DD} = 50$ V)	75	mJ

ELECTRICAL CHARACTERISTICS ($T_{case} = 25^{\circ}\text{C}$ unless otherwise specified)

OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{(BR)DSS}$	Drain-source Breakdown Voltage	$I_D = 250 \mu\text{A}$ $V_{GS} = 0$	250			V
I_{DSS}	Zero Gate Voltage Drain Current ($V_{GS} = 0$)	$V_{DS} = \text{Max Rating}$ $V_{DS} = \text{Max Rating}$ $T_c = 125^{\circ}\text{C}$			1 10	μA μA
I_{GSS}	Gate-body Leakage Current ($V_{DS} = 0$)	$V_{GS} = \pm 30$ V			± 100	nA

ON (*)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$ $I_D = 250 \mu\text{A}$	3	4	5	V
$R_{DS(\text{on})}$	Static Drain-source On Resistance	$V_{GS} = 10$ V $I_D = 2$ A		0.95	1.1	Ω
$I_{D(\text{on})}$	On State Drain Current	$V_{DS} > I_{D(\text{on})} \times R_{DS(\text{on})\text{max}}$ $V_{GS} = 10$ V	4			A

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
g_{fs} (*)	Forward Transconductance	$V_{DS} > I_{D(\text{on})} \times R_{DS(\text{on})\text{max}}$ $I_D = 2$ A	0.7	1.6		S
C_{iss} C_{oss} C_{rss}	Input Capacitance Output Capacitance Reverse Transfer Capacitance	$V_{DS} = 25$ V $f = 1$ MHz $V_{GS} = 0$		260 70 9	350 100 13	pF pF pF

ELECTRICAL CHARACTERISTICS (continued)

SWITCHING ON

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$ t_r	Turn-on Time Rise Time	$V_{DD} = 125 \text{ V}$ $I_D = 3 \text{ A}$ $R_G = 4.7 \Omega$ $V_{GS} = 10 \text{ V}$		9 9	13 13	ns ns
Q_g Q_{gs} Q_{gd}	Total Gate Charge Gate-Source Charge Gate-Drain Charge	$V_{DD} = 200 \text{ V}$ $I_D = 6 \text{ A}$ $V_{GS} = 10 \text{ V}$		12 7.5 3	17	nC nC nC

SWITCHING OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{r(Voff)}$ t_f t_c	Off-voltage Rise Time Fall Time Cross-over Time	$V_{DD} = 200 \text{ V}$ $I_D = 6 \text{ A}$ $R_G = 4.7 \Omega$ $V_{GS} = 10 \text{ V}$		8 7 15	11 10 20	ns ns ns

SOURCE DRAIN DIODE

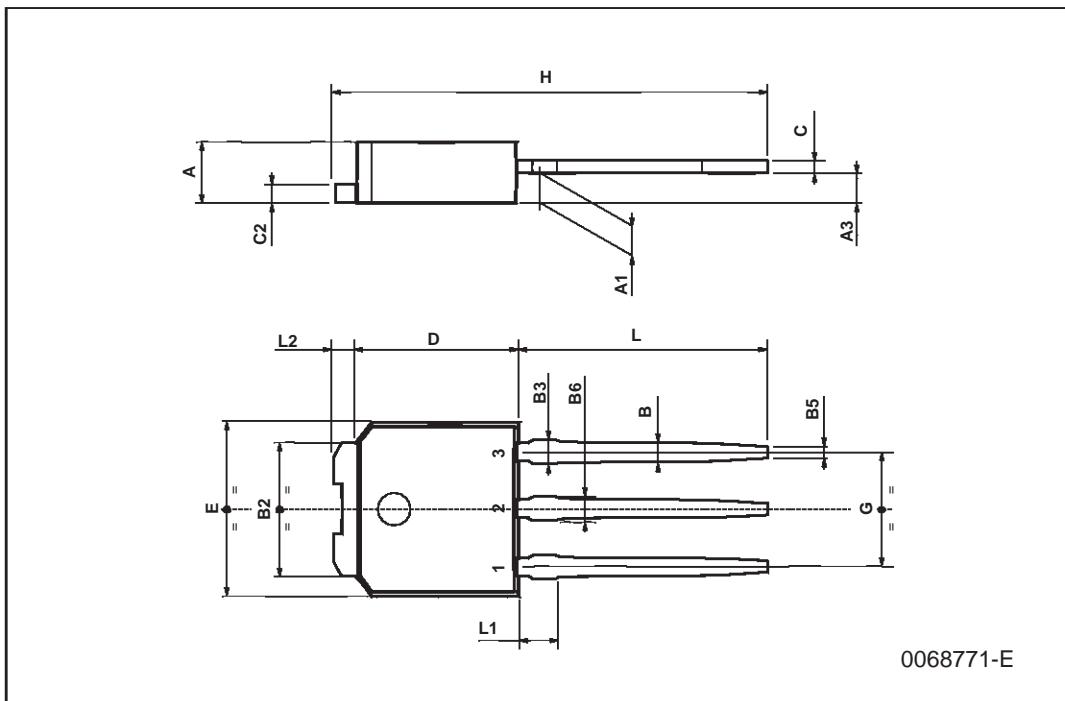
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{SD} $I_{SDM}(\bullet)$	Source-drain Current Source-drain Current (pulsed)				4 16	A A
V_{SD} (*)	Forward On Voltage	$I_{SD} = 6 \text{ A}$ $V_{GS} = 0$			1.5	V
t_{rr} Q_{rr} I_{RRM}	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current	$I_{SD} = 6 \text{ A}$ $di/dt = 100 \text{ A}/\mu\text{s}$ $V_{DD} = 100 \text{ V}$ $T_j = 150^\circ\text{C}$		160 720 9		ns nC A

(*) Pulsed: Pulse duration = 300 μs , duty cycle 1.5 %

(*) Pulse width limited by safe operating area

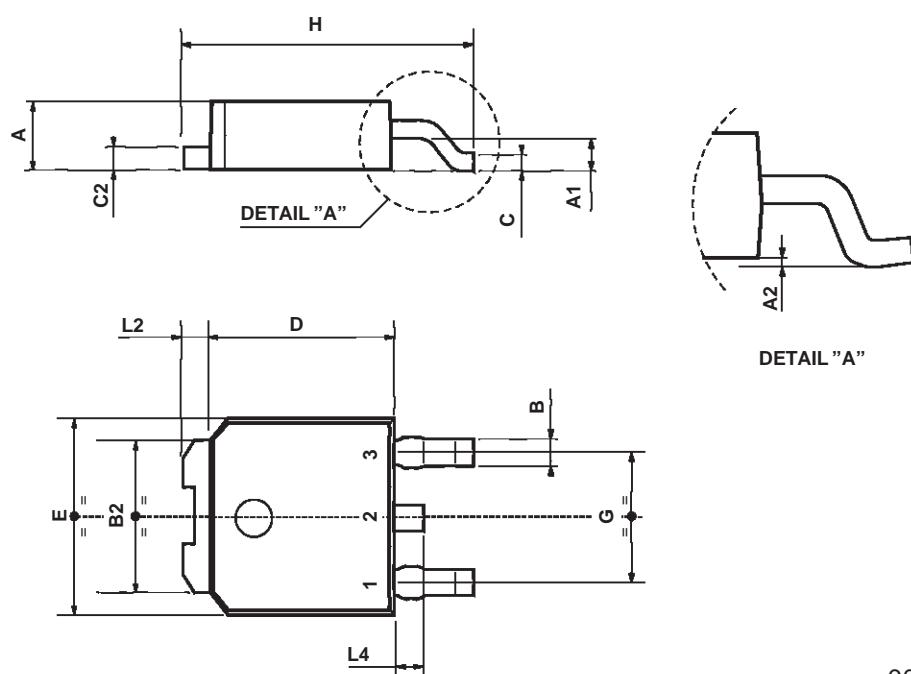
TO-251 (IPAK) MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	2.2		2.4	0.086		0.094
A1	0.9		1.1	0.035		0.043
A3	0.7		1.3	0.027		0.051
B	0.64		0.9	0.025		0.031
B2	5.2		5.4	0.204		0.212
B3			0.85			0.033
B5		0.3			0.012	
B6			0.95			0.037
C	0.45		0.6	0.017		0.023
C2	0.48		0.6	0.019		0.023
D	6		6.2	0.236		0.244
E	6.4		6.6	0.252		0.260
G	4.4		4.6	0.173		0.181
H	15.9		16.3	0.626		0.641
L	9		9.4	0.354		0.370
L1	0.8		1.2	0.031		0.047
L2		0.8	1		0.031	0.039



TO-252 (DPAK) MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	2.2		2.4	0.086		0.094
A1	0.9		1.1	0.035		0.043
A2	0.03		0.23	0.001		0.009
B	0.64		0.9	0.025		0.035
B2	5.2		5.4	0.204		0.212
C	0.45		0.6	0.017		0.023
C2	0.48		0.6	0.019		0.023
D	6		6.2	0.236		0.244
E	6.4		6.6	0.252		0.260
G	4.4		4.6	0.173		0.181
H	9.35		10.1	0.368		0.397
L2		0.8			0.031	
L4	0.6		1	0.023		0.039



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