

**STN2NF06****N - CHANNEL 60V - 0.12Ω - 2A - SOT-223
STripFET™ POWER MOSFET**

| TYPE | V _{DSS} | R _{D(on)} | I _D |
|----------|------------------|--------------------|----------------|
| STN2NF06 | 60 V | < 0.15 Ω | 2 A |

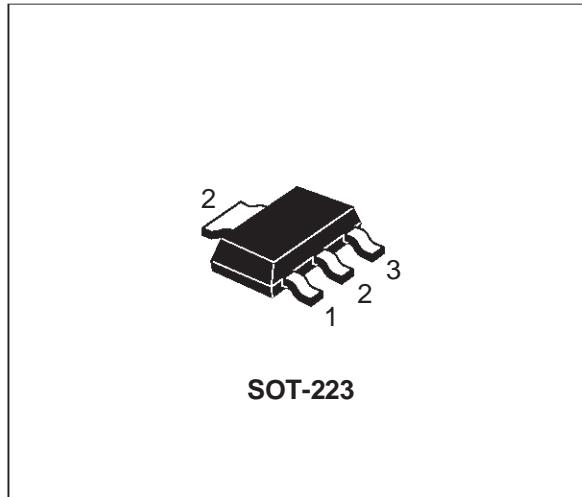
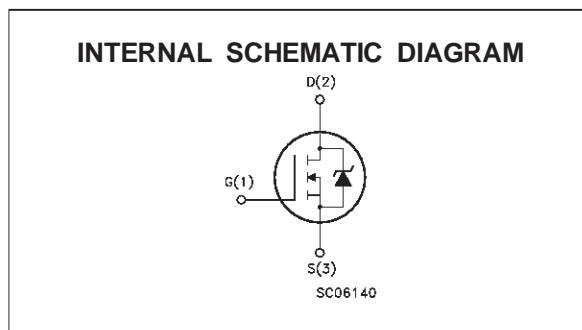
- TYPICAL R_{D(on)} = 0.12 Ω
- EXCEPTIONAL dv/dt CAPABILITY
- AVALANCHE RUGGED TECHNOLOGY
- 100 % AVALANCHE TESTED
- APPLICATION ORIENTED CHARACTERIZATION

DESCRIPTION

This Power Mosfet is the latest development of STMicroelectronics unique "Single Feature Size™" strip-based process. The resulting transistor shows extremely high packing density for low on-resistance, rugged avalanche characteristics and less critical alignment steps therefore a remarkable manufacturing reproducibility.

APPLICATIONS

- DC MOTOR CONTROL (DISK DRIVES,etc.)
- DC-DC & DC-AC CONVERTERS
- SYNCHRONOUS RECTIFICATION

**SOT-223****ABSOLUTE MAXIMUM RATINGS**

| Symbol | Parameter | Value | Unit |
|--------------------|---|------------|------|
| V _{DS} | Drain-source Voltage (V _{GS} = 0) | 60 | V |
| V _{DGR} | Drain-gate Voltage (R _{GS} = 20 kΩ) | 60 | V |
| V _{GS} | Gate-source Voltage | ± 20 | V |
| I _D | Drain Current (continuous) at T _c = 25 °C | 2 | A |
| I _D | Drain Current (continuous) at T _c = 100 °C | 1.8 | A |
| I _{DM(•)} | Drain Current (pulsed) | 8 | A |
| P _{tot} | Total Dissipation at T _c = 25 °C | 2.5 | W |
| | Derating Factor | 0.02 | W/°C |
| dv/dt(1) | Peak Diode Recovery voltage slope | 6 | V/ns |
| T _{stg} | Storage Temperature | -65 to 150 | °C |
| T _j | Max. Operating Junction Temperature | 150 | °C |

(•) Pulse width limited by safe operating area
New RDS (on) spec. starting from JULY 98

(1) I_{SD} ≤ 8 A, di/dt ≤ 200 A/μs, V_{DD} ≤ V_{(BR)DSS}, T_j ≤ T_{JMAX}

STN2NF06

THERMAL DATA

| | | | | |
|----------------------|--|-----|-----|------|
| R _{thj-pcb} | Thermal Resistance Junction-PC Board | Max | 50 | °C/W |
| R _{thj-amb} | Thermal Resistance Junction-ambient (Surface Mounted) | Max | 60 | °C/W |
| T _L | Maximum Lead Temperature For Soldering Purpose | | 260 | °C |

AVALANCHE CHARACTERISTICS

| Symbol | Parameter | Max Value | Unit |
|-----------------|---|-----------|------|
| I _{AR} | Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T _j max) | 2 | A |
| E _{AS} | Single Pulse Avalanche Energy (starting T _j = 25 °C, I _D = I _{AR} , V _{DD} = 25 V) | 20 | mJ |

ELECTRICAL CHARACTERISTICS (T_{case} = 25 °C unless otherwise specified)

OFF

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|----------------------|---|--|------|------|---------|----------|
| V _{(BR)DSS} | Drain-source Breakdown Voltage | I _D = 250 μA V _{GS} = 0 | 60 | | | V |
| I _{DSS} | Zero Gate Voltage Drain Current (V _{GS} = 0) | V _{DS} = Max Rating V _{DS} = Max Rating T _c = 125 °C | | | 1 10 | μA μA |
| I _{GSS} | Gate-body Leakage Current (V _{DS} = 0) | V _{GS} = ± 20 V | | | ± 100 | nA |

ON (*)

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|---------------------|-----------------------------------|--|------|------|------|------|
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} = V _{GS} I _D = 250 μA | 2 | 3 | 4 | V |
| R _{D(on)} | Static Drain-source On Resistance | V _{GS} = 10 V I _D = 6 A | | 0.12 | 0.15 | Ω |
| I _{D(on)} | On State Drain Current | V _{DS} > I _{D(on)} × R _{D(on)max} V _{GS} = 10 V | 2 | | | A |

DYNAMIC

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|--|---|---|------|------------------|------|----------------|
| g _{fs} (*) | Forward Transconductance | V _{DS} > I _{D(on)} × R _{D(on)max} I _D = 1 A | 1 | 3 | | S |
| C _{iss} C _{oss} C _{rss} | Input Capacitance Output Capacitance Reverse Transfer Capacitance | V _{DS} = 25 V f = 1 MHz V _{GS} = 0 V | | 760 100 30 | | pF pF pF |

ELECTRICAL CHARACTERISTICS (continued)

SWITCHING ON

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|-------------------------------|--|---|------|--------------|------|------------------|
| $t_{d(on)}$ t_r | Turn-on Time Rise Time | $V_{DD} = 30 \text{ V}$ $I_D = 6 \text{ A}$ $R_G = 4.7 \Omega$ $V_{GS} = 10 \text{ V}$ | | 10 35 | | ns ns |
| $(di/dt)_{on}$ | Turn-on Current Slope | $V_{DD} = 25 \text{ V}$ $I_D = 6 \text{ A}$ $R_G = 4.7 \Omega$ $V_{GS} = 10 \text{ V}$ | | 200 | | A/ μs |
| Q_g Q_{gs} Q_{gd} | Total Gate Charge Gate-Source Charge Gate-Drain Charge | $V_{DD} = 40 \text{ V}$ $I_D = 12 \text{ A}$ $V_{GS} = 10 \text{ V}$ | | 20 5 7 | | nC nC nC |

SWITCHING OFF

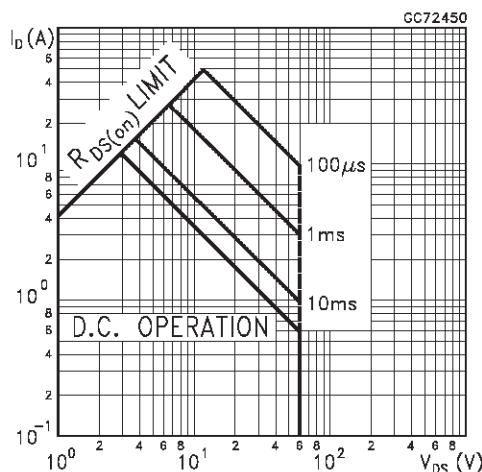
| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|---------------------------------|---|--|------|---------------|------|----------------|
| $t_{r(voff)}$ t_f t_c | Off-voltage Rise Time Fall Time Cross-over Time | $V_{DD} = 48 \text{ V}$ $I_D = 12 \text{ A}$ $R_G = 4.7 \Omega$ $V_{GS} = 10 \text{ V}$ | | 7 18 30 | | ns ns ns |

SOURCE DRAIN DIODE

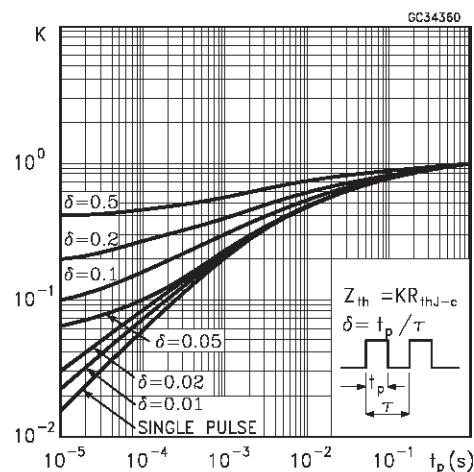
| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|-----------------------------------|--|--|------|-------------------|--------|--------------------------|
| I_{SD} $I_{SDM}(\bullet)$ | Source-drain Current Source-drain Current (pulsed) | | | | 2 8 | A A |
| $V_{SD} (\ast)$ | Forward On Voltage | $I_{SD} = 2 \text{ A}$ $V_{GS} = 0$ | | | 1.5 | V |
| t_{rr} Q_{rr} I_{RRM} | Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current | $I_{SD} = 12 \text{ A}$ $di/dt = 100 \text{ A}/\mu\text{s}$ $V_{DD} = 30 \text{ V}$ $T_j = 150^\circ\text{C}$ | | 65 0.18 5.5 | | ns μC A |

(*) Pulsed: Pulse duration = 300 μs , duty cycle 1.5 %(\bullet) Pulse width limited by safe operating area

Safe Operating Area

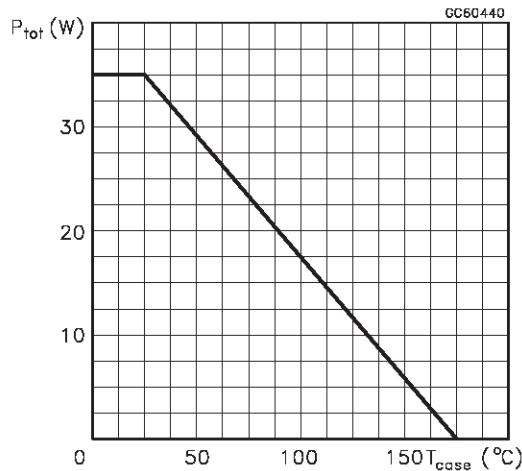


Thermal Impedance

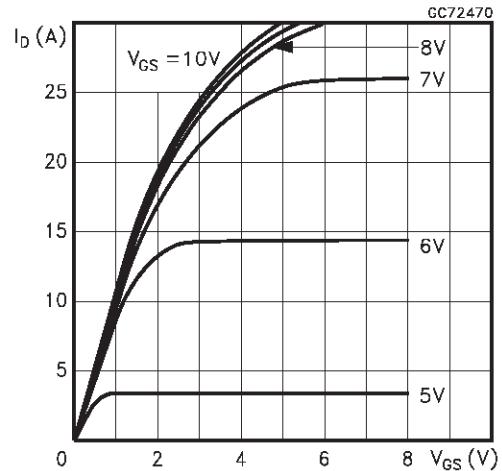


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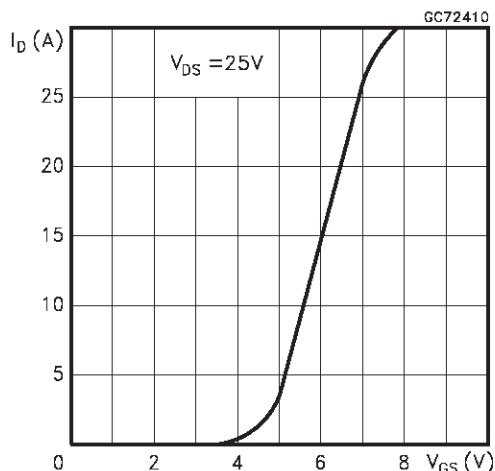
Derating Curve



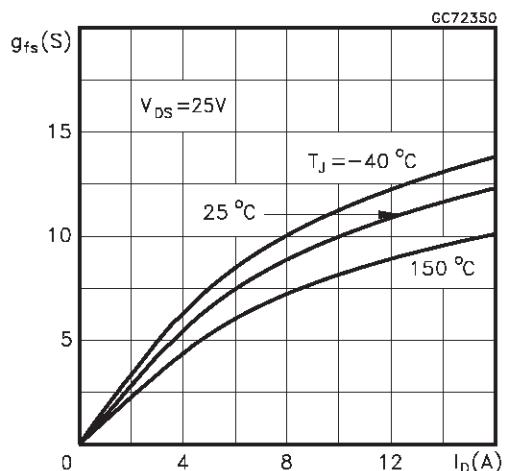
Output Characteristics



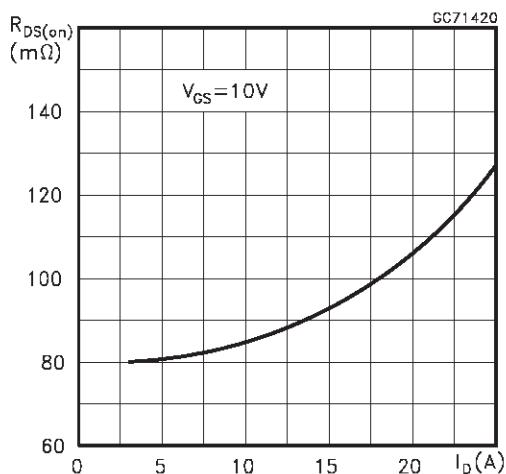
Transfer Characteristics



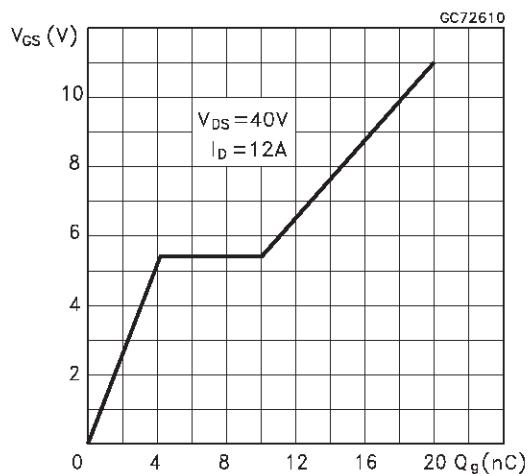
Transconductance



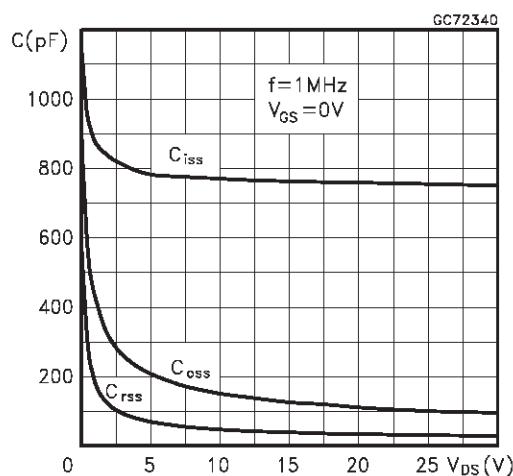
Static Drain-source On Resistance



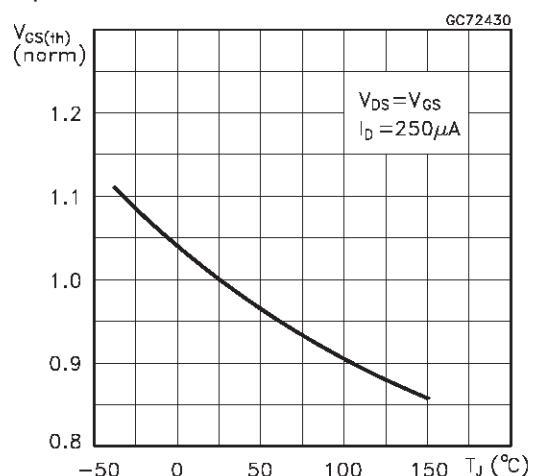
Gate Charge vs Gate-source Voltage



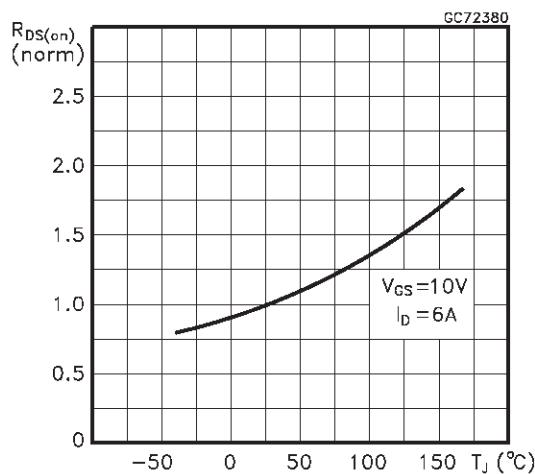
Capacitance Variations



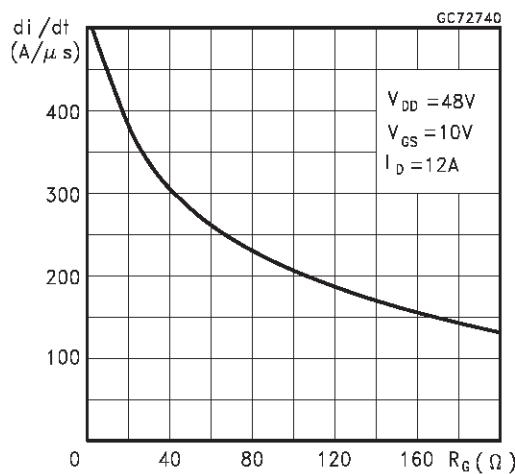
Normalized Gate Threshold Voltage vs Temperature



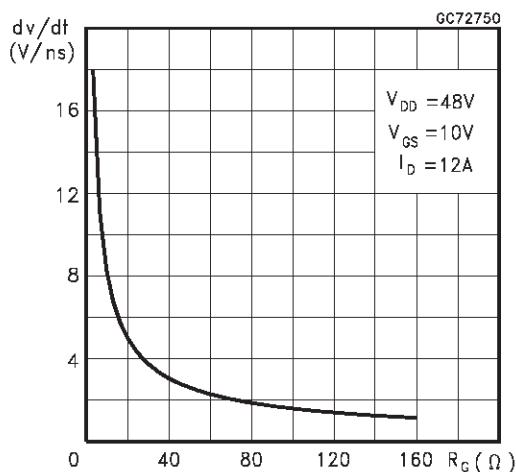
Normalized On Resistance vs Temperature



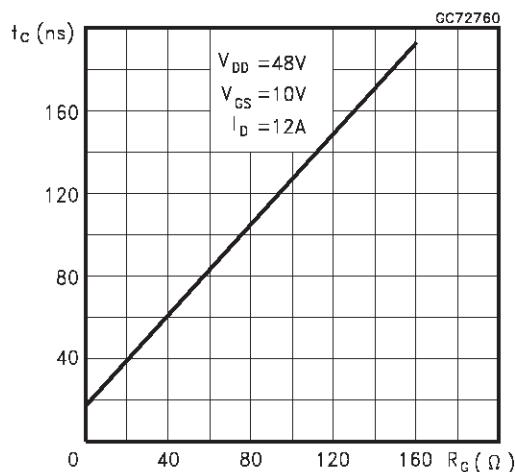
Turn-on Current Slope



Turn-off Drain-source Voltage Slope

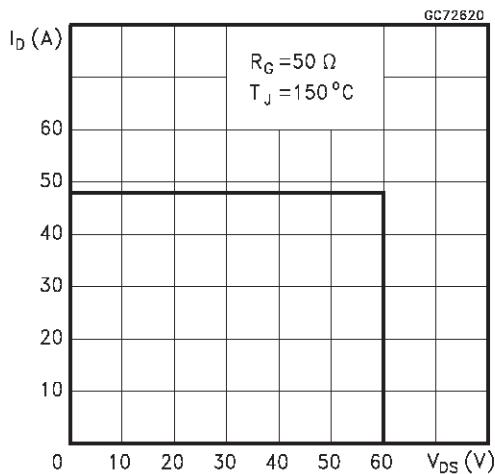


Cross-over Time

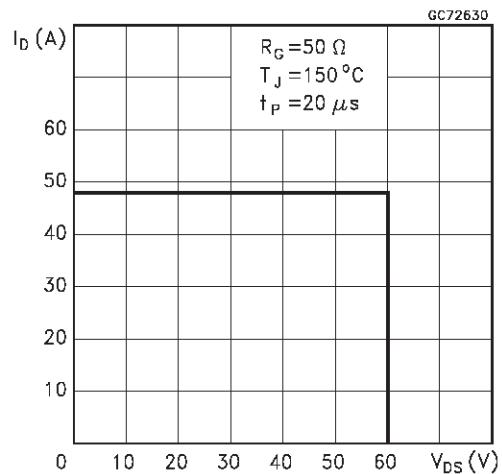


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Switching Safe Operating Area



Accidental Overload Area



Source-drain Diode Forward Characteristics

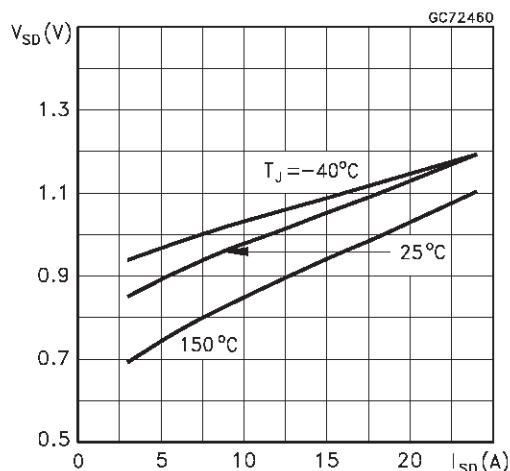


Fig. 1: Unclamped Inductive Load Test Circuit

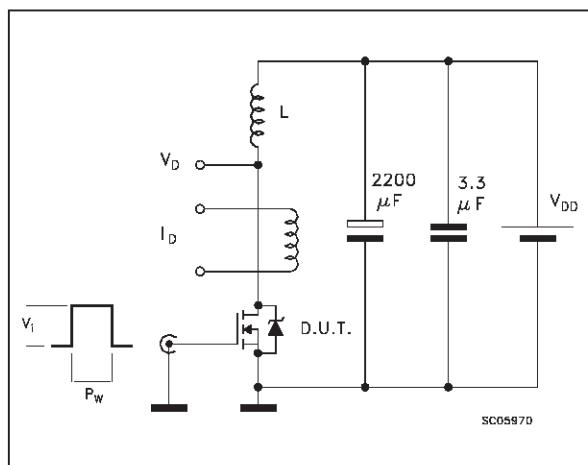


Fig. 2: Unclamped Inductive Waveform

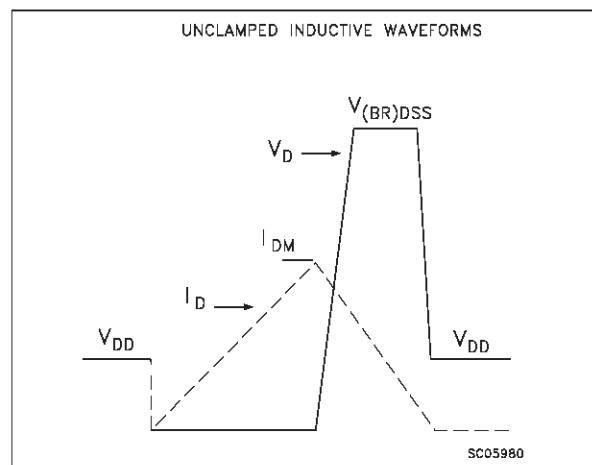


Fig. 3: Switching Times Test Circuits For Resistive Load

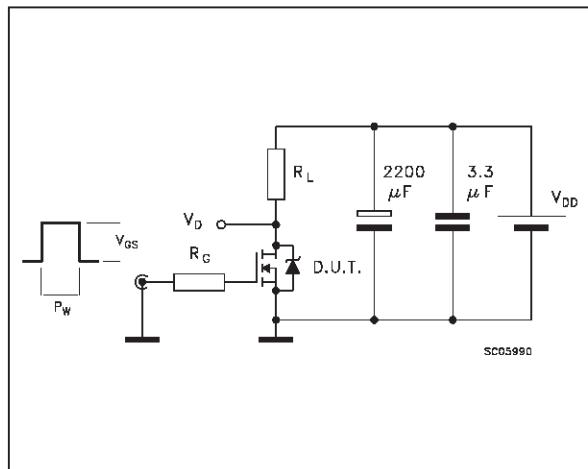


Fig. 4: Gate Charge test Circuit

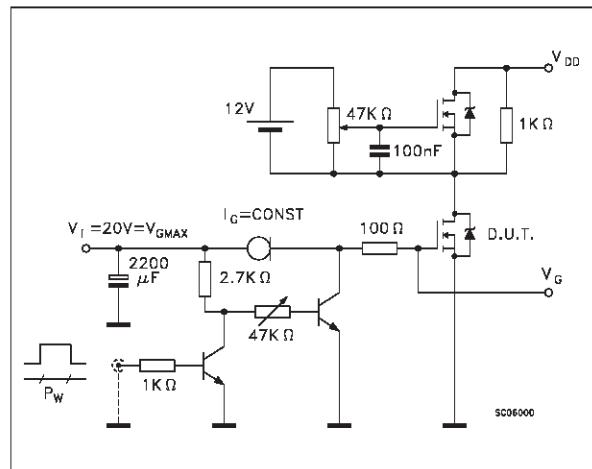
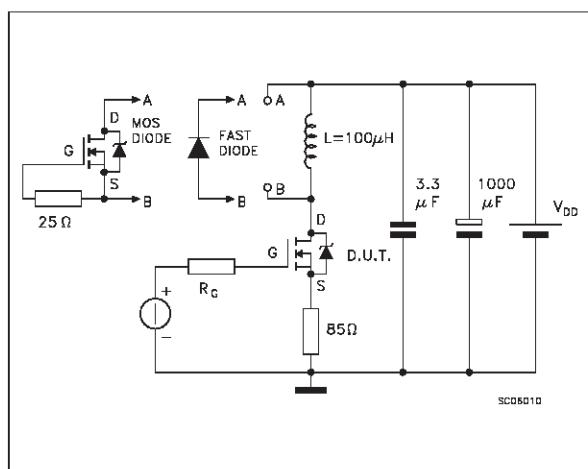
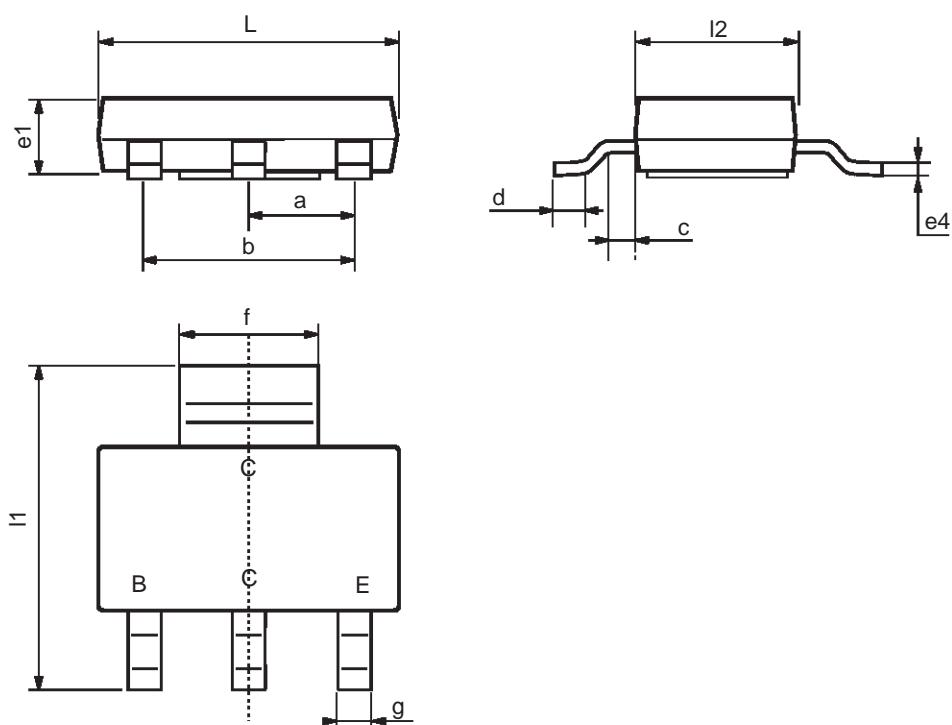


Fig. 5: Test Circuit For Inductive Load Switching And Diode Recovery Times



SOT-223 MECHANICAL DATA

| DIM. | mm | | | mils | | |
|------|------|------|------|-------|-------|-------|
| | MIN. | TYP. | MAX. | MIN. | TYP. | MAX. |
| a | 2.27 | 2.3 | 2.33 | 89.4 | 90.6 | 91.7 |
| b | 4.57 | 4.6 | 4.63 | 179.9 | 181.1 | 182.3 |
| c | 0.2 | 0.4 | 0.6 | 7.9 | 15.7 | 23.6 |
| d | 0.63 | 0.65 | 0.67 | 24.8 | 25.6 | 26.4 |
| e1 | 1.5 | 1.6 | 1.7 | 59.1 | 63 | 66.9 |
| e4 | | | 0.32 | | | 12.6 |
| f | 2.9 | 3 | 3.1 | 114.2 | 118.1 | 122.1 |
| g | 0.67 | 0.7 | 0.73 | 26.4 | 27.6 | 28.7 |
| l1 | 6.7 | 7 | 7.3 | 263.8 | 275.6 | 287.4 |
| l2 | 3.5 | 3.5 | 3.7 | 137.8 | 137.8 | 145.7 |
| L | 6.3 | 6.5 | 6.7 | 248 | 255.9 | 263.8 |



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