

ST13003

HIGH VOLTAGE FAST-SWITCHING NPN POWER TRANSISTOR

- n MEDIUM VOLTAGE CAPABILITY
- **LOW SPREAD OF DYNAMIC PARAMETERS**
- MINIMUM LOT-TO-LOT SPREAD FOR RELIABLE OPERATION
- n VERY HIGH SWITCHING SPEED

APPLICATIONS:

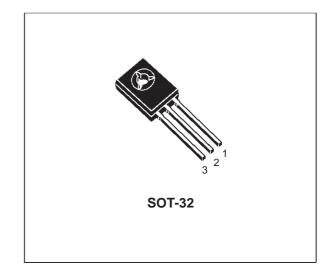
- ELECTRONIC BALLASTS FOR FLUORESCENT LIGHTING
- n SWITCH MODE POWER SUPPLIES

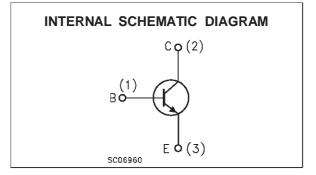
DESCRIPTION

The device is manufactured using high voltage Multi Epitaxial Planar technology for high switching speeds and medium voltage capability.

It uses a Cellular Emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.

The device is designed for use in lighting applications and low cost switch-mode power supplies.





ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
VCES	Collector-Emitter Voltage (V _{BE} = 0)	700	V
V _{CEO}	Collector-Emitter Voltage (I _B = 0)	400	V
V _{EBO}	Emitter-Base Voltage	BV _{EBO}	V
	$(I_{C} = 0, I_{B} = 0.75 \text{ A}, t_{p} < 10 \mu \text{s}, T_{j} < 150^{\circ} \text{C})$		
lc	Collector Current	1.5	A
Ісм	Collector Peak Current (t _p < 5 ms)	3	A
Ι _Β	Base Current	0.75	A
I _{BM}	Base Peak Current (t _p < 5 ms)	1.5	A
Ptot	Total Dissipation at $T_c = 25 \ ^{\circ}C$	40	W
T _{stg}	Storage Temperature	-65 to 150	°C
Tj	Max. Operating Junction Temperature	150	°C

THERMAL DATA

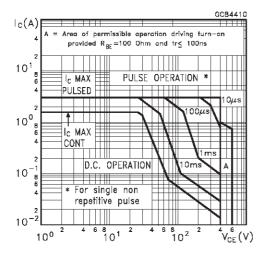
R _{thj-case}	Thermal Resistance Junction-case	Max	3.12	°C/W
R _{thj-amb}	Thermal Resistance Junction-ambient	Max	89	°C/W

ELECTRICAL CHARACTERISTICS (T_{case} = 25 °C unless otherwise specified)

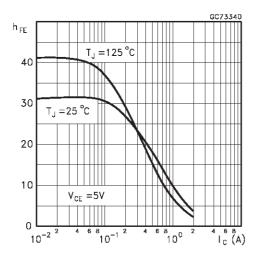
Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
ICEV	Collector Cut-off Current (V _{BE} = -1.5V)	V _{CE} = 700V V _{CE} = 700V	$T_{j} = 125^{\circ}C$			1 5	mA mA
BV _{EBO}	Emitter-Base Breakdown Voltage (I _C = 0)	I _E = 10 mA		9		18	V
$V_{CEO(sus)^*}$	Collector-Emitter Sustaining Voltage (I _B = 0)	I _C = 10 mA L = 25mH		400			V
V _{CE(sat)} *	Collector-Emitter Saturation Voltage	$I_{C} = 0.5 A$ $I_{C} = 1 A$ $I_{C} = 1.5 A$	$I_{B} = 0.1 A$ $I_{B} = 0.25 A$ $I_{B} = 0.5 A$			0.5 1 3	V V V
V _{BE(sat)*}	Base-Emitter Saturation Voltage	$I_{C} = 0.5 A$ $I_{C} = 1 A$	I _B = 0.1 A I _B = 0.25 A			1.0 1.2	V V
h _{FE}	DC Current Gain	$I_{C} = 0.5 A$ Group A Group B $I_{C} = 1 A$	$V_{CE} = 2 V$ $V_{CE} = 2 V$	8 15 5		20 35 25	
t _r ts t _f	RESISTIVE LOAD Rise Time Storage Time Fall Time	$I_{C} = 1 A$ $I_{B1} = 0.2 A$ $T_{p}= 25 \ \mu s$	V _{CC} = 125 V I _{B2} = -0.2 A			1.0 4.0 0.7	μs μs μs
t _s	INDUCTIVE LOAD Storage Time	$I_{C} = 1 A$ $V_{BE} = -5 V$ $V_{clamp} = 300 V$	I _{B1} = 0.2 A L = 50 mH		0.8		μs

* Pulsed: Pulse duration = 300µs, duty cycle = 1.5 % Note : Product is pre-selected in DC current gain (GROUP A and GROUP B). STMicroelectronics reserves the right to ship either groups according to production availability. Please contact your nearest STMicroelectronics sales office for delivery details.

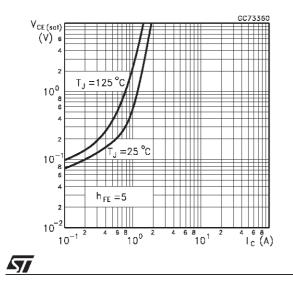
Safe Operating Areas



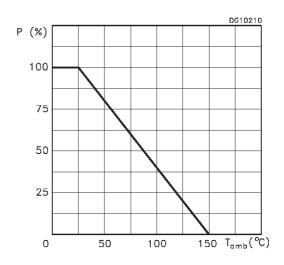
DC Current Gain



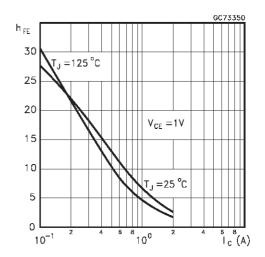
Collector Emitter Saturation Voltage

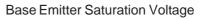


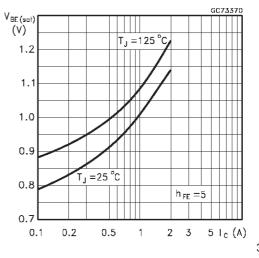
Derating Curve



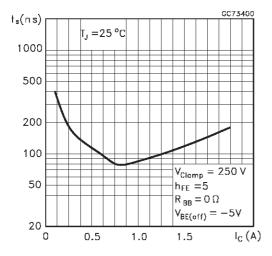




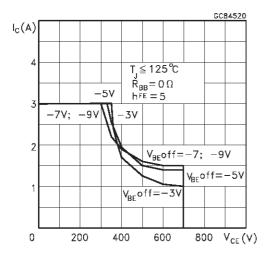




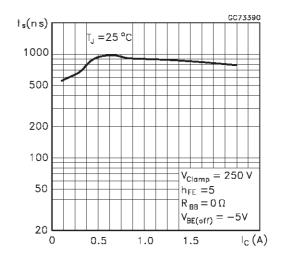
Inductive Fall Time



Reverse Biased SOA



Inductive Storage Time



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Figure 1: Inductive Load Switching Test Circuits.

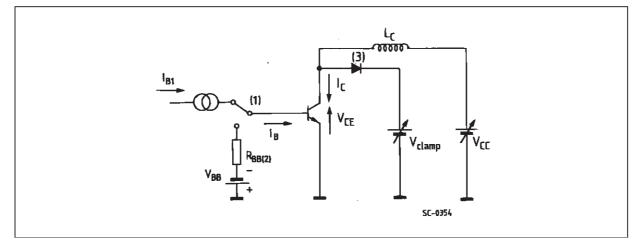
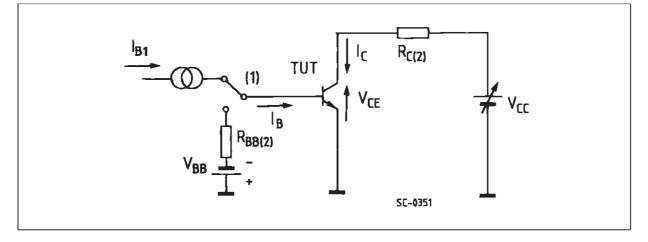
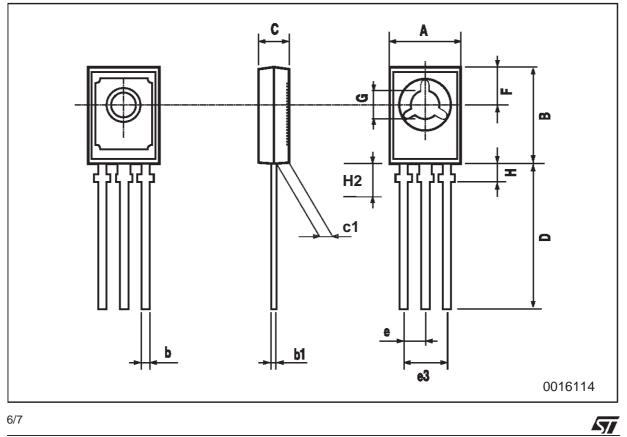


Figure 2: Resistive Load Switching Test Circuits.



DIM.	mm			inch			
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
А	7.4		7.8	0.291		0.307	
В	10.5		10.8	0.413		0.445	
b	0.7		0.9	0.028		0.035	
b1	0.49		0.75	0.019		0.030	
С	2.4		2.7	0.040		0.106	
c1	1.0		1.3	0.039		0.050	
D	15.4		16.0	0.606		0.629	
е		2.2			0.087		
e3	4.15		4.65	0.163		0.183	
F		3.8			0.150		
G	3		3.2	0.118		0.126	
Н			2.54			0.100	





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