

BULK128D

HIGH VOLTAGE FAST-SWITCHING NPN POWER TRANSISTOR

- SGS-THOMSON PREFERRED SALESTYPE
- ORDERING CODES: BULK128D-A AND BULK128D-B
- NPN TRANSISTOR
- HIGH VOLTAGE CAPABILITY
- LOW SPREAD OF DYNAMIC PARAMETERS
- MINIMUM LOT-TO-LOT SPREAD FOR RELIABLE OPERATION
- VERY HIGH SWITCHING SPEED

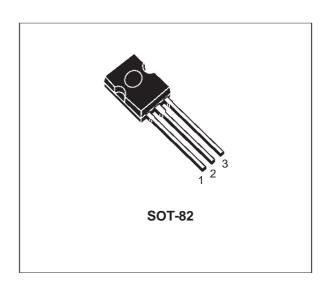
APPLICATIONS:

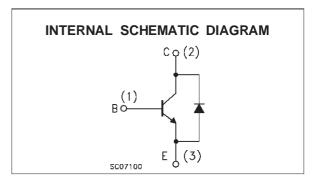
- ELECTRONIC BALLASTS FOR FLUORESCENT LIGHTING
- FLYBACK AND FORWARD SINGLE TRANSISTOR LOW POWER CONVERTERS



The device is manufactured using high voltage Multi Epitaxial Planar technology for high switching speeds and medium voltage capability. It uses a Cellular Emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.

The device is designed for use in lighting applications and low cost switch-mode power supplies.





ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{CES}	Collector-Emitter Voltage (V _{BE} = 0)	700	V
VCEO	Collector-Emitter Voltage (I _B = 0)	400	V
V_{EBO}	Emitter-Base Voltage ($I_C = 0$)	9	V
Ic	Collector Current	4	Α
I _{CM}	Collector Peak Current (tp < 5 ms)	8	Α
lΒ	Base Current	2	Α
I _{BM}	Base Peak Current (t _p < 5 ms)	4	Α
P _{tot}	Total Dissipation at T _c = 25 °C	55	W
T _{stg}	Storage Temperature	-65 to 150	°C
Tj	Max. Operating Junction Temperature	150	°C

December 1997 1/7

THERMAL DATA

Rtl	hj-case	Thermal	Resistance	Junction-Case	Max	2.27	°C/W
Rt	hj-amb	Thermal	Resistance	Junction-Ambient	Max	80	°C/W

ELECTRICAL CHARACTERISTICS (T_{case} = 25 °C unless otherwise specified)

Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
I _{CES}	Collector Cut-off Current (V _{BE} = -1.5 V)	V _{CE} = 700 V V _{CE} = 700 V	T _j = 125 °C			100 500	μA μA
I _{CEO}	Collector-Emitter Leakage Current (I _B = 0)	V _{CE} = 400 V				250	μΑ
V _{EBO}	Emitter-Base Voltage	I _E = 10 mA		9			V
V _{CEO(sus)}	Collector-Emitter Sustaining Voltage	I _C = 100 mA	L = 25 mH	400			V
VCE(sat)*	Collector-Emitter Saturation Voltage	I _C = 0.5 A I _C = 1 A I _C = 2.5 A	$I_B = 0.1 A$ $I_B = 0.2 A$ $I_B = 0.5 A$			0.7 1.0 1.5	V V V
V _{BE(sat)*}	Base-Emitter Saturation Voltage	Ic = 0.5 A I _C = 1 A I _C = 2.5 A	$I_B = 0.1 A$ $I_B = 0.2 A$ $I_B = 0.5 A$			1.1 1.2 1.3	V V V
h _{FE} *	DC Current Gain	I _C = 10 mA I _C = 2 A	$V_{CE} = 5 V$ $V_{CE} = 5 V$	10 8			
Vf	Forward Voltage Drop	I _f = 2 A				2.5	V
t _s	RESISTIVE LOAD Storage Time BULK128D-A BULK128D-B Fall Time	$V_{CC} = 250 \text{ V}$ $I_{B1} = 0.4 \text{ A}$ $T_p = 30 \mu\text{s}$ (see fig. 2)	I _C = 2 A I _{B2} = -0.4 A	1.7 2.0	0.2	2.5 2.9	μs μs μs
t _s	INDUCTIVE LOAD Storage Time Fall Time	$V_{CI} = 200 \text{ V}$ $I_{B1} = 0.4 \text{ A}$ $R_{BB} = 0 \Omega$ (see fig. 1)	$I_C = 2 \text{ A}$ $V_{BE(off)} = -5 \text{ V}$ $L = 200 \mu\text{H}$		0.6		μs μs

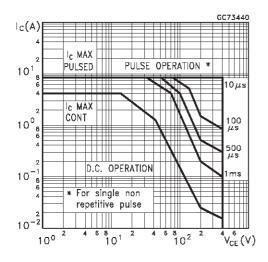
^{*} Pulsed: Pulse duration = 300 μs, duty cycle 1.5 %

Note: Ordering codes: - BULK128D-A

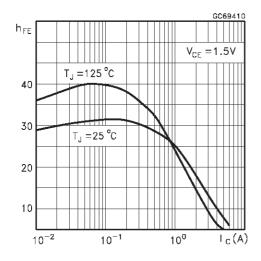
Please contact your nearest ST Microelectronics sales office for delivery details.

⁻ BULK128D-B.

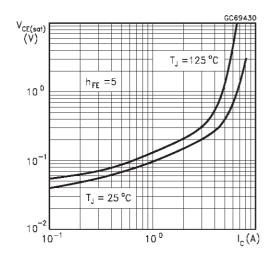
Safe Operating Areas



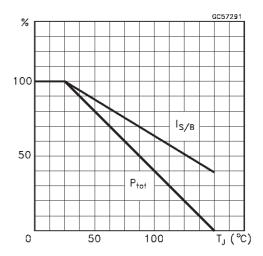
DC Current Gain



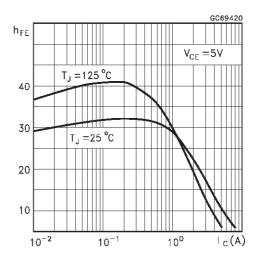
Collector Emitter Saturation Voltage



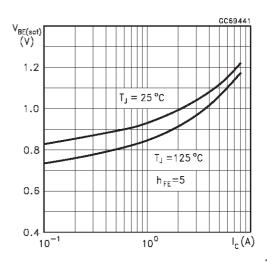
Derating Curve



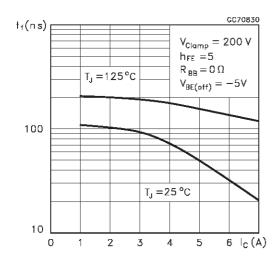
DC Current Gain



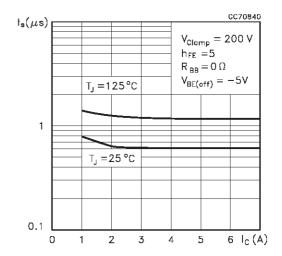
Base Emitter Saturation Voltage



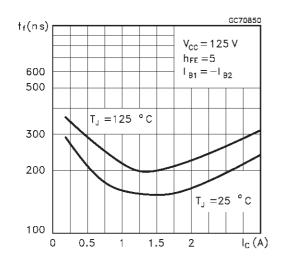
Inductive Fall Time



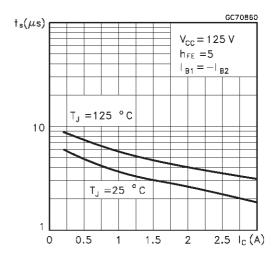
Inductive Storage Time



Resistive Load Fall Time



Resistive Load Storage Time



Reverse Biased SOA

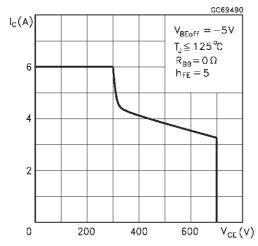


Figure 1: Inductive Load Switching Test Circuit.

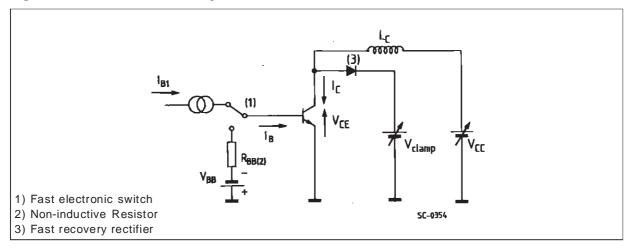
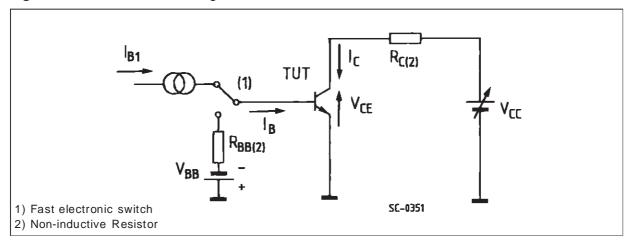
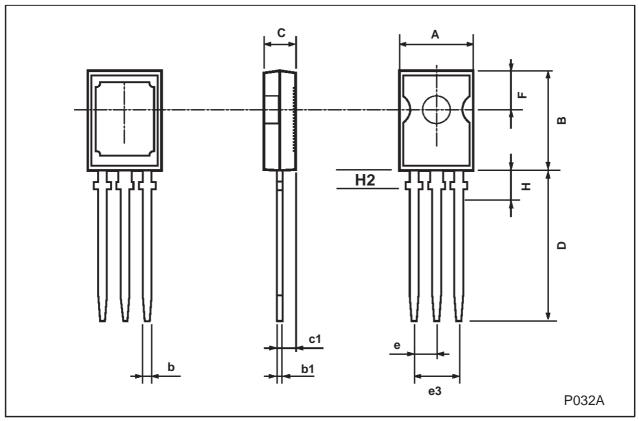


Figure 2: Resistive Load Switching Test Circuit.



SOT-82 MECHANICAL DATA

DIM.		mm		inch			
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
А	7.4		7.8	0.291		0.307	
В	10.5		10.8	0.413		0.444	
b	0.7		0.9	0.028		0.035	
b1	0.49		0.75	0.019		0.030	
С	2.4		2.7	0.04		0.106	
c1	1.0		1.3	0.039		0.05	
D	15.4		16	0.606		0.629	
е		2.2			0.087		
e3	4.15		4.65	0.163		0.183	
F		3.8			0.150		
Н			2.54		0.100		
H2		2.15			0.084		



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