

N - CHANNEL ENHANCEMENT MODE POWER MOS TRANSISTOR

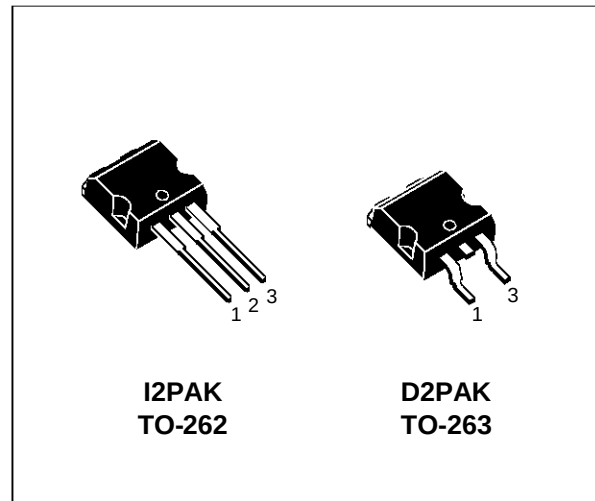
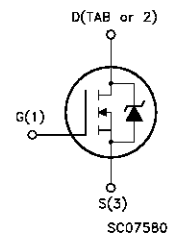
PRELIMINARY DATA

TYPE	V _{DSS}	R _{DS(on)}	I _D
STB15N25	250 V	< 0.25 Ω	15 A

- TYPICAL R_{DS(on)} = 0.2 Ω
- AVALANCHE RUGGED TECHNOLOGY
- 100% AVALANCHE TESTED
- REPETITIVE AVALANCHE DATA AT 100°C
- LOW INPUT CAPACITANCE
- LOW GATE CHARGE
- LOW LEAKAGE CURRENT
- APPLICATION ORIENTED CHARACTERIZATION
- THROUGH-HOLE I2PAK (TO-262) POWER PACKAGE IN TUBE (SUFFIX "-1")
- SURFACE-MOUNTING D2PAK (TO-263) POWER PACKAGE IN TUBE (NO SUFFIX) OR IN TAPE & REEL (SUFFIX "T4")

APPLICATIONS

- HIGH CURRENT, HIGH SPEED SWITCHING
- SWITCH MODE POWER SUPPLY (SMPS)
- CONSUMER AND INDUSTRIAL LIGHTING
- DC-AC CONVERTER FOR WELDING EQUIPMENT AND UNINTERRUPTABLE POWER SUPPLY (UPS)


INTERNAL SCHEMATIC DIAGRAM

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{DS}	Drain-source Voltage (V _{GS} = 0)	250	V
V _{DGR}	Drain- gate Voltage (R _{GS} = 20 k Ω)	250	V
V _{GS}	Gate-source Voltage	± 20	V
I _D	Drain Current (continuous) at T _c = 25 °C	15	A
I _D	Drain Current (continuous) at T _c = 100 °C	10	A
I _{DM} (•)	Drain Current (pulsed)	60	A
P _{tot}	Total Dissipation at T _c = 25 °C	125	W
	Derating Factor	1	W/°C
T _{stg}	Storage Temperature	-65 to 150	°C
T _j	Max. Operating Junction Temperature	150	°C

(•) Pulse width limited by safe operating area

THERMAL DATA

$R_{thj-case}$	Thermal Resistance Junction-case	Max	1	$^{\circ}C/W$
$R_{thj-amb}$	Thermal Resistance Junction-ambient	Max	62.5	$^{\circ}C/W$
$R_{thj-amb}$	Thermal Resistance Case-sink	Typ	0.5	$^{\circ}C/W$
T_I	Maximum Lead Temperature For Soldering Purpose		300	$^{\circ}C$

AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I_{AR}	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T_J max, $\delta < 1\%$)	15	A
E_{AS}	Single Pulse Avalanche Energy (starting $T_J = 25^{\circ}C$, $I_D = I_{AR}$, $V_{DD} = 25 V$)	40	mJ
E_{AR}	Repetitive Avalanche Energy (pulse width limited by T_J max, $\delta < 1\%$)	10	mJ
I_{AR}	Avalanche Current, Repetitive or Not-Repetitive ($T_c = 100^{\circ}C$, pulse width limited by T_J max, $\delta < 1\%$)	10	A

ELECTRICAL CHARACTERISTICS ($T_{case} = 25^{\circ}C$ unless otherwise specified)

OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{(BR)DSS}$	Drain-source Breakdown Voltage	$I_D = 250 \mu A$ $V_{GS} = 0$	250			V
I_{DSS}	Zero Gate Voltage Drain Current ($V_{GS} = 0$)	$V_{DS} = \text{Max Rating}$ $V_{DS} = \text{Max Rating} \times 0.8$ $T_c = 125^{\circ}C$			250 1000	μA μA
I_{GSS}	Gate-body Leakage Current ($V_{DS} = 0$)	$V_{GS} = \pm 20 V$			± 100	nA

ON (*)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$ $I_D = 250 \mu A$	2	3	4	V
$R_{DS(on)}$	Static Drain-source On Resistance	$V_{GS} = 10 V$ $I_D = 7.5 A$ $V_{GS} = 10 V$ $I_D = 7.5 A$ $T_c = 100^{\circ}C$			0.25 0.5	Ω Ω
$I_{D(on)}$	On State Drain Current	$V_{DS} > I_{D(on)} \times R_{DS(on)max}$ $V_{GS} = 10 V$	15			A

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$g_{fs} (*)$	Forward Transconductance	$V_{DS} > I_{D(on)} \times R_{DS(on)max}$ $I_D = 7.5 A$	5			S
C_{iss}	Input Capacitance	$V_{DS} = 25 V$ $f = 1 MHz$ $V_{GS} = 0$		1600		pF
C_{oss}	Output Capacitance			270		pF
C_{rss}	Reverse Transfer Capacitance			50		pF

ELECTRICAL CHARACTERISTICS (continued)**SWITCHING ON**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on Time	$V_{DD} = 100\text{ V}$		20		ns
t_r	Rise Time	$R_G = 9.1\ \Omega$ $I_D = 15\text{ A}$ $V_{GS} = 10\text{ V}$		75		ns
$(di/dt)_{on}$	Turn-on Current Slope	$V_{DD} = 160\text{ V}$ $I_D = 15\text{ A}$ $R_G = 9.1\ \Omega$ $V_{GS} = 10\text{ V}$		470		A/ μ s
Q_g	Total Gate Charge	$V_{DD} = 160\text{ V}$ $I_D = 15\text{ A}$ $V_{GS} = 10\text{ V}$		60		nC
Q_{gs}	Gate-Source Charge			10		nC
Q_{gd}	Gate-Drain Charge			25		nC

SWITCHING OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{r(Voff)}$	Off-voltage Rise Time	$V_{DD} = 160\text{ V}$ $I_D = 15\text{ A}$		35		ns
t_f	Fall Time	$R_G = 9.1\ \Omega$ $V_{GS} = 10\text{ V}$		30		ns
t_c	Cross-over Time			70		ns

SOURCE DRAIN DIODE

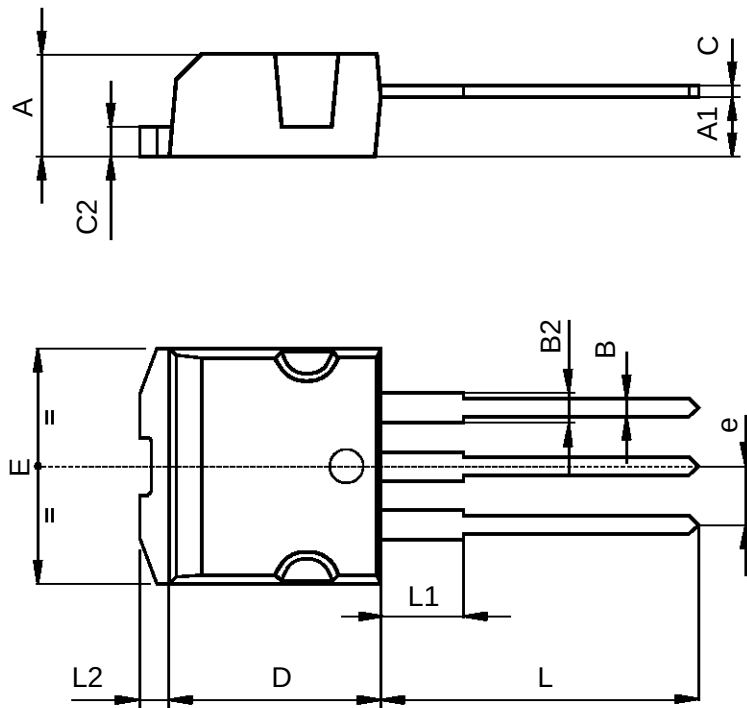
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{SD}	Source-drain Current				15	A
$I_{SDM}(\bullet)$	Source-drain Current (pulsed)				60	A
$V_{SD} (*)$	Forward On Voltage	$I_{SD} = 15\text{ A}$ $V_{GS} = 0$			1.5	V
t_{rr}	Reverse Recovery Time	$I_{SD} = 15\text{ A}$ $di/dt = 100\text{ A}/\mu\text{s}$ $V_R = 100\text{ V}$ $T_J = 150\text{ }^\circ\text{C}$		290		ns
Q_{rr}	Reverse Recovery Charge			3		μC
I_{RRM}	Reverse Recovery Current			21		A

(*) Pulsed: Pulse duration = 300 μ s, duty cycle 1.5 %

(\bullet) Pulse width limited by safe operating area

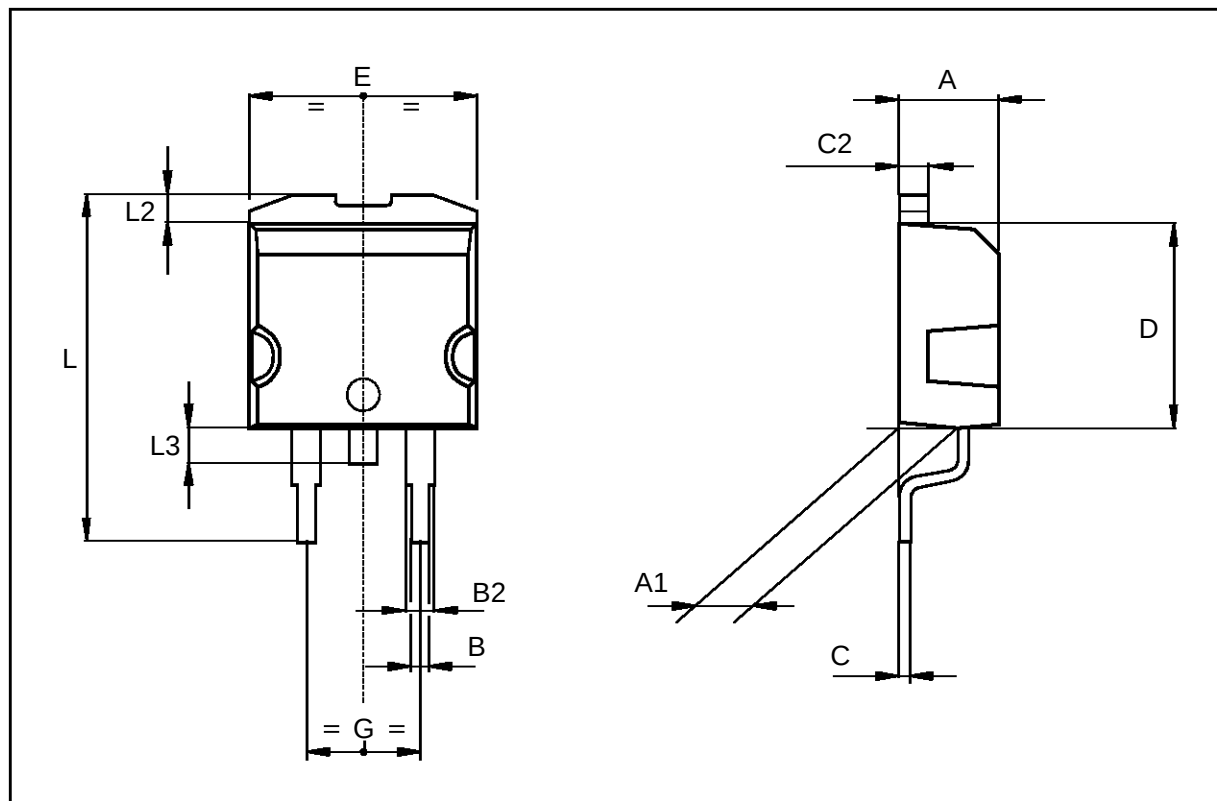
TO-262 (I2PAK) MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.3		4.6	0.169		0.181
A1	2.49		2.69	0.098		0.106
B	0.7		0.93	0.027		0.036
B1	1.2		1.38	0.047		0.054
B2	1.25		1.4	0.049		0.055
C	0.45		0.6	0.017		0.023
C2	1.21		1.36	0.047		0.053
D	9		9.35	0.354		0.368
e	2.44		2.64	0.096		0.104
E	10		10.28	0.393		0.404
L	13.2		13.5	0.519		0.531
L1	3.48		3.78	0.137		0.149
L2	1.27		1.37	0.050		0.054



TO-263 (D2PAK) MECHANICAL DATA

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A1	2.49		2.69	0.098		0.106
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B2	1.25		1.4	0.049		0.055
C	0.45		0.6	0.017		0.023
C2	1.21		1.36	0.047		0.053
D	9		9.35	0.354		0.368
E	10		10.28	0.393		0.404
G	4.88		5.28	0.192		0.208
L	15		15.85	0.590		0.624
L2	1.27		1.37	0.050		0.054
L3	1.4		1.75	0.055		0.068



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