

**STW20NB50****N - CHANNEL 500V - 0.22Ω - 20A - TO-247
PowerMESH™ MOSFET**

| TYPE | V _{DSS} | R _{D(on)} | I _D |
|-----------|------------------|--------------------|----------------|
| STW20NB50 | 500 V | < 0.25 Ω | 20 A |

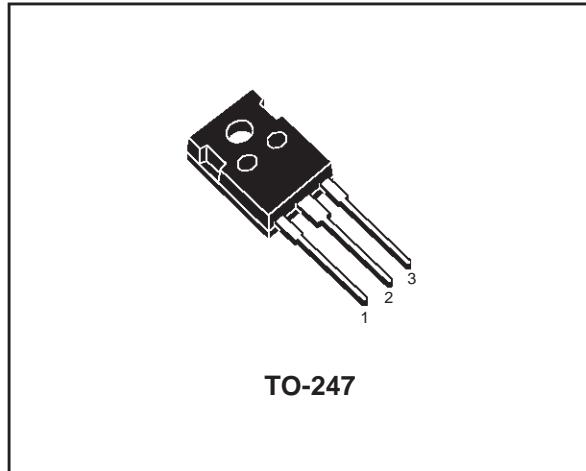
- TYPICAL R_{D(on)} = 0.22 Ω
- EXTREMELY HIGH dv/dt CAPABILITY
- ± 30V GATE TO SOURCE VOLTAGE RATING
- 100% AVALANCHE TESTED
- REPETITIVE AVALANCHE DATA AT 100°C
- VERY LOW INTRINSIC CAPACITANCES
- GATE CHARGE MINIMIZED

DESCRIPTION

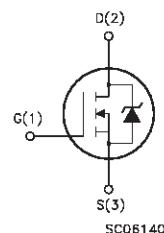
Using the latest high voltage technology, STMicroelectronics has designed an advanced family of power Mosfets with outstanding performances. The new patent pending strip layout coupled with the Company's proprietary edge termination structure, gives the lowest R_{D(on)} per area, exceptional avalanche and dv/dt capabilities and unrivalled gate charge and switching characteristics.

APPLICATIONS

- HIGH CURRENT, HIGH SPEED SWITCHING
- SWITCH MODE POWER SUPPLIES (SMPS)
- DC-AC CONVERTERS FOR WELDING EQUIPMENT AND UNINTERRUPTIBLE POWER SUPPLIES AND MOTOR DRIVE



TO-247

INTERNAL SCHEMATIC DIAGRAM**ABSOLUTE MAXIMUM RATINGS**

| Symbol | Parameter | Value | Unit |
|--------------------|---|------------|------|
| V _{DS} | Drain-source Voltage (V _{GS} = 0) | 500 | V |
| V _{DGR} | Drain-gate Voltage (R _{GS} = 20 kΩ) | 500 | V |
| V _{GS} | Gate-source Voltage | ± 30 | V |
| I _D | Drain Current (continuous) at T _c = 25 °C | 20 | A |
| I _D | Drain Current (continuous) at T _c = 100 °C | 12.7 | A |
| I _{DM(•)} | Drain Current (pulsed) | 80 | A |
| P _{tot} | Total Dissipation at T _c = 25 °C | 250 | W |
| | Derating Factor | 2 | W/°C |
| dv/dt(1) | Peak Diode Recovery voltage slope | 4 | V/ns |
| T _{stg} | Storage Temperature | -65 to 150 | °C |
| T _j | Max. Operating Junction Temperature | 150 | °C |

(•) Pulse width limited by safe operating area

(1) I_{SD} ≤ 20A, di/dt ≤ 200 A/μs, V_{DD} ≤ V_{(BR)DSS}, T_j ≤ T_{JMAX}

STW20NB50

THERMAL DATA

| | | | | |
|-----------------------|--|-----|-----|------|
| R _{thj-case} | Thermal Resistance Junction-case | Max | 0.5 | °C/W |
| R _{thj-amb} | Thermal Resistance Junction-ambient | Max | 30 | °C/W |
| R _{thc-sink} | Thermal Resistance Case-sink | Typ | 0.1 | °C/W |
| T _I | Maximum Lead Temperature For Soldering Purpose | | 300 | °C |

AVALANCHE CHARACTERISTICS

| Symbol | Parameter | Max Value | Unit |
|-----------------|---|-----------|------|
| I _{AR} | Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T _j max, δ < 1%) | 20 | A |
| E _{AS} | Single Pulse Avalanche Energy (starting T _j = 25 °C, I _D = I _{AR} , V _{DD} = 50 V) | 1000 | mJ |

ELECTRICAL CHARACTERISTICS (T_{case} = 25 °C unless otherwise specified)

OFF

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|----------------------|---|--|------|------|-----------|----------|
| V _{(BR)DSS} | Drain-source Breakdown Voltage | I _D = 250 μA V _{GS} = 0 | 500 | | | V |
| I _{DSS} | Zero Gate Voltage Drain Current (V _{GS} = 0) | V _{DS} = Max Rating V _{DS} = Max Rating T _c = 125 °C | | | 10 100 | μA μA |
| I _{GSS} | Gate-body Leakage Current (V _{DS} = 0) | V _{GS} = ± 30 V | | | ± 100 | nA |

ON (*)

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|---------------------|-----------------------------------|--|------|------|------|------|
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} = V _{GS} I _D = 250 μA | 3 | 4 | 5 | V |
| R _{D(on)} | Static Drain-source On Resistance | V _{GS} = 10 V I _D = 10 A | | 0.22 | 0.25 | Ω |
| I _{D(on)} | On State Drain Current | V _{DS} > I _{D(on)} × R _{D(on)max} V _{GS} = 10 V | 20 | | | A |

DYNAMIC

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|--|---|--|------|-------------------|-------------------|----------------|
| g _{fs} (*) | Forward Transconductance | V _{DS} > I _{D(on)} × R _{D(on)max} I _D = 10 A | 9 | 13.5 | | S |
| C _{iss} C _{oss} C _{rss} | Input Capacitance Output Capacitance Reverse Transfer Capacitance | V _{DS} = 25 V f = 1 MHz V _{GS} = 0 | | 3600 460 55 | 4700 600 75 | pF pF pF |

ELECTRICAL CHARACTERISTICS (continued)

SWITCHING ON

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|-------------------------------|--|---|------|----------------|----------|----------------|
| $t_{d(on)}$ t_r | Turn-on Time Rise Time | $V_{DD} = 250 \text{ V}$ $I_D = 10 \text{ A}$ $R_G = 4.7 \Omega$ $V_{GS} = 10 \text{ V}$ (see test circuit, figure 3) | | 32 15 | 43 21 | ns ns |
| Q_g Q_{gs} Q_{gd} | Total Gate Charge Gate-Source Charge Gate-Drain Charge | $V_{DD} = 400 \text{ V}$ $I_D = 20 \text{ A}$ $V_{GS} = 10 \text{ V}$ | | 85 21 37 | 110 | nC nC nC |

SWITCHING OFF

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|------------------------------------|---|---|------|----------------|----------------|----------------|
| $t_{r(V_{off})}$ t_f t_c | Off-voltage Rise Time Fall Time Cross-over Time | $V_{DD} = 400 \text{ V}$ $I_D = 20 \text{ A}$ $R_G = 4.7 \Omega$ $V_{GS} = 10 \text{ V}$ (see test circuit, figure 5) | | 20 25 47 | 27 33 62 | ns ns ns |

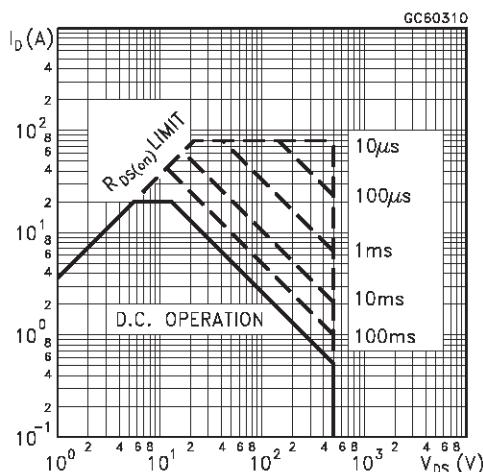
SOURCE DRAIN DIODE

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|-----------------------------------|---|--|------|----------------|----------|--------------------------|
| I_{SD} $I_{SDM}(\bullet)$ | Source-drain Current Source-drain Current (pulsed) | | | | 20 80 | A A |
| $V_{SD} (\ast)$ | Forward On Voltage | $I_{SD} = 20 \text{ A}$ $V_{GS} = 0$ | | | 1.6 | V |
| t_{rr} Q_{rr} I_{RRM} | Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current | $I_{SD} = 20 \text{ A}$ $di/dt = 100 \text{ A}/\mu\text{s}$ $V_{DD} = 100 \text{ V}$ $T_j = 150 \text{ }^\circ\text{C}$ (see test circuit, figure 5) | | 700 9 25 | | ns μC A |

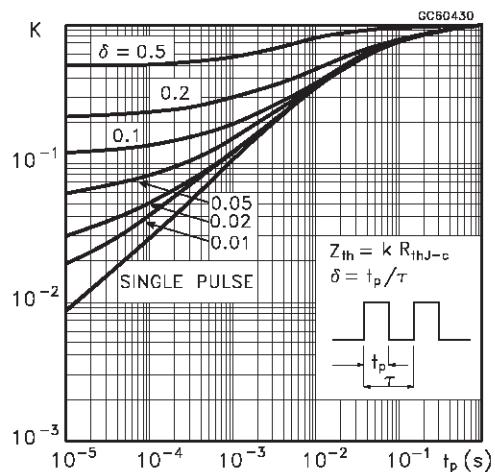
(*) Pulsed: Pulse duration = 300 μs , duty cycle 1.5 %

(•) Pulse width limited by safe operating area

Safe Operating Area

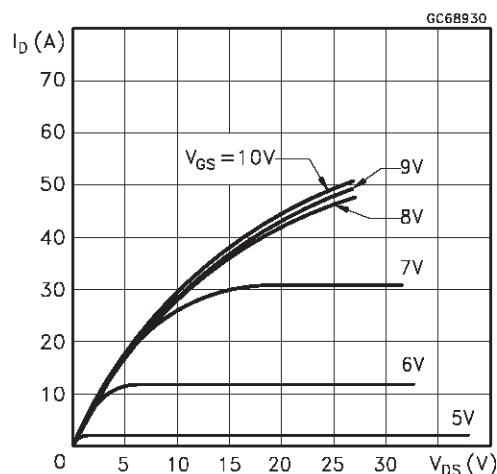


Thermal Impedance

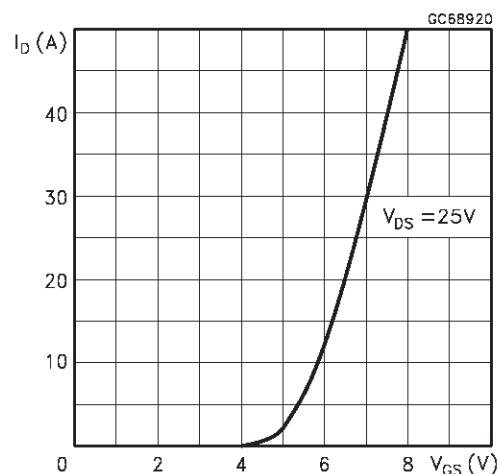


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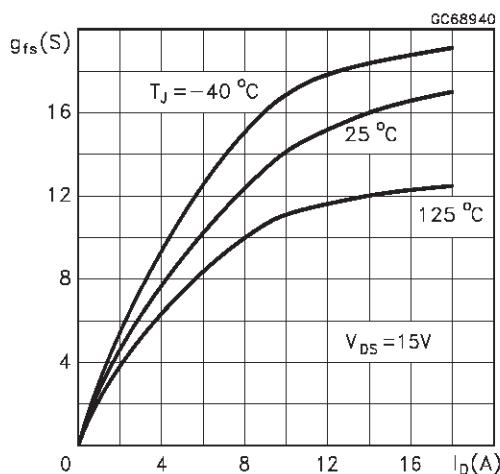
Output Characteristics



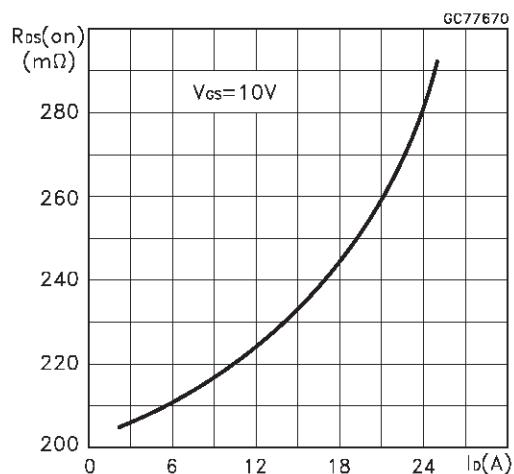
Transfer Characteristics



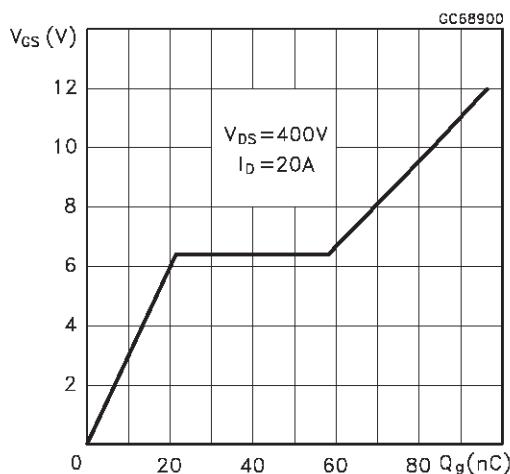
Transconductance



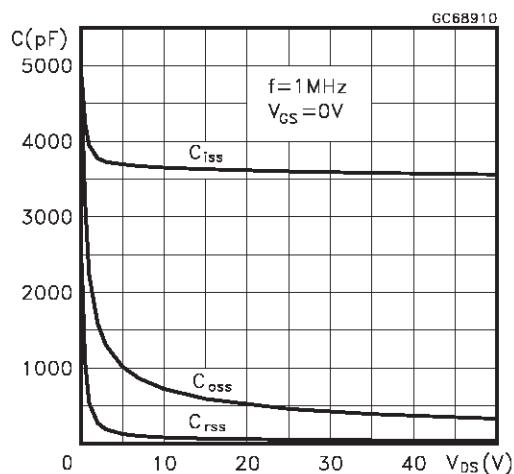
Static Drain-source On Resistance



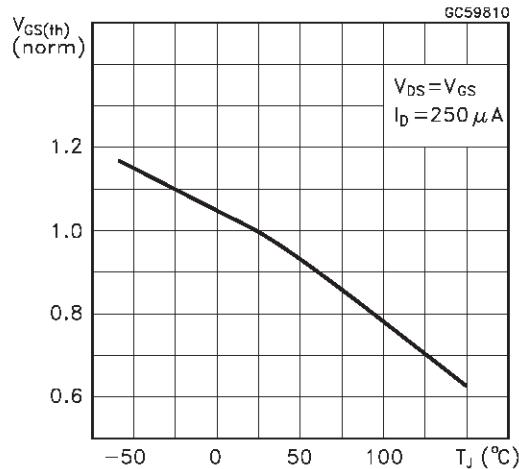
Gate Charge vs Gate-source Voltage



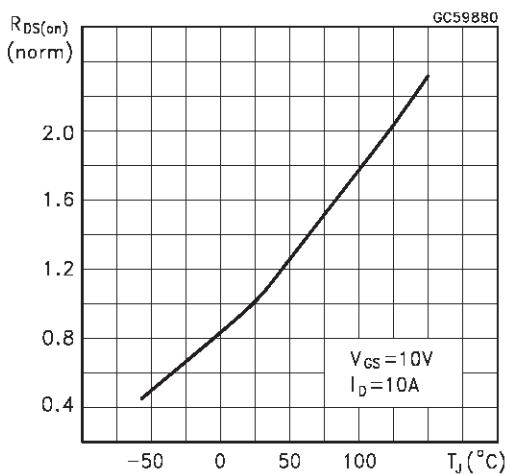
Capacitance Variations



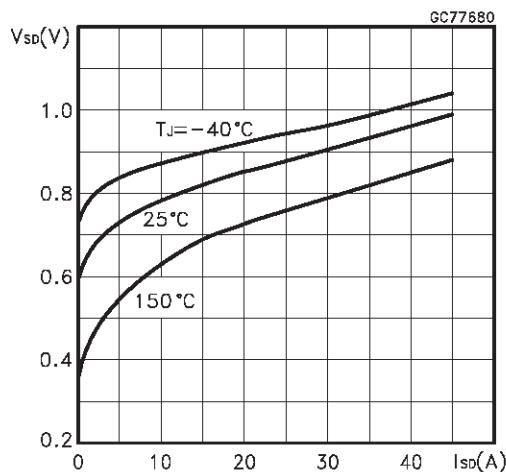
Normalized Gate Threshold Voltage vs Temperature



Normalized On Resistance vs Temperature



Source-drain Diode Forward Characteristics



STW20NB50

Fig. 1: Unclamped Inductive Load Test Circuit



Fig. 2: Unclamped Inductive Waveform

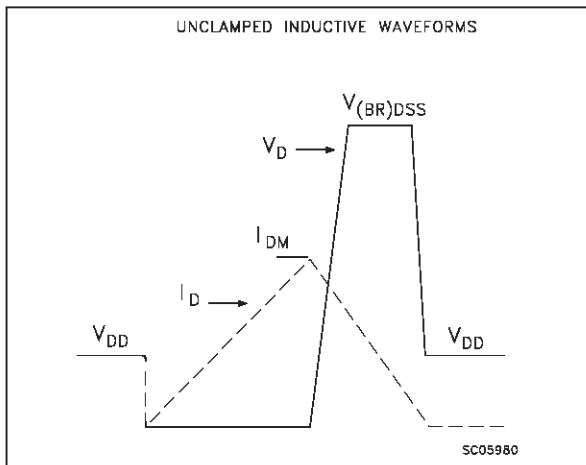


Fig. 3: Switching Times Test Circuits For Resistive Load



Fig. 4: Gate Charge test Circuit

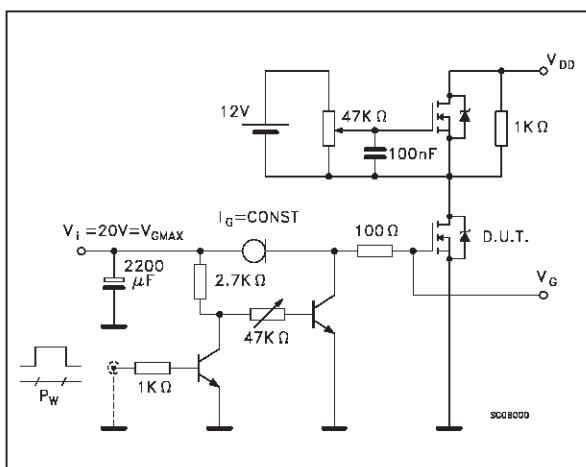
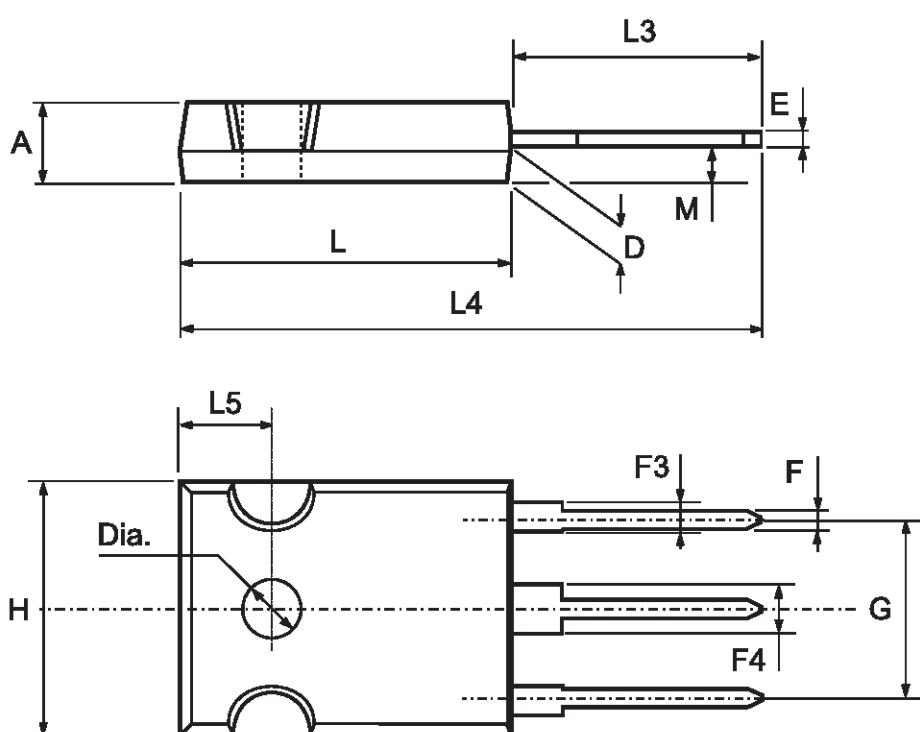


Fig. 5: Test Circuit For Inductive Load Switching And Diode Recovery Times



TO-247 MECHANICAL DATA

| DIM. | mm | | | inch | | |
|------|------|------|------|-------|-------|-------|
| | MIN. | TYP. | MAX. | MIN. | TYP. | MAX. |
| A | 4.7 | | 5.3 | 0.185 | | 0.209 |
| D | 2.2 | | 2.6 | 0.087 | | 0.102 |
| E | 0.4 | | 0.8 | 0.016 | | 0.031 |
| F | 1 | | 1.4 | 0.039 | | 0.055 |
| F3 | 2 | | 2.4 | 0.079 | | 0.094 |
| F4 | 3 | | 3.4 | 0.118 | | 0.134 |
| G | | 10.9 | | | 0.429 | |
| H | 15.3 | | 15.9 | 0.602 | | 0.626 |
| L | 19.7 | | 20.3 | 0.776 | | 0.779 |
| L3 | 14.2 | | 14.8 | 0.559 | | 0.582 |
| L4 | | 34.6 | | | 1.362 | |
| L5 | | 5.5 | | | 0.217 | |
| M | 2 | | 3 | 0.079 | | 0.118 |



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