

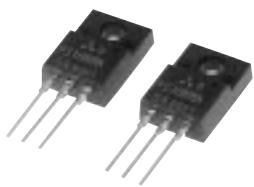
PRELIMINARY
Notice: This is not a final specification.
Some parametric limits are subject to change.

MITSUBISHI Nch POWER MOSFET

FS10KMA-5A

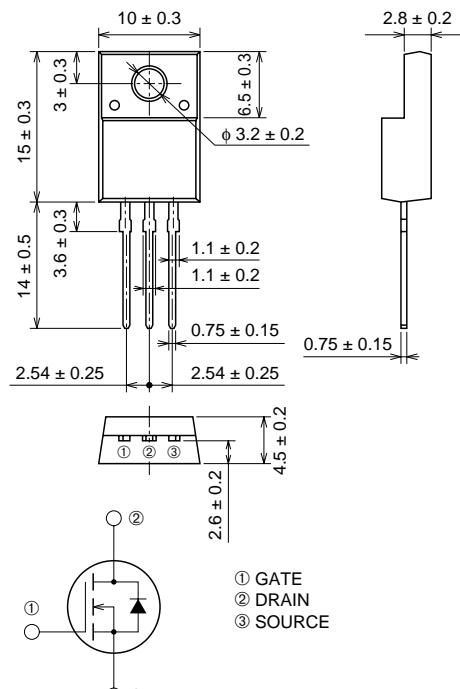
HIGH-SPEED SWITCHING USE

FS10KMA-5A



- 10V DRIVE
- V_{DSS} 250V
- r_{Ds} (ON) (MAX) 0.52Ω
- I_D 10A

OUTLINE DRAWING



APPLICATION

Cs Switch for CRT Display monitor

MAXIMUM RATINGS ($T_c = 25^\circ\text{C}$)

| Symbol | Parameter | Conditions | Ratings | Unit |
|------------------|----------------------------------|-----------------------------------|------------|------|
| V _{DSS} | Drain-source voltage | V _{GS} = 0V | 250 | V |
| V _{GSS} | Gate-source voltage | V _{DS} = 0V | ±20 | V |
| I _D | Drain current | | 10 | A |
| I _{DM} | Drain current (Pulsed) | | 30 | A |
| I _{DA} | Avalanche drain current (Pulsed) | L = 200μH | 10 | A |
| P _D | Maximum power dissipation | | 32 | W |
| T _{ch} | Channel temperature | | -55 ~ +150 | °C |
| T _{stg} | Storage temperature | | -55 ~ +150 | °C |
| V _{iso} | Isolation voltage | AC for 1 minute, Terminal to case | 2000 | V |
| — | Weight | Typical value | 2.0 | g |

Sep.1998

PRELIMINARY
 Notice: This is not a final specification.
 Some parametric limits are subject to change.

FS10KMA-5A**HIGH-SPEED SWITCHING USE****ELECTRICAL CHARACTERISTICS** ($T_{ch} = 25^\circ C$)

| Symbol | Parameter | Test conditions | Limits | | | Unit |
|-----------------------|----------------------------------|---|--------|------|----------|---------------------------|
| | | | Min. | Typ. | Max. | |
| $V_{(BR)DSS}$ | Drain-source breakdown voltage | $ID = 1\text{mA}$, $V_{GS} = 0\text{V}$ | 250 | — | — | V |
| I_{GSS} | Gate-source leakage current | $V_{GS} = \pm 20\text{V}$, $V_{DS} = 0\text{V}$ | — | — | ± 10 | μA |
| I_{DSS} | Drain-source leakage current | $V_{DS} = 250\text{V}$, $V_{GS} = 0\text{V}$ | — | — | 1 | mA |
| $V_{GS(\text{th})}$ | Gate-source threshold voltage | $ID = 1\text{mA}$, $V_{DS} = 10\text{V}$ | 2.0 | 3.0 | 4.0 | V |
| $r_{DS(\text{ON})}$ | Drain-source on-state resistance | $ID = 5\text{A}$, $V_{GS} = 10\text{V}$ | — | 0.40 | 0.52 | Ω |
| $V_{DS(\text{ON})}$ | Drain-source on-state voltage | $ID = 5\text{A}$, $V_{GS} = 10\text{V}$ | — | 2.00 | 2.60 | V |
| $ y_{fs} $ | Forward transfer admittance | $ID = 5\text{A}$, $V_{DS} = 10\text{V}$ | — | 9.0 | — | S |
| C_{iss} | Input capacitance | $V_{DS} = 25\text{V}$, $V_{GS} = 0\text{V}$, $f = 1\text{MHz}$ | — | 950 | — | pF |
| C_{oss} | Output capacitance | | — | 90 | — | pF |
| C_{rss} | Reverse transfer capacitance | | — | 25 | — | pF |
| $t_{d(\text{on})}$ | Turn-on delay time | $V_{DD} = 150\text{V}$, $ID = 5\text{A}$, $V_{GS} = 10\text{V}$, $R_{GEN} = R_{GS} = 50\Omega$ | — | 20 | — | ns |
| t_r | Rise time | | — | 25 | — | ns |
| $t_{d(\text{off})}$ | Turn-off delay time | | — | 150 | — | ns |
| t_f | Fall time | | — | 40 | — | ns |
| V_{SD} | Source-drain voltage | $I_S = 5\text{A}$, $V_{GS} = 0\text{V}$ | — | 0.95 | — | V |
| $R_{th(\text{ch-c})}$ | Thermal resistance | Channel to case | — | — | 3.91 | $^\circ\text{C}/\text{W}$ |