

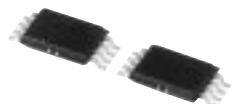
PRELIMINARY
Notice: This is not a final specification.
Some parametric limits are subject to change.

MITSUBISHI Nch POWER MOSFET

FY7BCH-02A

HIGH-SPEED SWITCHING USE

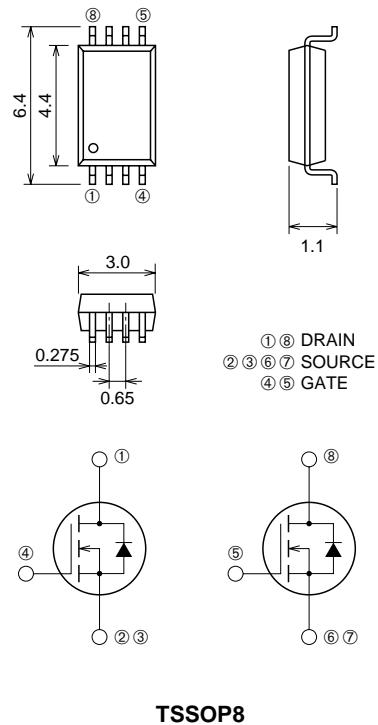
FY7BCH-02A



- 2.5V DRIVE
- V_{DSS} 20V
- r_{Ds} (ON) (MAX) 25mΩ
- I_D 7A

OUTLINE DRAWING

Dimensions in mm



APPLICATION

Motor control, Lamp control, Solenoid control
DC-DC converter, etc.

MAXIMUM RATINGS ($T_c = 25^\circ\text{C}$)

Symbol	Parameter	Conditions	Ratings	Unit
V _{DSS}	Drain-source voltage	V _{GS} = 0V	20	V
V _{GSS}	Gate-source voltage	V _{DS} = 0V	±10	V
I _D	Drain current		7	A
I _{DM}	Drain current (Pulsed)		49	A
I _{DA}	Avalanche drain current (Pulsed)	L = 10μH	7	A
I _S	Source current		1.5	A
I _{SM}	Source current (Pulsed)		6.0	A
P _D	Maximum power dissipation		1.6	W
T _{ch}	Channel temperature		-55 ~ +150	°C
T _{stg}	Storage temperature		-55 ~ +150	°C
—	Weight	Typical value	0.035	g

Sep.1998

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ELECTRICAL CHARACTERISTICS ($T_{ch} = 25^\circ\text{C}$)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
$V_{(BR)DSS}$	Drain-source breakdown voltage	$Id = 1\text{mA}$, $V_{GS} = 0\text{V}$	20	—	—	V
$IGSS$	Gate-source leakage current	$V_{GS} = \pm 10\text{V}$, $V_{DS} = 0\text{V}$	—	—	± 0.1	μA
$IdSS$	Drain-source leakage current	$V_{DS} = 20\text{V}$, $V_{GS} = 0\text{V}$	—	—	0.1	mA
$V_{GS(th)}$	Gate-source threshold voltage	$Id = 1\text{mA}$, $V_{DS} = 10\text{V}$	0.4	0.9	1.3	V
$r_{DS(on)}$	Drain-source on-state resistance	$Id = 7\text{A}$, $V_{GS} = 4\text{V}$	—	20	25	$\text{m}\Omega$
$r_{DS(on)}$	Drain-source on-state resistance	$Id = 3.5\text{A}$, $V_{GS} = 2.5\text{V}$	—	26	37	$\text{m}\Omega$
$V_{DS(on)}$	Drain-source on-state voltage	$Id = 7\text{A}$, $V_{GS} = 4\text{V}$	—	0.140	0.182	V
$ y_{fs} $	Forward transfer admittance	$Id = 7\text{A}$, $V_{DS} = 10\text{V}$	—	16	—	S
C_{iss}	Input capacitance	$V_{DS} = 10\text{V}$, $V_{GS} = 0\text{V}$, $f = 1\text{MHz}$	—	1150	—	pF
C_{oss}	Output capacitance		—	380	—	pF
C_{rss}	Reverse transfer capacitance		—	300	—	pF
$t_{d(on)}$	Turn-on delay time	$V_{DD} = 10\text{V}$, $Id = 3.5\text{A}$, $V_{GS} = 4\text{V}$, $R_{GEN} = R_{GS} = 50\Omega$	—	20	—	ns
t_r	Rise time		—	80	—	ns
$t_{d(off)}$	Turn-off delay time		—	140	—	ns
t_f	Fall time		—	135	—	ns
V_{SD}	Source-drain voltage	$Is = 1.5\text{A}$, $V_{GS} = 0\text{V}$	—	0.75	1.1	V
$R_{th(ch-a)}$	Thermal resistance	Channel to ambient	—	—	78.1	$^\circ\text{C}/\text{W}$
trr	Reverse recovery time	$Is = 1.5\text{A}$, $dIs/dt = -50\text{A}/\mu\text{s}$	—	50	—	ns