

LM2405 Monolithic Triple 7 ns CRT Driver

General Description

The LM2405 is an integrated high voltage CRT driver circuit designed for use in color monitor applications. The IC contains three high input impedance, wide band amplifiers which directly drive the RGB cathodes of a CRT. Each channel has its gain internally set at -14 and can drive CRT capacitive loads as well as resistive loads presented by other applications, limited only by the package's power dissipation.

The IC is packaged in an industry standard 11 lead TO-220 molded plastic power package. See thermal considerations on page 5.

Features

- Rise/fall times typically 7 ns with 8 pF load
- Output swing capability: 50 V_{PP} for V_{CC} = 80
40 V_{PP} for V_{CC} = 70
30 V_{PP} for V_{CC} = 60
- Pinout designed for easy PCB layout
- 0V to 6V input range
- Stable with 0 pF–20 pF capacitive loads
- Convenient TO-220 staggered lead package style

Applications

- CRT driver for 1280 × 1024 (Non-interfaced) and XGA display resolution color monitors
- Pixel clock frequency up to 130 MHz
- Monitors using video blanking

Schematic and Connection Diagrams

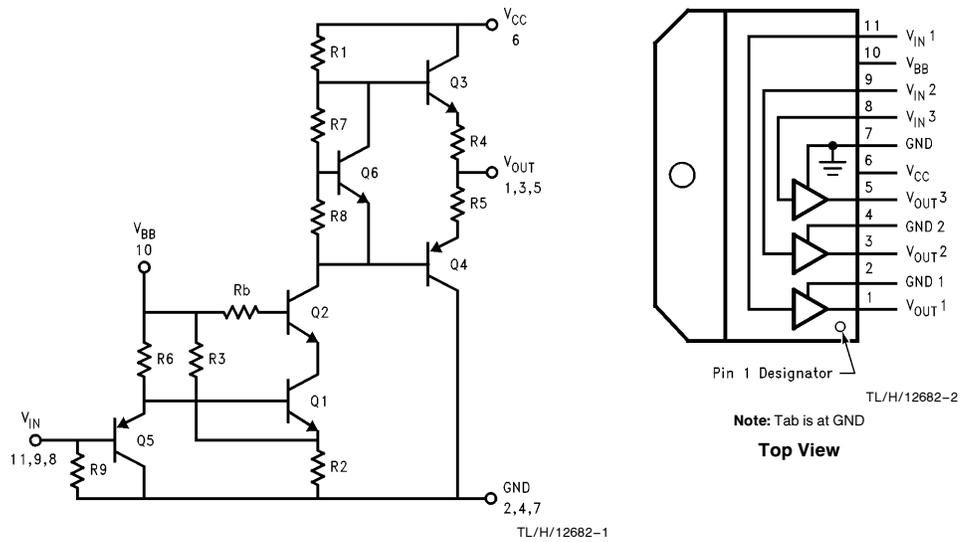


FIGURE 1. Simplified Schematic Diagram (One Channel)

Absolute Maximum Ratings

(Notes 1 and 3)

Supply Voltage (V_{CC})	+90V
Bias Voltage (V_{BB})	+16V
Input Voltage (V_{IN})	-0.5V to $V_{BIAS} + 0.5V$
Storage Temperature Range (T_{STG})	-65°C to +150°C
Lead Temperature (Soldering, <10 sec.)	300°C
ESD Tolerance	2 kV

Operating Ranges (Note 2)

V_{CC}	+60V to +85V
V_{BB}	+8V to +15V
V_{IN}	0V to +6V
Case Temperature (T_{CASE})	-20°C to +100°C

Do not operate the part without a heat sink.

Electrical Characteristics

Unless otherwise noted: $V_{CC} = +80V$, $V_{BB} = +12V$, $V_{IN} = +2.6V$ (at LM2405 input pins), $C_L = 8\text{ pF}$, Output = 40 V_{PP} at 1 MHz, $T_A = 25^\circ\text{C}$.

Symbol	Parameter	Conditions	LM2405			Units
			Min	Typical	Max	
I_{CC}	Supply Current	Per Channel, No Output Load		18	30	mA
I_{BB}	Bias Current			38		mA
V_{OUT}	DC Output Voltage	No Input Signal	47	50	53	V_{DC}
A_V	DC Voltage Gain	No Input Signal	-12	-14	-16	
ΔA_V	Gain Matching	No Input Signal (Note 4)		1.0		dB
LE	Linearity Error	No Input Signal (Notes 4, 5)		8		%
t_R	Rise Time	10% to 90%		7		ns
t_F	Fall Time	90% to 10%		5.5		ns

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur.

Note 2: Operating ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may change when the device is not operated under the listed test conditions.

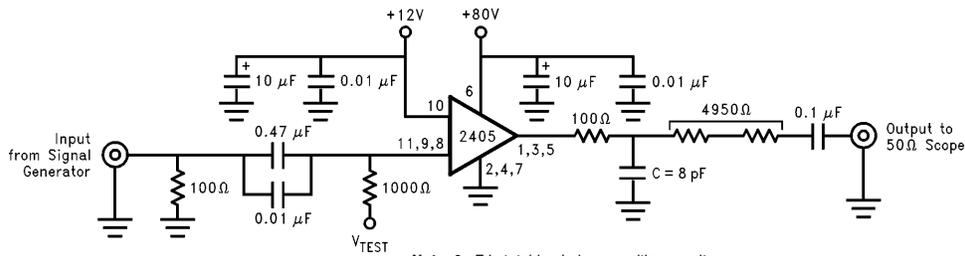
Note 3: All voltages are measured with respect to GND, unless otherwise specified.

Note 4: Calculated value from Voltage Gain test on each channel.

Note 5: Linearity Error is the variation in DC gain from $V_{IN} = +1.3V$ to $V_{IN} = +3.9V$.

Note 6: Input from signal generator: $t_R, t_F < 1\text{ ns}$.

AC Test Circuit



Note: 8 pF is total load plus parasitic capacitance.

Note: Adjust V_{TEST} for +2.6V DC at LM2405 input pins. See "Input Resistance" section of Application Hints.

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FIGURE 2. Test Circuit (One Channel)

Figure 2 shows a typical test circuit for evaluation of the LM2405. This circuit is designed to allow testing of the LM2405 in a 50Ω environment, such as a pulse generator,

oscilloscope or network analyzer. The 4950Ω resistor at the output forms a 100:1 voltage divider when connected to a 50Ω load.

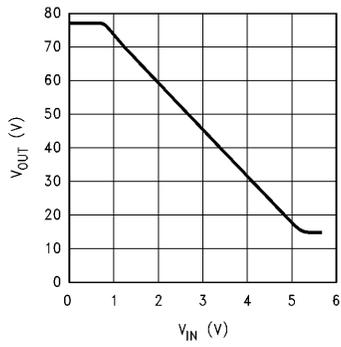


FIGURE 3. V_{OUT} vs V_{IN}

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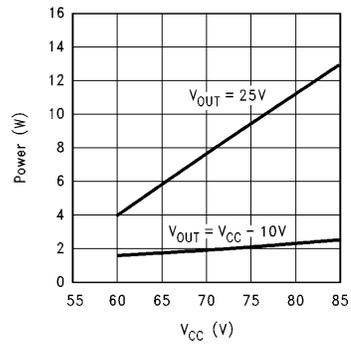


FIGURE 4. Power Dissipation vs V_{CC}

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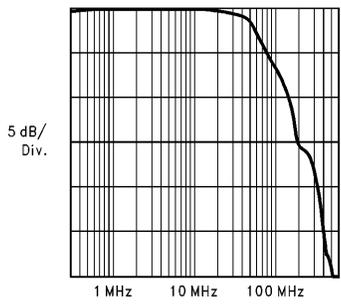


FIGURE 5. Large Signal Frequency Response

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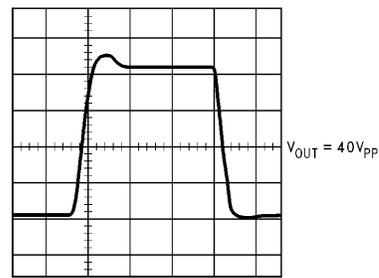


FIGURE 6. Pulse Response

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Theory of Operation

The LM2405 is a high voltage monolithic triple CRT driver suitable for SVGA and XGA display applications. The LM2405 features +80V operation and low power dissipation. The part is housed in the industry standard 11-lead TO-220 molded plastic power package.

The circuit diagram of the LM2405 is shown in *Figure 1*. A PNP emitter follower, Q5, provides input buffering. Q1 and Q2 form a fixed gain cascode amplifier, with a gain of -14 . Emitter followers Q3 and Q4 isolate the high output impedance of the amplifier from the capacitance of the CRT cathode, and make the circuit relative insensitive to load capacitance. Q6 provides biasing to the output emitter follower stage to reduce crossover distortion at low signal levels.

Figure 2 shows a typical test circuit for evaluation of the LM2405. This circuit is designed to allow testing of the LM2405 in a 50Ω environment, such as a pulse generator and a scope, or a network analyzer. In this test circuit, two low inductance resistors in series totaling $4.95\text{ k}\Omega$ form a 100:1 wideband low capacitance probe when connected to a 50Ω cable and load. The input signal from the generator is AC coupled to the base of Q5.

Application Hints

INTRODUCTION

National Semiconductor is committed to providing application information that assists our customers in obtaining the best performance possible from our products. The following information is provided in order to support this commitment. The reader should be aware that the optimization of performance was done using a specific printed circuit board designed at National. Variations in performance can be realized due to physical changes in the printed circuit board and the application. Therefore, the designer should be aware that component value changes may be required in order to optimize performance in a given application. The values shown in this document can be used as a starting point for evaluation purposes. When working with high bandwidth circuits, good layout practices are also critical to achieving maximum performance.

POWER SUPPLY BYPASS

Since the LM2405 is a wide bandwidth amplifier, proper power supply bypassing is critical for optimum performance. Improper power supply bypassing can result in large overshoot, ringing and oscillation. A $0.01\ \mu\text{F}$ capacitor should be connected from the supply pin, V_{CC} , to ground, as close to the supply pin as is practical (preferably less than $1/4$ " from the supply pin). Additionally, a $10\ \mu\text{F}$ to $100\ \mu\text{F}$ electrolytic capacitor should be connected from the supply pin to ground. The electrolytic capacitor should also be placed reasonably close to the LM2405's supply pin. A $0.1\ \mu\text{F}$ capacitor should be connected from the bias pin, V_{BB} , to ground, as close as is practical to the part.

ARC PROTECTION

During normal CRT operation, internal arcing may occasionally occur. Spark gaps of 200V to 300V at the cathodes will limit the maximum voltage, but to a value that is much higher than allowable on the LM2405. This fast, high voltage,

high energy pulse can damage the LM2405 output stage. The addition of clamp diodes D1 and D2 (as shown in *Figure 7*) will help clamp the voltage at the output of the LM2405 to a safe level. The clamp diodes should have a fast transient response, high peak current rating, low series impedance and low shunt capacitance. FDH400 or equivalent diodes are recommended. Resistor R2 in *Figure 7* limits the arcover current while R1 limits the current into the LM2405 and reduces the power dissipation of the output transistors when the output is stressed beyond the supply voltage. (Peaking inductor L_p also helps protect the CRT driver from arc over.) Having large value resistors for R1 and R2 would be desirable, but this has the effect of increasing rise and fall times. For proper arc protection, it is important to not omit any of the arc protection components shown in *Figure 7*.

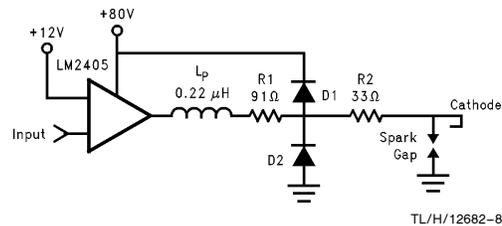


FIGURE 7. One Section of the LM2405 with Arc Protection and Peaking Inductor L_p

There are also ESD protection diodes built into the part. To avoid damaging these diodes, do not apply an input voltage from a low impedance source when the V_{BB} and V_{CC} pins are held at ground potential.

IMPROVING RISE AND FALL TIMES

Because of an emitter follower output stage, the rise and fall times of the LM2405 are relatively insensitive to capacitive loading. However, the series resistors R1 and R2 (see *Figure 7*) will increase the rise and fall times when driving the CRT's cathode which appears as a capacitive load. The capacitance at the cathode typically ranges from 8 pF to 12 pF .

To improve the rise and fall times at the cathode, a small inductor is often used in series with the output of the amplifier. The inductor L_p in *Figure 7* peaks the amplifier's frequency response at the cathode, thus improving rise and fall times. It also acts with the output load capacitance to form a low pass filter, which reduces the amplitudes of high frequency harmonics of the video signal, to lower radiated electromagnetic interference. The inductor value is empirically determined and is dependent on the load. An inductor value of $0.22\ \mu\text{H}$ is a good starting value. Note that excessive peaking of the amplifier's frequency response will increase the overshoot. (Increasing the value of resistor R1 or R2 will reduce ringing and overshoot.)

EFFECT OF LOAD CAPACITANCE

The output rise and fall times will be slower than specified if the load capacitance at the output is more than 8 pF, as shown in *Figure 8*.

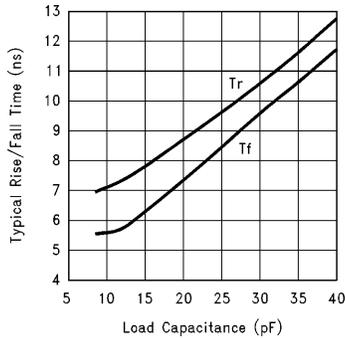


FIGURE 8. Effect of Load Capacitance on Rise/Fall Time

The monitor designer should ensure that stray capacitance applied to the LM2405 is as low as possible.

THERMAL CONSIDERATIONS

Power supply current increases as the input signal increases and consequently power dissipation also increases.

The LM2405 cannot be used without heat sinking. Typical "average" power dissipation with the device output voltage at one half the supply voltage is 2.4W per channel for a total dissipation of 7.2W package dissipation. Under white screen conditions, i.e., 25V output, dissipation increases to 3.5W per channel or 10.5W total. The LM2405 case temperature must be maintained below 100°C. If the maximum expected ambient temperature is 50°C, then a maximum heat sink thermal resistance can be calculated:

$$R_{th} = \frac{100^{\circ}\text{C} - 50^{\circ}\text{C}}{10.5\text{W}} = 4.8^{\circ}\text{C}/\text{W}.$$

This example assumes a typical CRT capacitive load and is without a resistive load. Note that this thermal resistance must be achieved when the heat sink is operating in the monitor.

INPUT RESISTANCE

The LM2405 has a fixed resistor of 3000Ω connected from each signal input pin to ground. In the *Figure 2* Test Circuit, the input DC voltage level, V_{test} , must be adjusted, (to about +3.5V) to allow for the voltage drop across the 1000Ω resistor, to set the actual voltage at the input pins to +2.6V. In actual use in a monitor, the 1000Ω resistor is not used and the video preamp supplies the 2.6V offset.

PC BOARD LAYOUT CONSIDERATIONS

For optimum performance, an adequate ground plane, isolation between channels, good supply bypassing and minimizing unwanted feedback are necessary. Also, the length of the signal traces from the preamplifier to the LM2405 and from the LM2405 to the CRT cathode should be as short as possible. The following references are recommended:

Ott, Henry W., "Noise Reduction Techniques in Electronic Systems", John Wiley and Sons, New York, 1976.

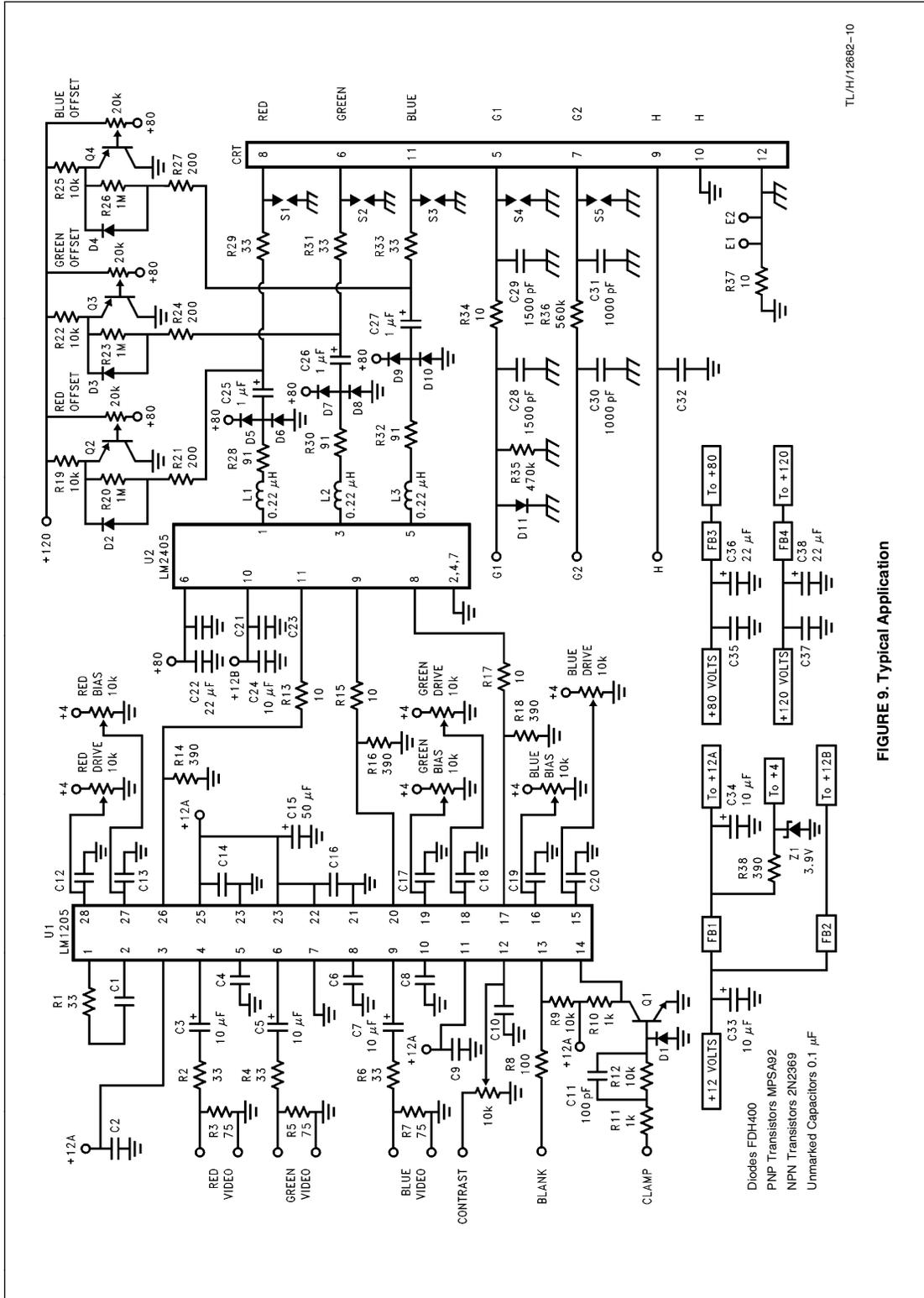
"Guide to CRT Video Design", National Semiconductor Application Note 861.

"Video Amplifier Design for Computer Monitors", National Semiconductor Application Note 1013.

Because of its high small signal bandwidth, the part may oscillate when it is used in a typical application with a preamp in a monitor, if feedback occurs around the video amplifier through the chassis wiring. To prevent this, leads to the input circuit should be shielded, and input circuit wiring should be spaced as far as possible from output circuit wiring. Power should be removed as quickly as possible from an amplifier that is oscillating, since power dissipation in the part is very high in this mode and the part may be damaged if oscillations continue and the power supply can supply more than 250 mA.

TYPICAL APPLICATION

A typical application of the LM2405 is shown in *Figure 9*. Used in conjunction with an LM1205, a complete video channel from monitor input to CRT cathode can be achieved. Performance is satisfactory for all applications up to 1280 × 1024 non-interlaced, and pixel clock frequencies up to 130 MHz.



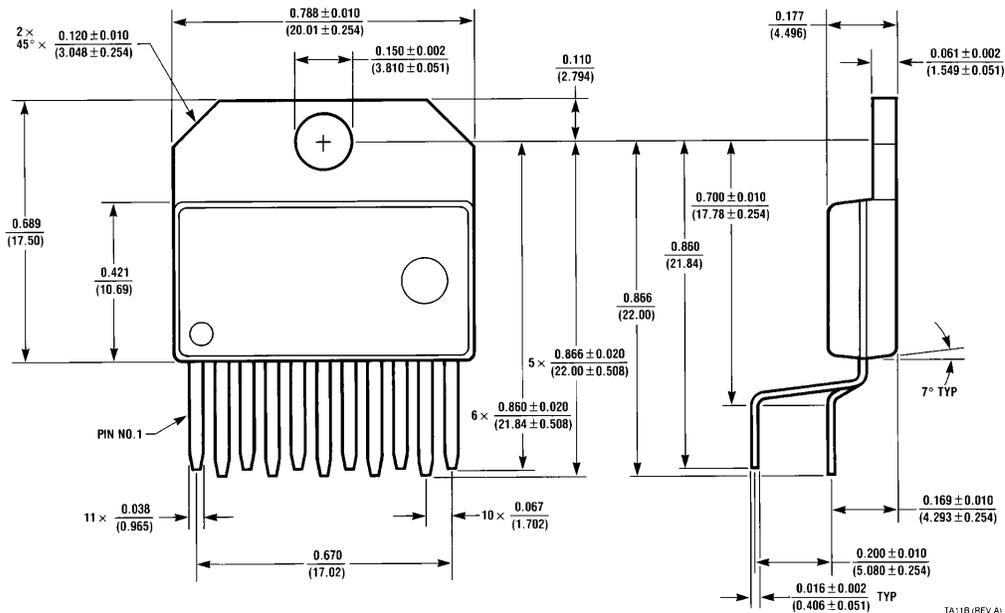
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FIGURE 9. Typical Application



LM2405 Monolithic Triple 7 ns CRT Driver

Physical Dimensions inches (millimeters) unless otherwise noted



LM2405
11-Lead Molded TO-220
NS Package Number TA11B

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National Semiconductor Corporation
 1111 West Bardin Road
 Arlington, TX 76017
 Tel: 1(800) 272-9959
 Fax: 1(800) 737-7018

<http://www.national.com>

National Semiconductor Europe

Fax: +49 (0) 180-530 85 86
 Email: europe.support@nsc.com
 Deutsch Tel: +49 (0) 180-530 85 85
 English Tel: +49 (0) 180-532 78 32
 Français Tel: +49 (0) 180-532 93 58
 Italiano Tel: +49 (0) 180-534 16 80

National Semiconductor Hong Kong Ltd.

19th Floor, Straight Block,
 Ocean Centre, 5 Canton Rd.
 Tsimshatsui, Kowloon
 Hong Kong
 Tel: (852) 2737-1600
 Fax: (852) 2736-9960

National Semiconductor Japan Ltd.

Tel: 81-043-299-2308
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