

LM137HV/LM337HV 3-Terminal Adjustable Negative Regulators (High Voltage)

General Description

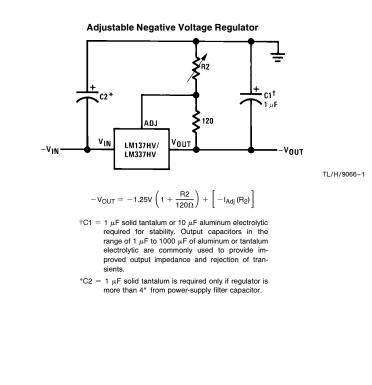
The LM137HV/LM337HV are adjustable 3-terminal negative voltage regulators capable of supplying in excess of -1.5A over an output voltage range of -1.2V to -47V. These regulators are exceptionally easy to apply, requiring only 2 external resistors to set the output voltage and 1 output capacitor for frequency compensation. The circuit design has been optimized for excellent regulation and low thermal transients. Further, the LM137HV series features internal current limiting, thermal shutdown and safe-area compensation, making them virtually blowout-proof against overloads.

The LM137HV/LM337HV serve a wide variety of applications including local on-card regulation, programmable-output voltage regulation or precision current regulation. The LM137HV/LM337HV are ideal complements to the LM117HV/LM317HV adjustable positive regulators.

Features

- Output voltage adjustable from -1.2V to -47V
- 1.5A output current guaranteed, -55°C to +150°C
- Line regulation typically 0.01%/V
- Load regulation typically 0.3%
- Excellent thermal regulation, 0.002%/W
- 77 dB ripple rejection
- Excellent rejection of thermal transients
- 50 ppm/°C temperature coefficient
- Temperature-independent current limit
- Internal thermal overload protection
- P⁺ Product Enhancement tested
- Standard 3-lead transistor package
- Output short circuit protected





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Absolute Maximum Ratings If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Operating Junction Temperature Range LM137HV -55°C to $+150^\circ\text{C}$ Office/Distributors for availability and specifications. LM337HV 0°C to +125°C (Note 3) Storage Temperature -65° C to $+150^{\circ}$ C Internally limited **Power Dissipation** Lead Temperature (Soldering, 10 sec.) 300° Input—Output Voltage Differential 50V ESD rating is to be determined.

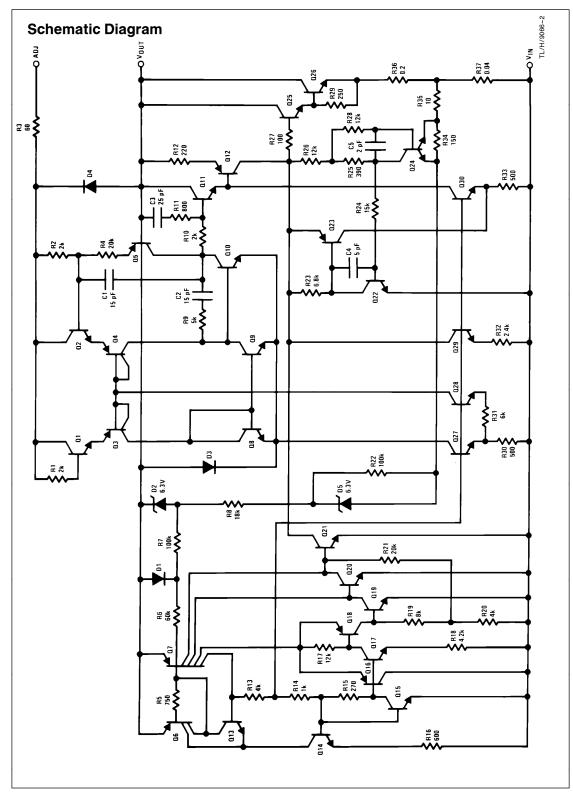
Electrical Characteristics (Note 1)

Parameter	Conditions	LM137HV			LM337HV			Units
		Min	Тур	Max	Min	Тур	Max	
Line Regulation	$\label{eq:total_states} \begin{split} T_J &= 25^{\circ}C, 3V \leq \left V_{IN}\!-\!V_{OUT}\right \leq 50V, \\ (Note 2) \ I_L &= 10 \ \text{mA} \end{split}$		0.01	0.02		0.01	0.04	%/V
Load Regulation	T_{J} = 25°C, 10 mA \leq I _{OUT} \leq I _{MAX}		0.3	0.5		0.3	1.0	%
Thermal Regulation	$T_J = 25^{\circ}C$, 10 ms Pulse		0.002	0.02		0.003	0.04	%/W
Adjustment Pin Current			65	100		65	100	μA
Adjustment Pin Current Change			2	5		2	5	μA
	$ \begin{array}{l} 3.0V \leq \left V_{IN} - V_{OUT} \right \leq 50V, \\ T_J = 25^\circ \end{array} $		4	6		3	6	μΑ
Reference Voltage	T _J = 25°C, (Note 3)	-1.225				-1.250	-1.287	V
	$\begin{array}{l} 3V \leq \left V_{IN}\!-\!V_{OUT}\right \leq 50V, (Note 3) \\ 10 \text{ mA} \leq I_{OUT} \leq I_{MAX}, P \leq P_{MAX} \end{array}$	-1.200	-1.250	-1.300	-1.200	-1.250	-1.300	V
Line Regulation	$3V \leq \left V_{IN}\!-\!V_{OUT}\right \leq$ 50V, (Note 2) $I_L = 10 \text{ mA}$		0.02	0.05		0.02	0.07	%/V
Load Regulation	10 mA \leq I _{OUT} \leq I _{MAX} , (Note 2)		0.3	1		0.3	1.5	%
Temperature Stability	$T_{MIN} \le T_j \le T_{MAX}$		0.6			0.6		%
Minimum Load Current	$ V_{IN} - V_{OUT} \le 50V$		2.5	5		2.5	10	mA
	$ V_{IN} - V_{OUT} \le 10V$		1.2	3		1.5	6	mA
Current Limit	$ V_{IN} - V_{OUT} \le 13V$ K Package	1.5	2.2	3.2	1.5		3.5	
	H Package	0.5	2.2 0.8	3.2 1.6	0.5	2.2 0.8	3.5 1.8	A
	$ V_{\rm IN} - V_{\rm OUT} = 50V$	0.0	0.0	1.0	0.0	0.0	1.0	
	KPackage	0.2	0.4	0.8	0.1	0.4	0.8	A
	H Package	0.1	0.17	0.5	0.050	0.17	0.5	A
RMS Output Noise, % of VOUT	T_{J} = 25°C, 10 Hz \leq f \leq 10 kHz		0.003			0.003		%
Ripple Rejection Ratio	$V_{OUT} = -10V, f = 120 Hz$		60			60		dB
	$C_{ADJ} = 10 \ \mu F$	66	77		66	77		dB
Long-Term Stability	T _A = 125°C, 1000 Hours		0.3	1		0.3	1	%
Thermal Resistance, Junction	H Package		12	15		12	15	°C/W
to Case	K Package		2.3	3		2.3	3	°C/W
Thermal Resistance, Junction	H Package		140			140		°C/W
to Ambient	K Package		35			35		°C/W

Note 1: Unless otherwise specified, these specifications apply: $-55^{\circ}C \le T_{j} \le +150^{\circ}C$ for the LM137HV, 0°C $\le T_{j} \le +125^{\circ}C$ for the LM337HV; $V_{IN} - V_{OUT} = 5V$; and $I_{OUT} = 0.1A$ for the TO-39 package and $I_{OUT} = 0.5A$ for the TO-3 package. Although power dissipation is internally limited, these specifications are applicable for power dissipations of 2W for the TO-39 and 20W for the TO-3. I_{MAX} is 1.5A for the TO-3 package and 0.2A for the TO-39 package.

Note 2: Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specification for thermal regulations. Load regulation is measured on the output pin at a point 1/6" below the base of the TO-3 and TO-39 packages.

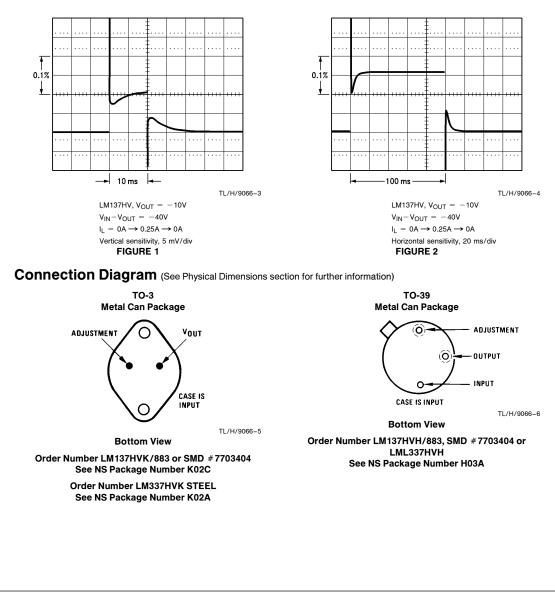
Note 3: Refer to RETS137HVH drawing for LM137HVH or RETS137HVK for LM137HVK military specifications.



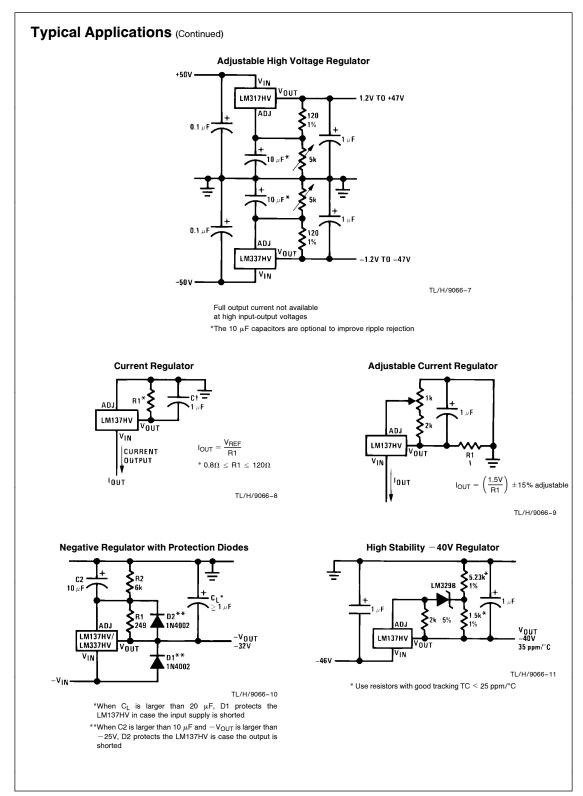
Thermal Regulation

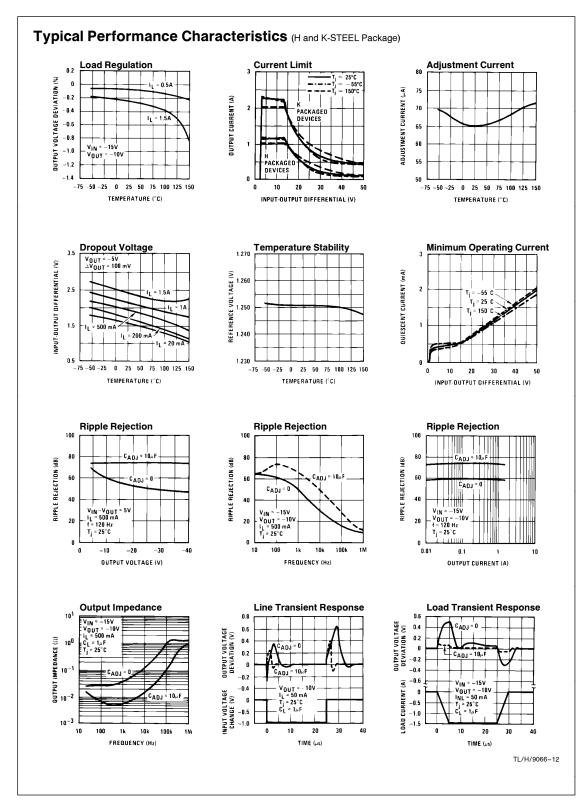
When power is dissipated in an IC, a temperature gradient occurs across the IC chip affecting the individual IC circuit components. With an IC regulator, this gradient can be especially severe since power dissipation is large. Thermal regulation is the effect of these temperature gradients on output voltage (in percentage output change) per Watt of power change in a specified time. Thermal regulation error is independent of electrical regulation or temperature coefficient, and occurs within 5 ms to 50 ms after a change in power dissipation. Thermal regulation depends on IC layout as well as electrical design. The thermal regulation of a voltage regulator is defined as the percentage change of V_{OUT} , per Watt, within the first 10 ms after a step of power is applied. The LM137HV's specification is 0.02%/W, max.

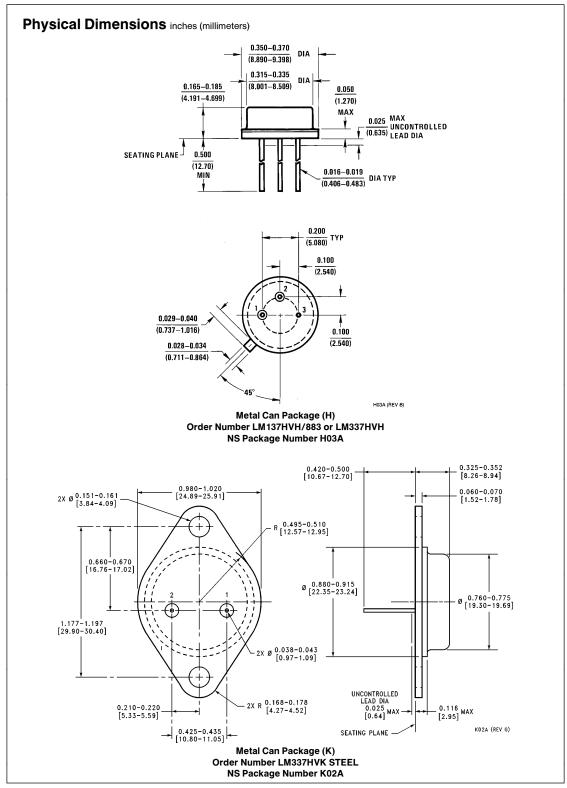
In *Figure 1*, a typical LM137HV's output drifts only 3 mV (or 0.03% of $V_{OUT} = -10V$) when a 10W pulse is applied for 10 ms. This performance is thus well inside the specification limit of 0.02% /W x 10W = 0.2% max. When the 10W pulse is ended, the thermal regulation again shows a 3 mV step as the LM137HV chip cools off. Note that the load regulation error of about 8 mV (0.08%) is additional to the thermal regulation error. In *Figure 2*, when the 10W pulse is applied for 100 ms, the output drifts only slightly beyond the drift in the first 10 ms, and the thermal error stays well within 0.1% (10 mV).



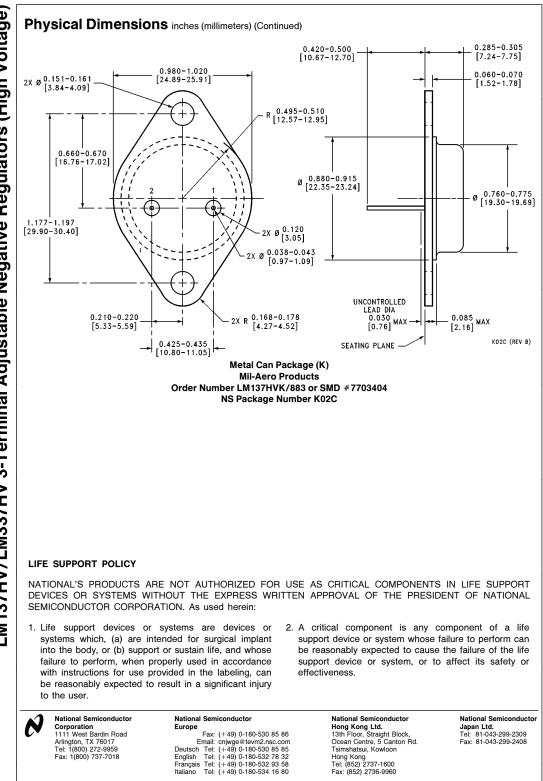
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