

TOTAL DOSE RADIATION TEST

I. INTRODUCTION

Total dose radiation tests are designed to characterize changes in device performance due to total dose radiation. These tests are not intended to classify maximum radiation tolerance of any particular device, rather, they simply show trends in the critical parameters as a function of total dose. Whether a device meets tolerance requirements is left up to the designer. In many occasions, designers have the ability to circumvent radiation effects by adding appropriate shielding or compensating for the variations in performance.

MIL-STD-883 method 1019 is used as a guideline for these tests. National's gamma radiation source is kept in compliance with method 1019 and radiation test samples are irradiated under dose rate condition A, which tests for total-dose effects. Samples are kept biased while irradiating. Dose rate is maintained between 50 - 300 Rads(Si)/sec and all samples are exposed to a total dose of 200 kRads(Si).

II. RADIATION SOURCE

A. Type

Atomic Energy of Canada Limited cobalt 60 irradiation unit model Gammacell 220 is used to irradiate the devices under test. The Gammacell 220 produces gamma radiation photons approximately 1.25MeV in energy. Dose rate in the gammacell is maintained between 50 and 300 Rads(Si)/sec with an accuracy of +/- 10%.

B. Dosimetery

Thermoluminescence Dosimetery is performed according to MIL-STD-883 method 1019. Actual dose rate for individual test is calculated from the exponential decay approximation of the dosimetery data.

III. TEST SETUP AND PROCEDURE

A. Pre-radiation Electrical Test

All test samples are verified to be functionally and parameterically working prior to irradiation. They are subject to group A qualification test including burn in. Samples are also verified to be within room temperature acceptance limits.

B. Test Environment

Samples are enclosed in a lead/aluminum container vertically aligned with the source of radiation while being irradiated. Ambient temperature throughout the test is approximately 25°C.

C. Biasing

All devices under test are kept biased during irradiation. Bias circuit used for burn-in is also used for irradiation.

C. Electrical Test

Remote electrical tests are performed on the irradiated devices at several total dose levels. All samples are short circuited while transporting to the automatic electrical tester. Electrical tests are completed within two hours of each irradiation step.

IV. DATA PRESENTATION

A Test Summary sheet provides details on the origins of test samples, dose rate, list of parameters tested and total variation in those parameters. Details of the test consists of select device parameter plotted and tabulated as a function of total dose. Test conditions for each parameter are also specified. Acceptance limits specified in RETS or MDS are also plotted on the graph for reference purpose.

This RHA report is supplied only as a guideline to demonstrate the characteristics of our product in a Total Dose Radiation environment. The results reported are representative only of the lot tested in this specific sample and should not be used as generic RHA qualification data. National Semiconductor uses different process flows for different product qualification levels, and National Semiconductor will not guarantee the RHA performance of any product unless National Semiconductor has tested and certified the specific manufacturing lot. At each radiation exposure level, minimum and maximum shows a plausible variation in the parameter values. It is important to remember that this variation includes variation due to radiation exposure as well as variation between lots and variation between wafers. Measurement variation is assumed insignificant. Whenever possible, radiation test reports will provide an estimate of the percentage of total variation that can be attributed to radiation exposure. This estimate is calculated by analysis of variance (ANOVA) or similar statistical method.



Summary:

LM118H performed exceptionally well up to the total dose of 200 kRads(Si). Seven critical parameters were monitored during the test and are presented in the report. Of those seven, six parameters remained inside the room temperature RETS limits throughout the test. One parameter, input bias current, was outside room temperature limit at 100 kRad(Si) total dose. All devices were functional at 200 kRad(Si). Summary of those seven parameters is shown in the following table.

Parameter	Average % Change Pre-rad to post 200k rad
CMRR	5.44
$V_{CC} = 15V$, $V_{CM} = 0V$	
PSRR	-5.61
V_{CC} = 5-20V, R_{S} = 50 Ω	
Supply Current	-12.93
$V_{CC} = +/-20V$, $V_{CM} = 0V$	
Gain	305.30
$V_O = 0-10V$, $R_L = 2k\Omega$	
Input Offset Voltage	-338.67
$V_{CC} = 15V$, $V_{CM} = 12V$	
Input Offset Current	-2633.33
V_{CM} = +/-11.5V, R_S = 10k Ω	
Input Bias Current	235.06
V_{CM} = +/-11.5V, R_{S} = 10k Ω	

Test Details:

Sample Size: 4

Lot #: MU0275E027

Die Run#: HL052983

Date Code: 9606

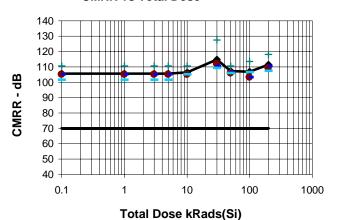
Test Date: 24 April 1996

Dose Rate: 104.66 +/- 10%

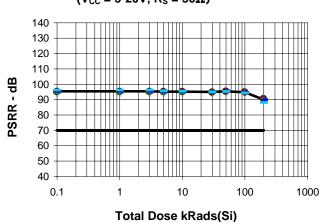
RETS: RETS118X Revision AA

Bias Circuit: 9557HR

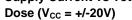


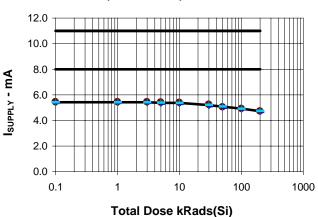


PSRR vs Total Dose $(V_{CC} = 5-20V, R_S = 50\Omega)$

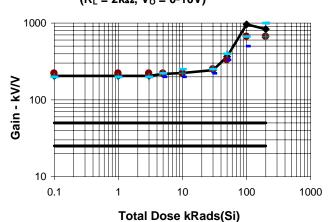


Supply Current vs Total

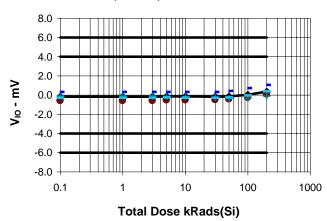




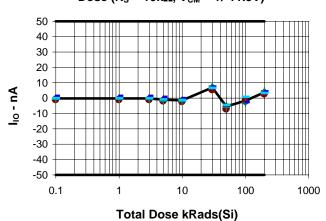
Gain vs Total Dose ($R_L = 2k\Omega$, $V_O = 0-10V$)



Input Offset Voltage vs Total Dose (+12V_{CM})



Input Offset Current vs Total Dose ($R_S = 10k\Omega$, $V_{CM} = +/-11.5V$)





CMRR vs Total Dose

Dose	Avg.	Min.	Max.	S. Dev.	Fail ¹
0.1	105.65	101.60	110.60	3.71	0
1	105.65	101.60	110.60	3.71	0
3	105.65	101.60	110.60	3.71	0
5	105.65	101.60	110.60	3.71	0
10	106.55	105.20	110.60	2.70	0
30	114.88	109.40	127.50	8.48	0
50	107.33	106.00	110.60	2.21	0
100	106.78	103.40	113.60	4.81	0
200	111.40	107.50	118.10	4.65	0

PSRR vs Total Dose (V_{CC} = 5-20V, R_S = 50 Ω)

Dose	Avg.	Min.	Max.	S. Dev.	Fail ¹
0.1	95.33	94.10	96.20	0.90	0
1	95.33	94.10	96.20	0.90	0
3	95.33	94.10	96.20	0.90	0
5	95.28	94.20	95.90	0.76	0
10	95.20	94.20	95.70	0.71	0
30	94.95	94.20	95.50	0.58	0
50	95.40	94.70	95.90	0.50	0
100	94.83	94.30	95.30	0.41	0
200	89.98	88.20	90.90	1.23	0

Supply Current vs Total Dose (V_{CC} = +/-20V)

Dose	Avg.	Min.	Max.	S. Dev.	Fail ¹
0.1	5.415	5.370	5.450	0.033	0
1	5.415	5.370	5.450	0.033	0
3	5.415	5.370	5.450	0.033	0
5	5.383	5.320	5.420	0.043	0
10	5.365	5.300	5.400	0.045	0
30	5.208	5.120	5.250	0.059	0
50	5.068	5.010	5.100	0.043	0
100	4.918	4.870	4.940	0.032	0
200	4.715	4.670	4.750	0.034	0

Gain vs Total Dose $(R_L = 2k\Omega, V_O = 0-10V)$

Dose	Avg.	Min.	Max.	S. Dev.	Fail ¹
0.1	205.6	200.0	222.2	11.1	0
1	205.6	200.0	222.2	11.1	0
3	205.6	200.0	222.2	11.1	0
5	216.7	200.0	222.2	11.1	0
10	223.6	200.0	250.0	20.5	0
30	243.1	222.2	250.0	13.9	0
50	366.7	333.3	400.0	38.5	0
100	958.3	500.0	2000.0	698.9	0
200	833.3	666.6	1000.0	192.5	0

Input Offset Voltage vs Total Dose (+12V_{CM})

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Dose	Avg.	Min.	Max.	S. Dev.	Fail ¹
0.1	-0.150	-0.610	0.350	0.399	0
1	-0.150	-0.610	0.350	0.399	0
3	-0.150	-0.610	0.350	0.399	0
5	-0.133	-0.540	0.350	0.370	0
10	-0.148	-0.540	0.330	0.363	0
30	-0.158	-0.490	0.330	0.347	0
50	-0.138	-0.420	0.450	0.401	0
100	0.035	-0.320	0.750	0.494	0
200	0.358	-0.100	1.060	0.512	0

Input Offset Current vs Total Dose ($R_S = 10k\Omega$, $V_{CM} = +/-11.5V$)

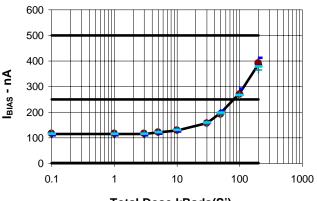
2000 (5 O	V CIVI — 17	,		
Dose	Avg.	Min.	Max.	S. Dev.	Fail ¹
0.1	-0.150	-1.200	1.400	1.147	0
1	-0.150	-1.200	1.400	1.147	0
3	-0.150	-1.200	1.400	1.147	0
5	-1.000	-2.000	0.800	1.327	0
10	-1.450	-2.400	0.000	1.100	0
30	6.800	5.200	8.000	1.211	0
50	-5.500	-7.200	-4.400	1.361	0
100	-1.750	-3.200	0.800	1.754	0
200	3.800	2.600	4.800	0.993	0

Note 1: Number of devices that were outside RETS sub group 1 limits.

Note 2: Dose Rate = 104.66 Rads(Si)/sec; V_{CC} = 15V, V_{CM} = 0V unless stated otherwise; RETS118X limits are shown.



Input Bias Current vs Total Dose ($V_{CM} = +/-11.5V$, $R_S = 10k\Omega$)



Total Dose kRads(Si)



Input Bias Current vs Total Dose ($V_{CM} = +/-11.5V$, $R_S = 10k\Omega$)

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Dose	Avg.	Min.	Max.	S. Dev.	Fail ¹
0.1	115.5	110.0	119.0	3.873	0
1	115.5	110.0	119.0	3.873	0
3	115.5	110.0	119.0	3.873	0
5	121.3	116.0	124.0	3.594	0
10	129.0	125.0	131.0	2.708	0
30	157.8	155.0	160.0	2.217	0
50	197.0	191.0	205.0	6.325	0
100	274.0	266.0	293.0	12.780	4
200	387.0	365.0	412.0	19.983	4