

***This application note is updated as new products are released. Please check with National Semiconductor for the latest version.***

Comlinear Corporation is a manufacturer and supplier of high-performance analog signal processing components. Comlinear's broad signal conditioning product line includes high-speed hybrid and monolithic operational amplifiers, buffers, video amplifiers, multiplexers, automatic gain control integrated circuits, track/hold amplifiers, and analog-to-digital converters. Comlinear continues as a leader in developing products offering exceptional performance, speed, quality, reliability and service.

## INTRODUCTION

This diskette is a collection of PSpice compatible models for Comlinear amplifiers. For additional information about SPICE Models supporting existing or new products, Comlinear customers can contact Comlinear Corporation directly at 1-800-776-0500. The SPICE Models found on this disk are created for use on an IBM compatible computer using analysis programs that accept Spice formats. Comlinear assumes no responsibility for designs created from these SPICE Models. These SPICE Model files model typical performance at room temperature. AC response is dominated by board layout and package parasitics at frequencies above 500 MHz. Before designs are released to production, Comlinear suggests that topologies be verified by prototyping the circuit. The part-to-part and over-temperature performance variations of Comlinear amplifiers are specified in current data sheets (see Reference page 3). The changes from the last SPICE Model diskette version are listed in this table:

**TABLE I. UPDATES TO SPICE MODEL DISKETTE**

CLC405.CIR	A new SPICE Model.
CLC406.CIR	A revised SPICE Model.
CLC407.CIR	A new SPICE Model.
CLC412.CIR	A new SPICE Model.
CLC430.CIR	A revised SPICE Model that improves disabled output response.
CLC440.CIR	A new SPICE Model.
CLC449.CIR	A new SPICE Model.

**TABLE II. SPICE MODEL SUBCIRCUIT FILES ON THIS DISKETTE**

File Name	Description
CLC109.CIR	A Low-Power, Wideband, Closed-Loop Buffer.
CLC111.CIR	A Very Wideband, Ultra-High Slew Rate, Closed-Loop Buffer.
CLC400.CIR	A Wideband, Low-Gain Monolithic Current Feedback Op Amp with Fast Settling, (0.05% in 12ns), Low Power and an Input Offset Adjustment Pin.
CLC401.CIR	A Wideband, High-Gain Monolithic Current Feedback Op Amp with Fast Settling (0.01% in 10ns) and Low Power.
CLC402.CIR	A Low-Gain Monolithic Current Feedback Op Amp with Fast 14-bit Settling (0.0025% in 25ns) and Low Power.

CLC404.CIR	A Wideband Monolithic Current Feedback Op Amp with High Slew Rate.
CLC405.CIR	A Low-Cost, Low Power, 110MHz Op Amp with Disable.
CLC406.CIR	A Wideband Low-Cost, Low-Power Monolithic Current Feedback Op Amp.
CLC407.CIR	A Low-Cost, Low Power, Programmable Gain Buffer with Disable.
CLC409.CIR	A Very Wideband, Low Distortion Monolithic Current Feedback Op Amp.
CLC410.CIR	A Video Monolithic Current Feedback Op Amp with disable, Fast Settling (0.05% in 12ns) and an Input Offset Adjust Pin.
CLC412.CIR	A Dual Wideband Video Op Amp.
CLC414.CIR	A Quad, Low-Power Monolithic Current-Feedback Op Amp.
CLC415.CIR	A Quad Wideband Monolithic Current Feedback Op Amp.
CLC420.CIR	A High-Speed, Unity Gain Stable Monolithic Voltage Feedback Op Amp.
CLC425.CIR	An Ultra Low-Noise, Wideband Monolithic Voltage Feedback Op Amp with Current Supply Adjust.
CLC426.CIR	An Ultra Low-Noise, Wideband Monolithic Voltage Feedback Op Amp with Current Supply Adjust and External Compensation.
CLC428.CIR	An Ultra Low-Noise, Wideband, Dual Monolithic Voltage Feedback Op Amp.
CLC430.CIR	A Wideband Monolithic Current Feedback Op Amp with disable and $\pm 5V$ to $\pm 15V$ supply capability.
CLC431.CIR	A Dual, Wideband Monolithic Current Feedback Op Amp with high slew rate.
CLC432.CIR	A Dual, Wideband Monolithic Current Feedback Op Amp with disable and $\pm 5V$ to $\pm 15V$ supply capability.
CLC440.CIR	A High-Speed, Low-Power Voltage Feedback Op Amp.
CLC449.CIR	A 1.2GHz Ultra-Wideband Monolithic Op Amp.
CLC501.CIR	A High-Speed Output Clamping Monolithic Current Feedback Op Amp for high gains.
CLC502.CIR	A High-Speed Output Clamping Monolithic Current Feedback Op Amp with Fast 14-bit Settling (0.0025% in 25ns) for low gain.
CLC505.CIR	A High-Speed, Programmable-Supply Current, Monolithic Current Feedback Op Amp.
CLC520.CIR	A Monolithic Amplifier with Voltage Controlled Gain (AGC).
CLC522.CIR	A Monolithic Wideband Variable Gain Amplifier.
CLC532.CIR	A High-Speed, 2:1 Analog Multiplexer with fast 12-bit settling (0.01% in 17ns), low noise, low distortion and adjustable noise bandwidth.

**SPICE Models are found in the root directory of the data disk supplied.**

## START UP INSTRUCTIONS

Make a backup copy of the SPICE files contained on this disk to another floppy. Copy all SPICE Model files of interest to a library on the hard disk. If the library directory is not in the SPICE program's path, the user should set that path in the autoexec.bat for easier access. The .INC statement in PSpice should be used in the simulation file to include the SPICE Models subcircuit.

Example: ".INC CLC400.CIR"

## AMPLIFIER SPICE MODELS

These SPICE Model files are written in ASCII file format for IBM-compatible PC's. They are compatible with PSpice and other Spice 2G simulators. For additional detailed information about using PSpice please contact MicroSim (See Reference below). Comlinear amplifier SPICE Models are written in a subcircuit format for easy incorporation into larger circuits. A listing of any amplifier subcircuit may be obtained by printing its CLC\*.CIR file to a local printer. The subcircuit node assignments match the device pin-outs as shown in the individual device data sheets. An example is an 8 pin op amp.

- Connections: NON-INVERTING INPUT PIN
- | INVERTING INPUT PIN
- || OUTPUT
- ||| +Vcc
- |||| -Vcc
- |||||
- .SUBCKT (NAME) 3 2 6 7 4

Some schematic capture software packages require a different pin connection order than what Comlinear uses. Changing the pin order in the .SUBCKT statement will not affect the SPICE Model performance.

## PERFORMANCE RESULTS

When substitutions of current feedback op amps are made for voltage feedback op amps, results may not be acceptable. Refer to Comlinear's application note OA-13 for a tutorial on current feedback op amp design.

## NOTICE

The information provided within these files and documents is believed to be reliable and correct. National assumes no responsibility for alterations, omissions or inaccuracies. National assumes no responsibility for the use of this information, and all use of such information shall be entirely at the user's own risk. National does not grant licenses or patent rights to any of the circuits described within this document.

## PARAMETERS MODELED

The following typical performance parameters are modeled by the SPICE Models.

### DC Effects

- VIO, IBI, IBN
- Supply current vs. supply voltages
- Common mode input/output voltage range
- Load current from supplies
- CMRR

### AC Effects < 500MHz

- Frequency response vs. gain & load
- Open loop gain & phase
- Noise
- Small signal input/output impedance

### Time Domain

- Rise/fall times
- Slew rates

### Special Features (where applicable)

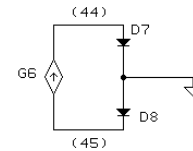
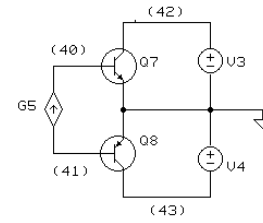
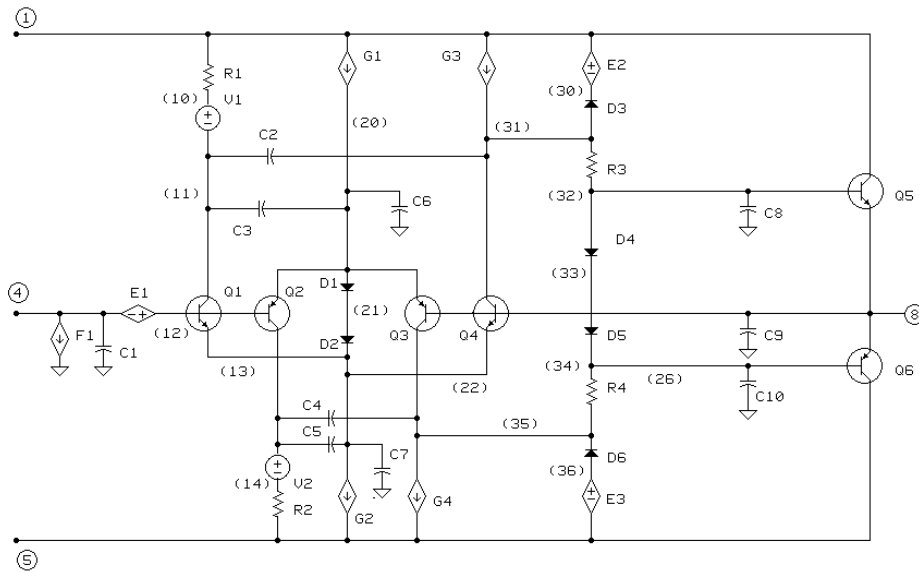
- Output clamping
- Supply current adjustment
- Offset voltage adjust
- Disable/enable times
- External compensation

## PARAMETERS NOT MODELED

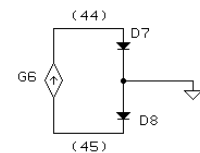
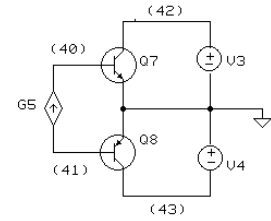
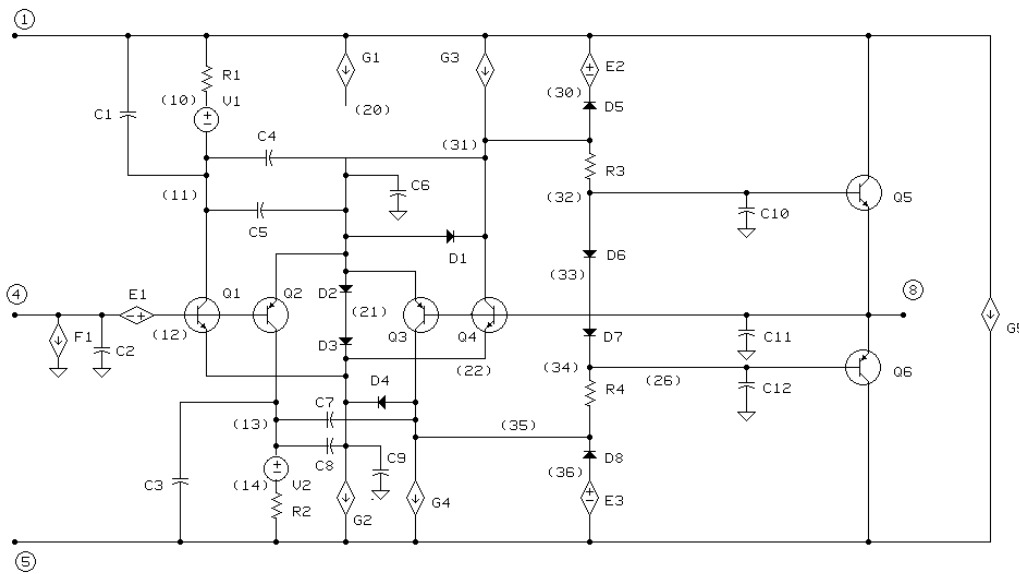
- Differential gain and phase
- PSRR
- Harmonic distortion
- Fine scale settling performance
- Thermal tail
- Overdrive recovery time (Except for the CLC501 and the CLC502)
- Variation in performance vs. temperature
- Part-to-part performance variation

## REFERENCES

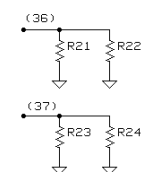
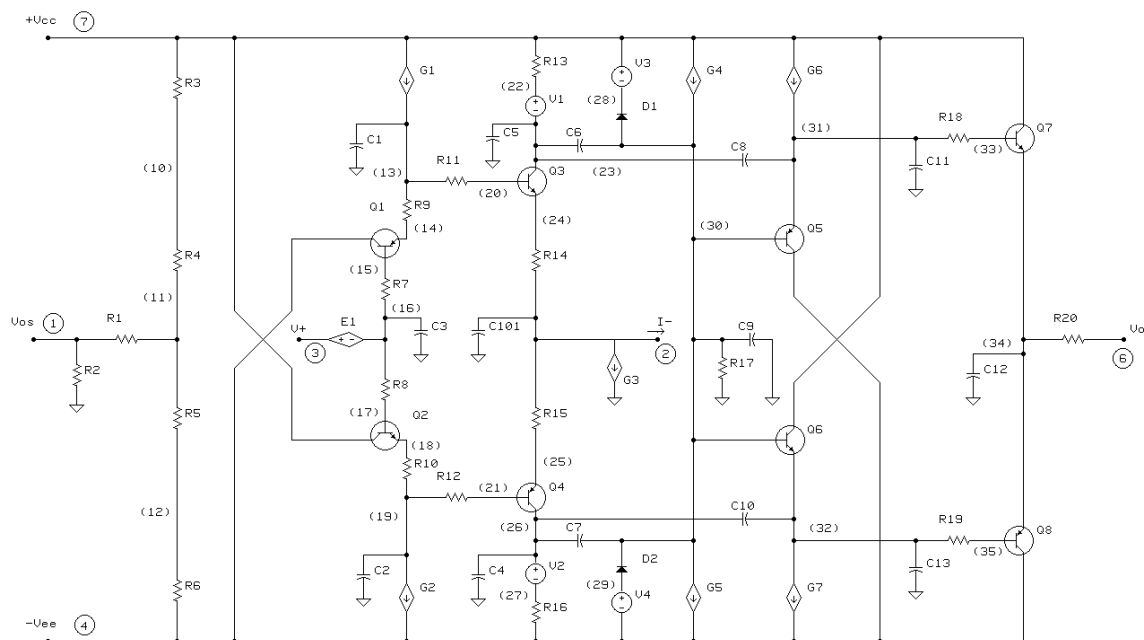
- 1) Comlinear's 1993/1994 Databook and 1995 Databook Supplement of standard products.
- 2) MicroSim Corporation, 20 Fairbanks, Irvine, CA 92718 USA, (714) 770-3022, (800) 245-3022.



**CLC109**

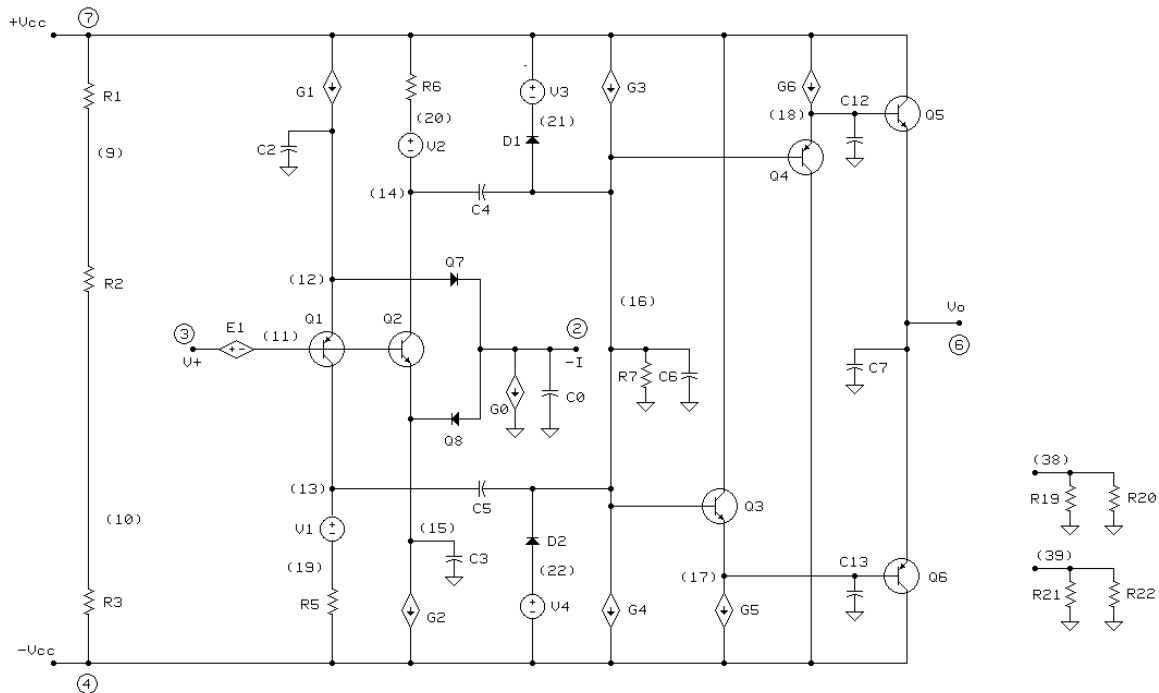


**CLC111**

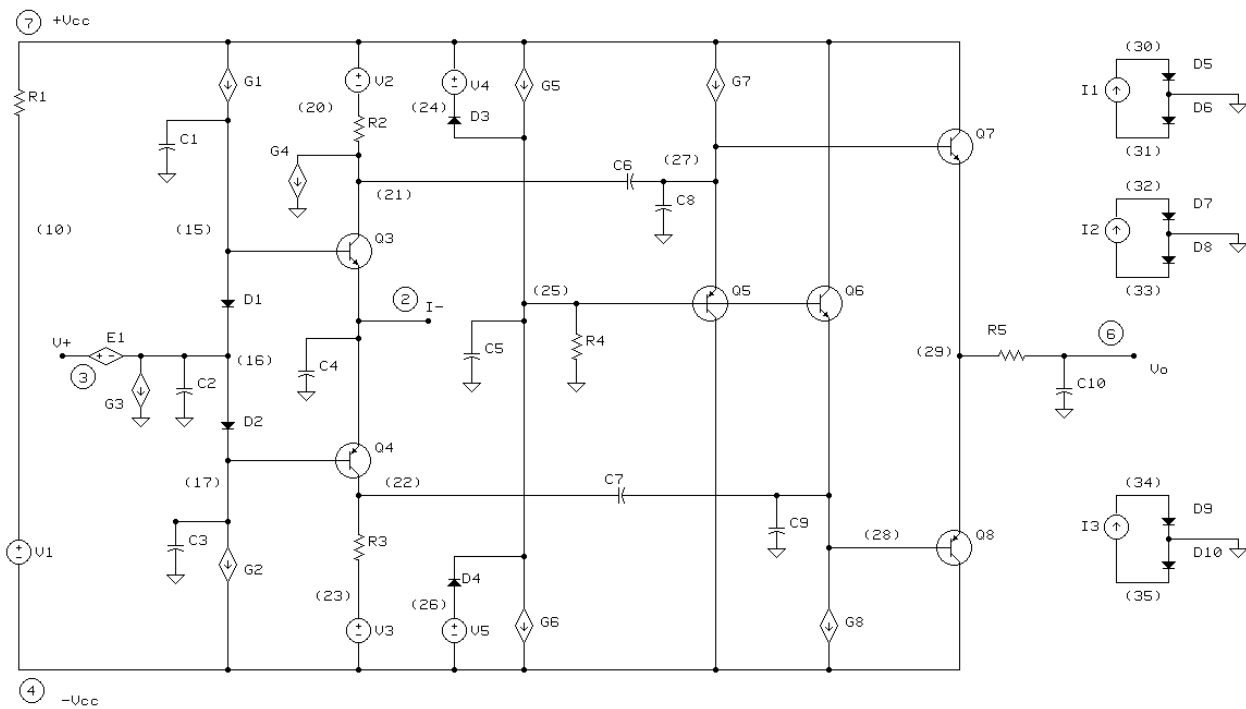


**CLC400**

**NOTE:** circled number denotes PIN number and number in parenthesis denotes NODE number.

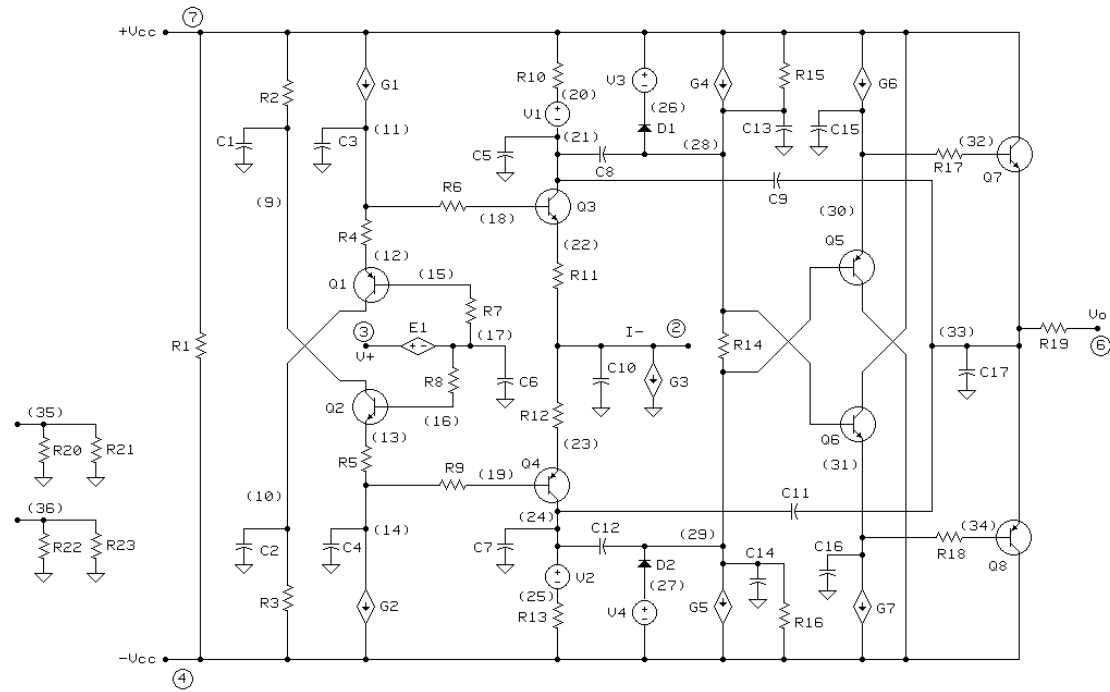


**CLC401**

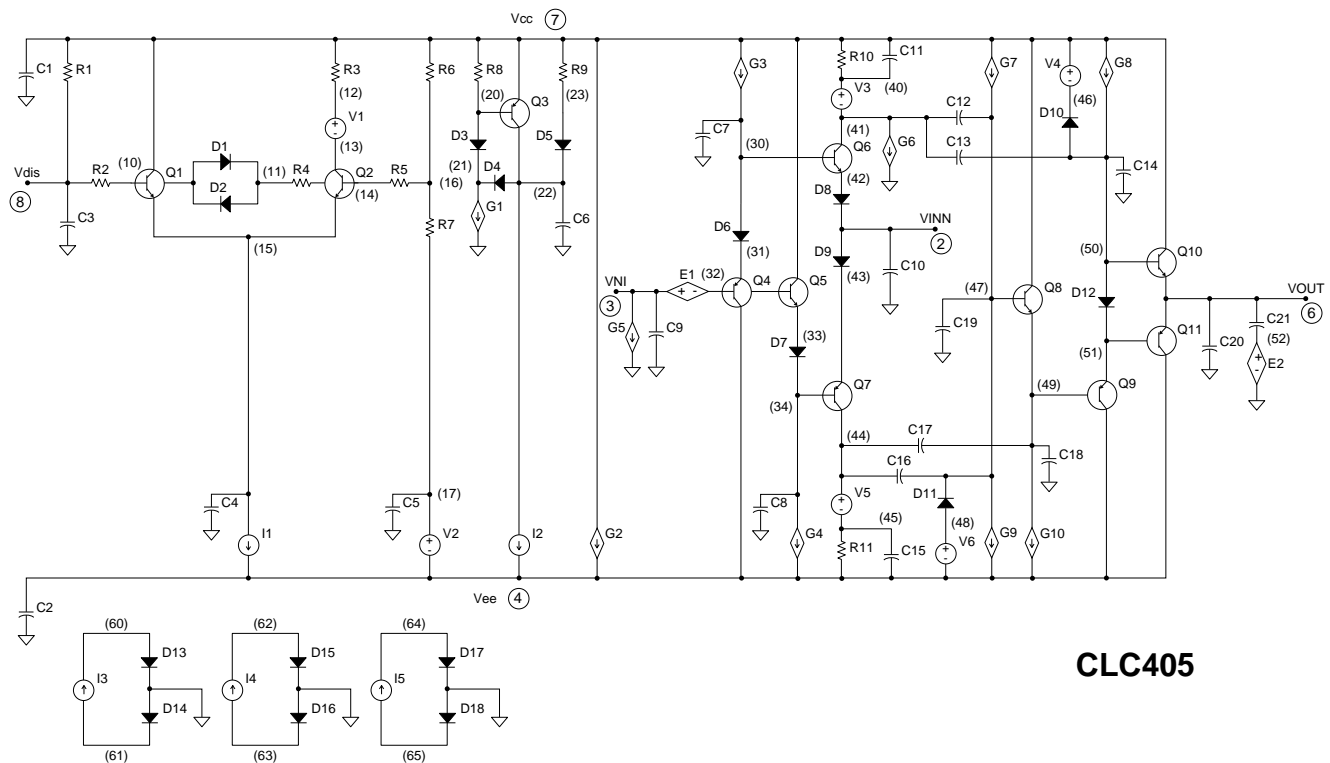


**CLC402**

**NOTE:** circled number denotes PIN number and number in parenthesis denotes NODE number.

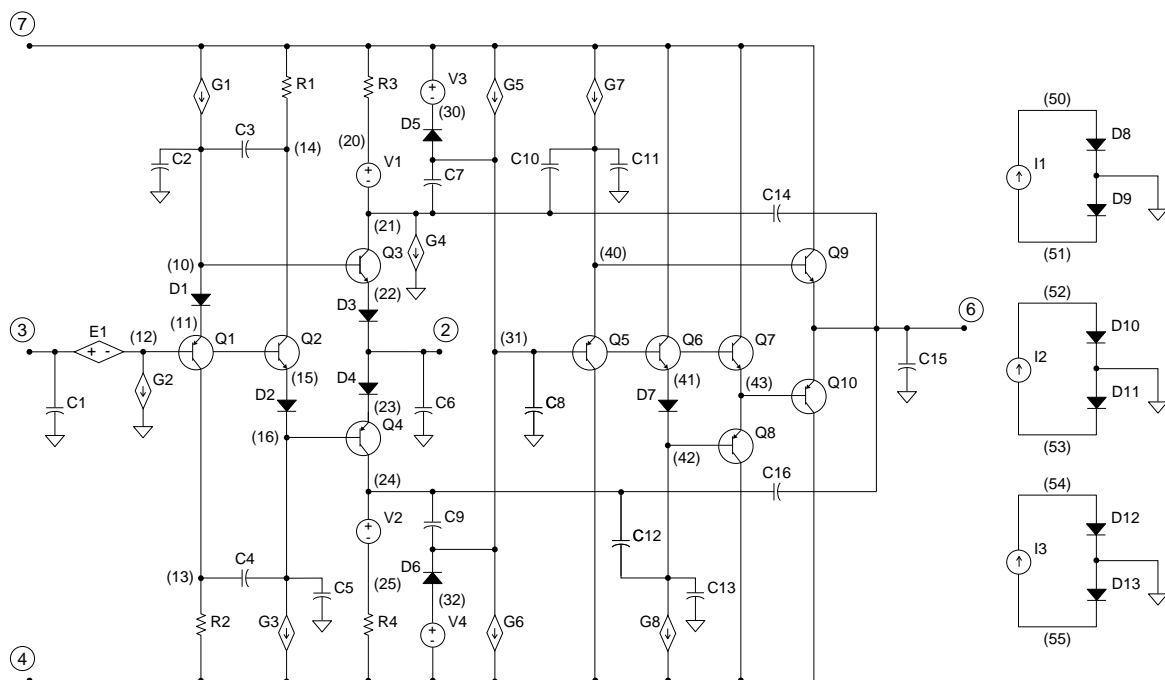


**CLC404**

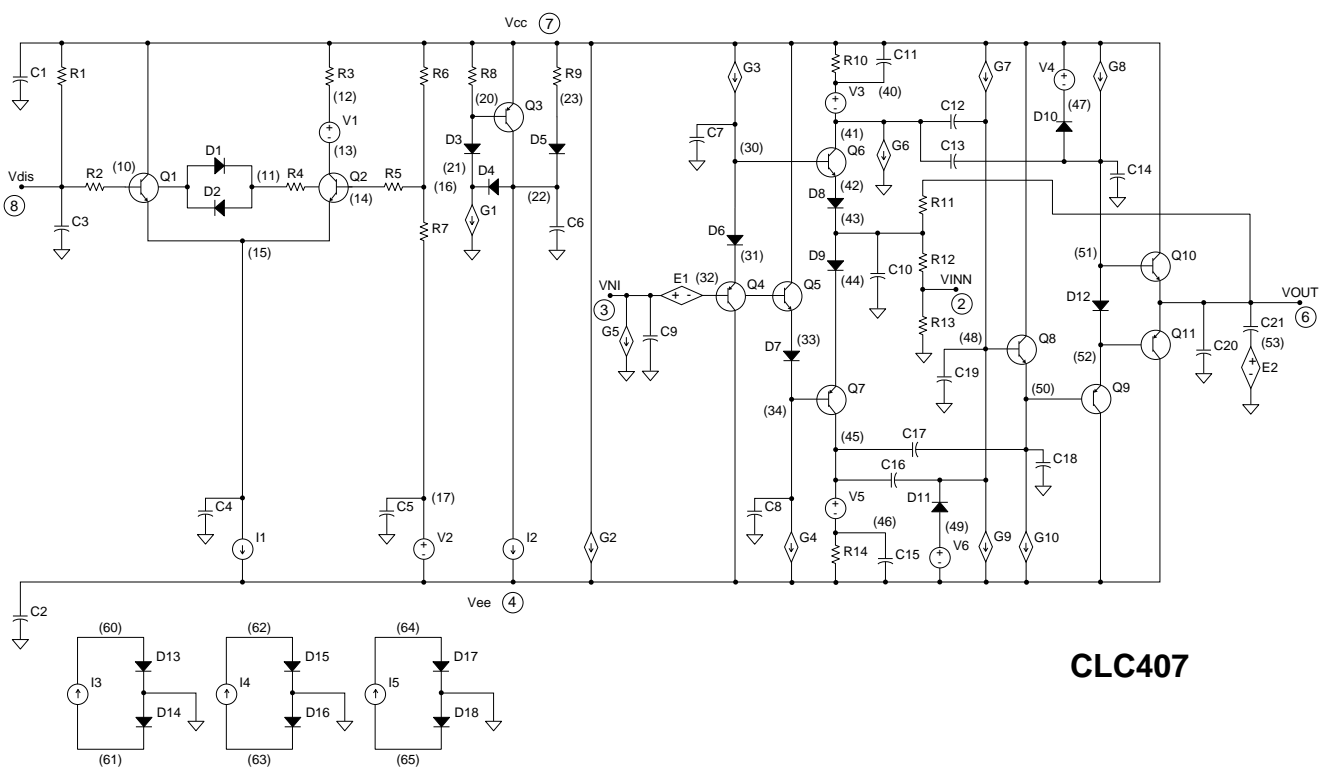


**CLC405**

**NOTE:** circled number denotes PIN number and number in parenthesis denotes NODE number.

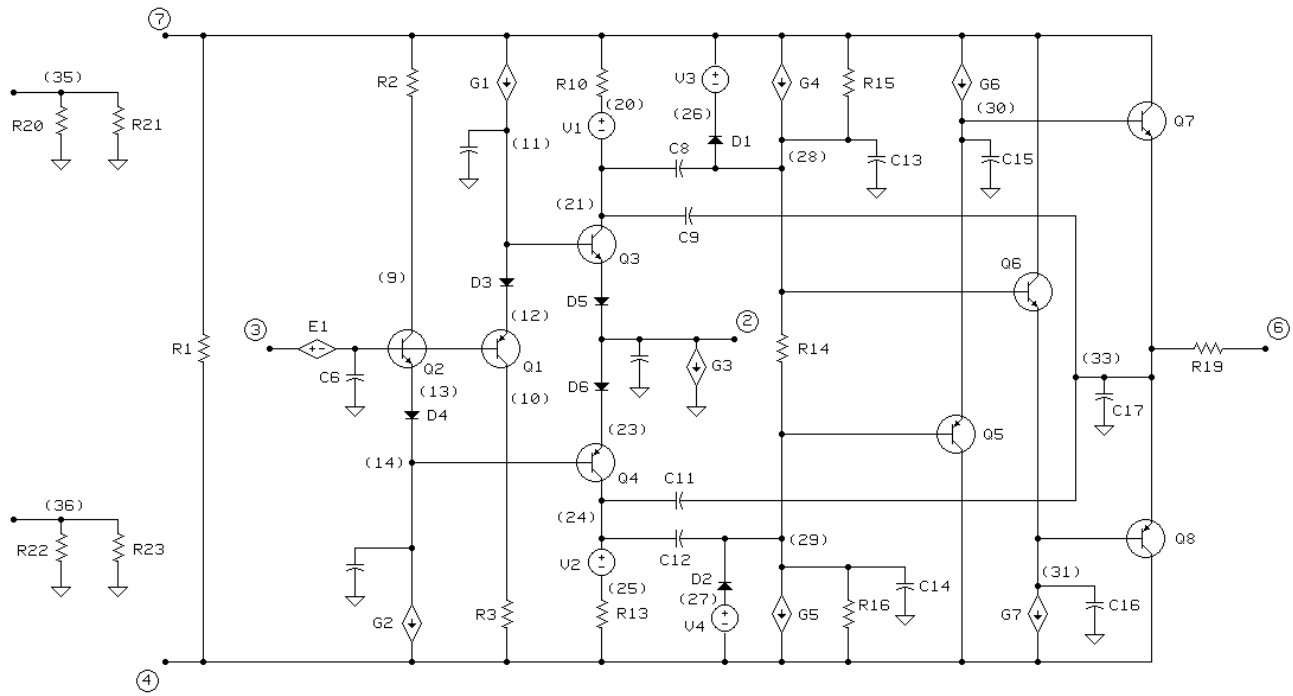


**CLC406**

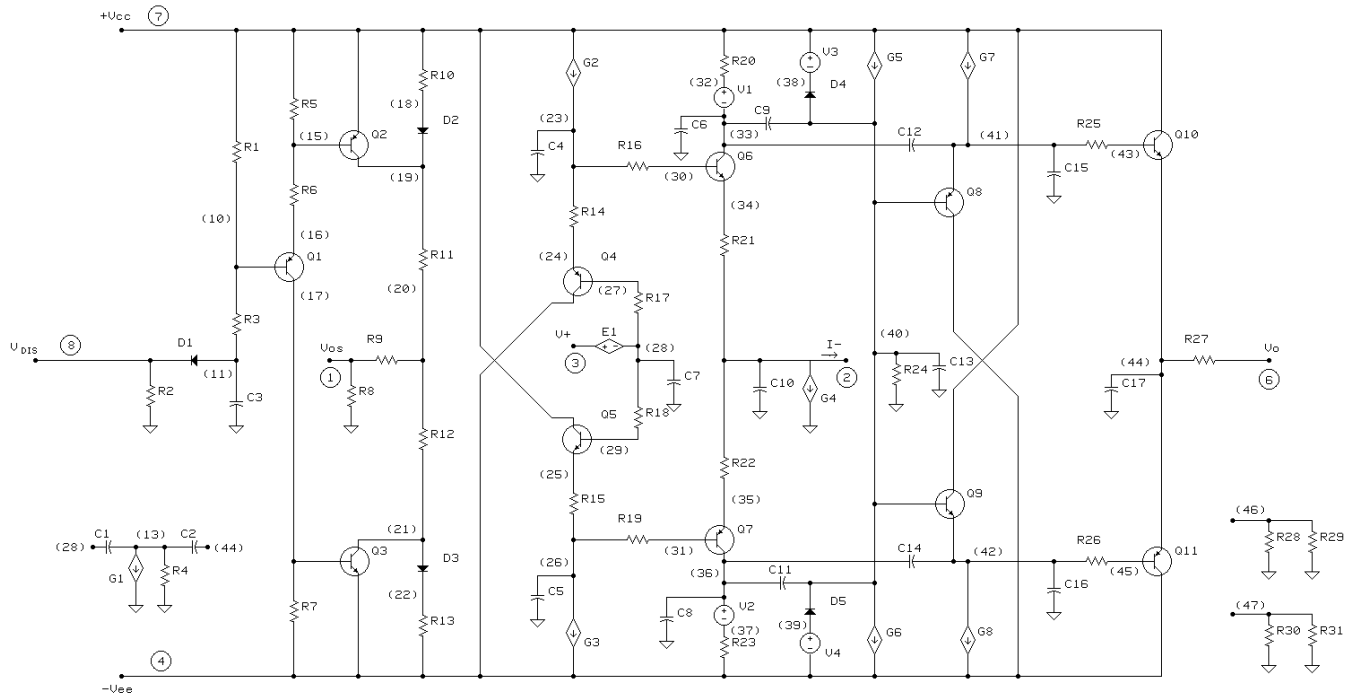


**CLC407**

**NOTE:** circled number denotes PIN number and number in parenthesis denotes NODE number.



**CLC409**



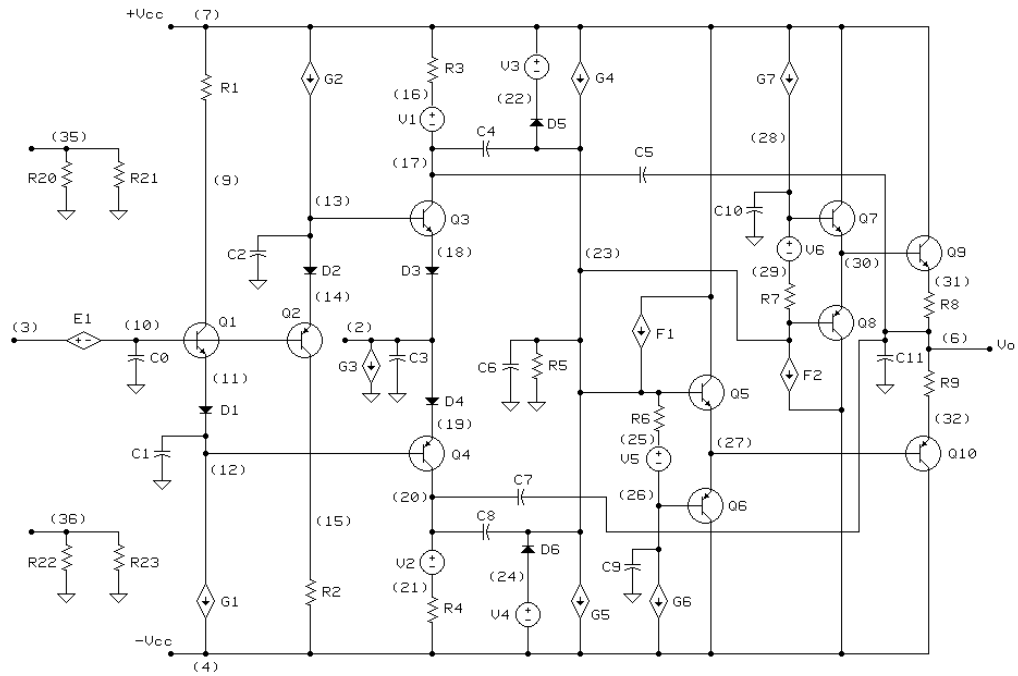
**CLC410**

**NOTE:** circled number denotes PIN number and number in parenthesis denotes NODE number.

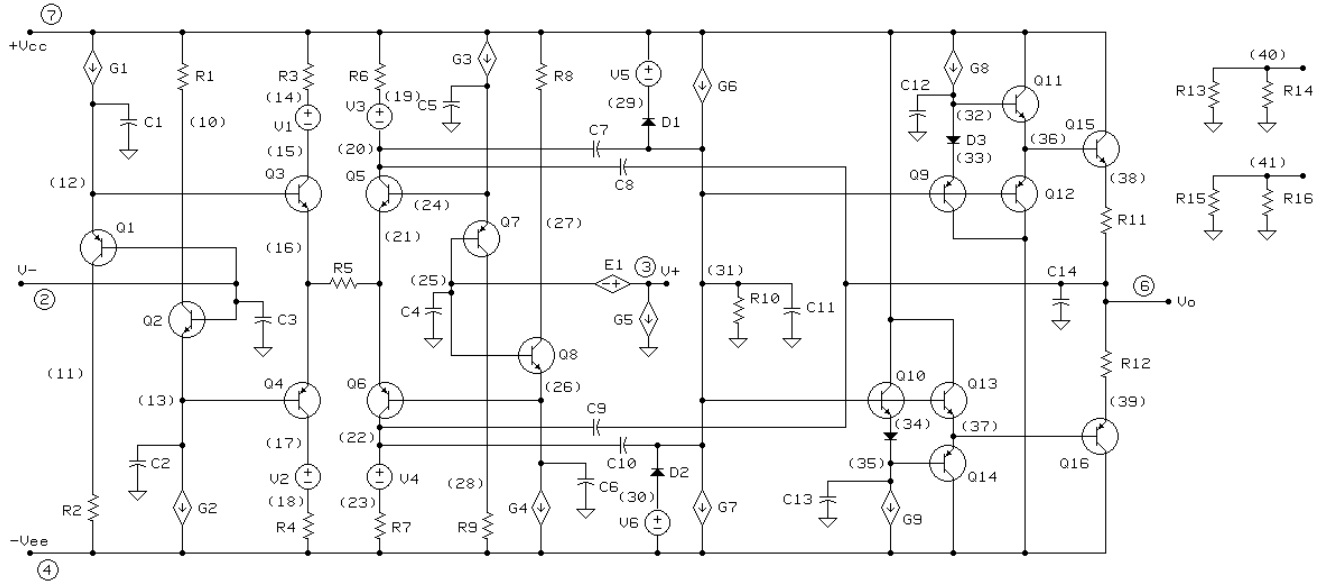




<http://www.national.com>

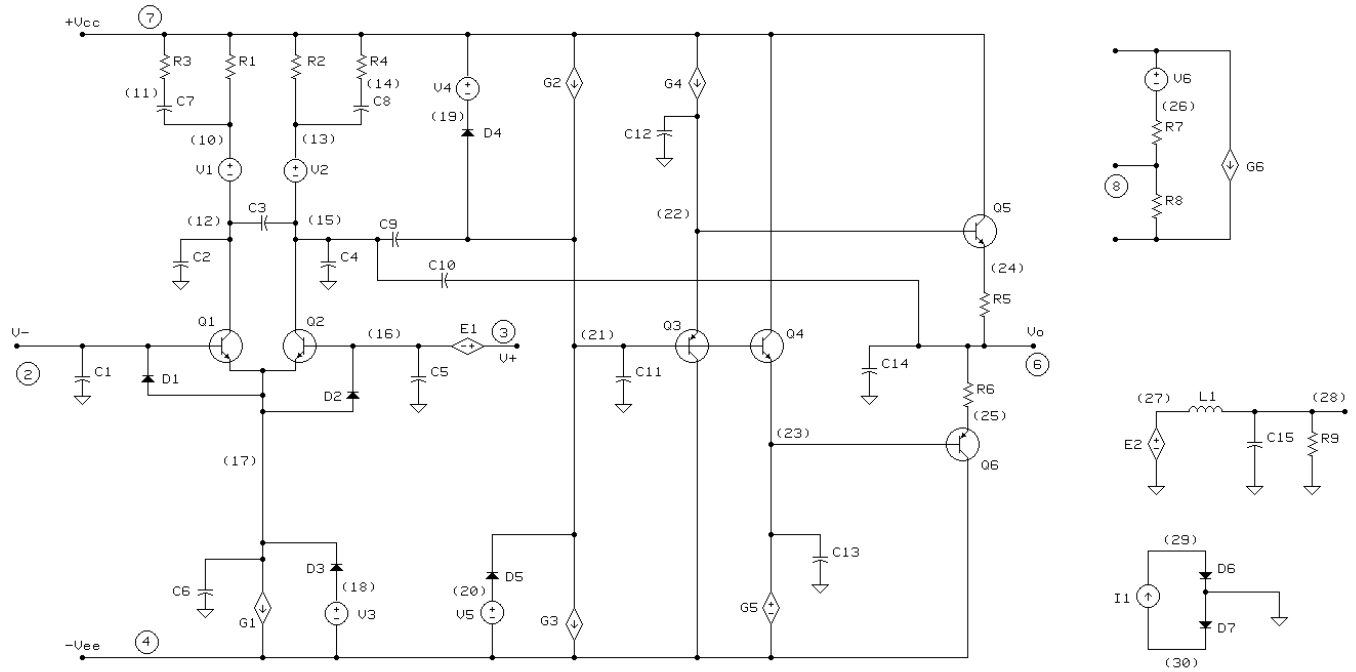


**CLC415**

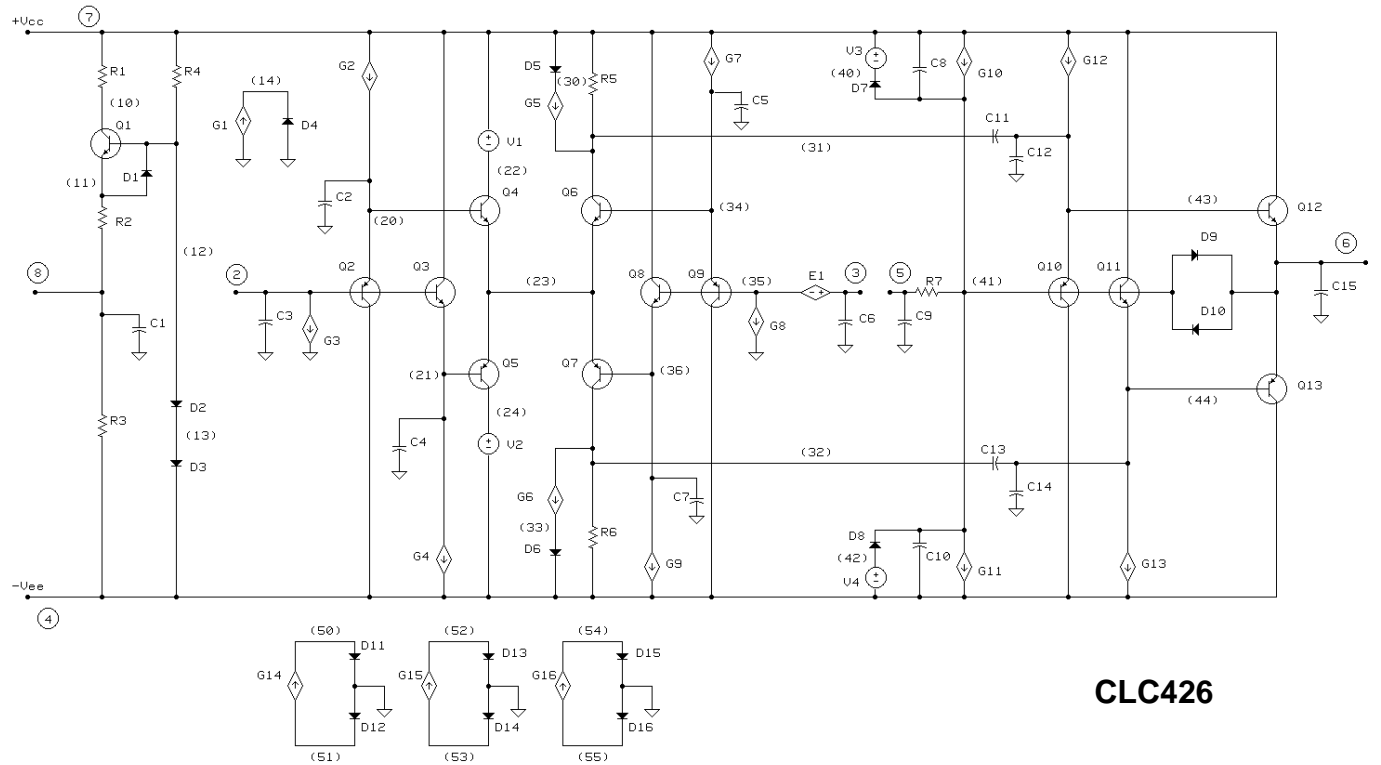


**CLC420**

**NOTE:** circled number denotes PIN number and number in parenthesis denotes NODE number.

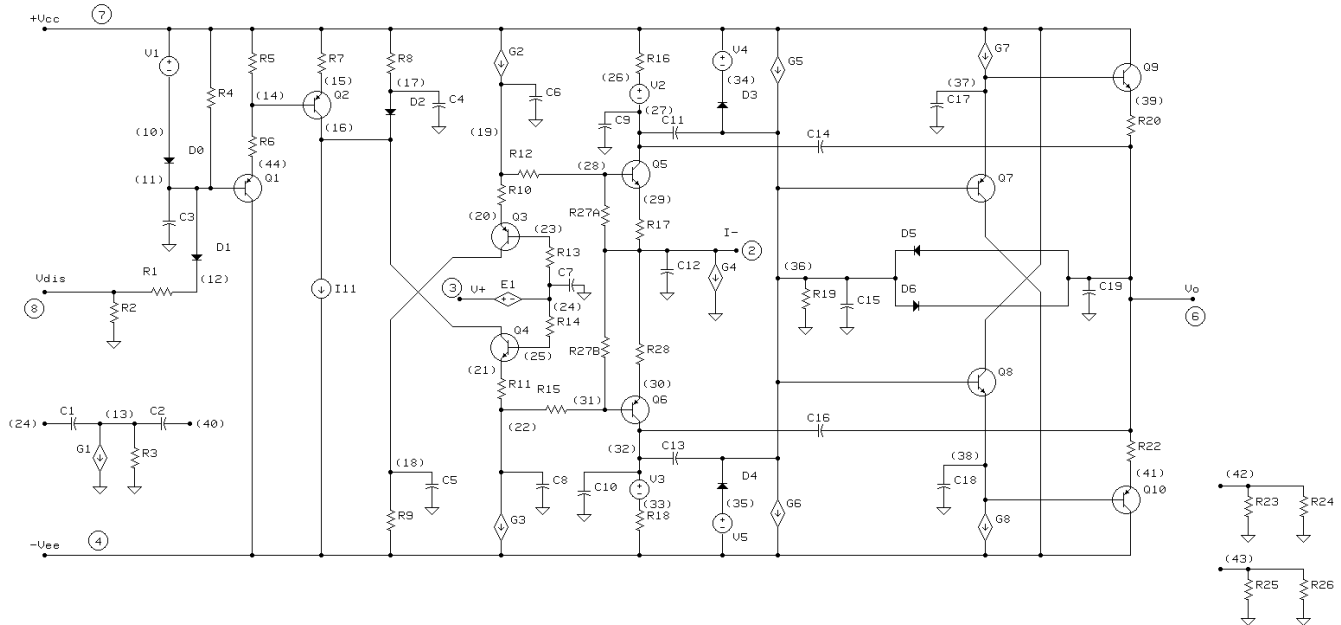
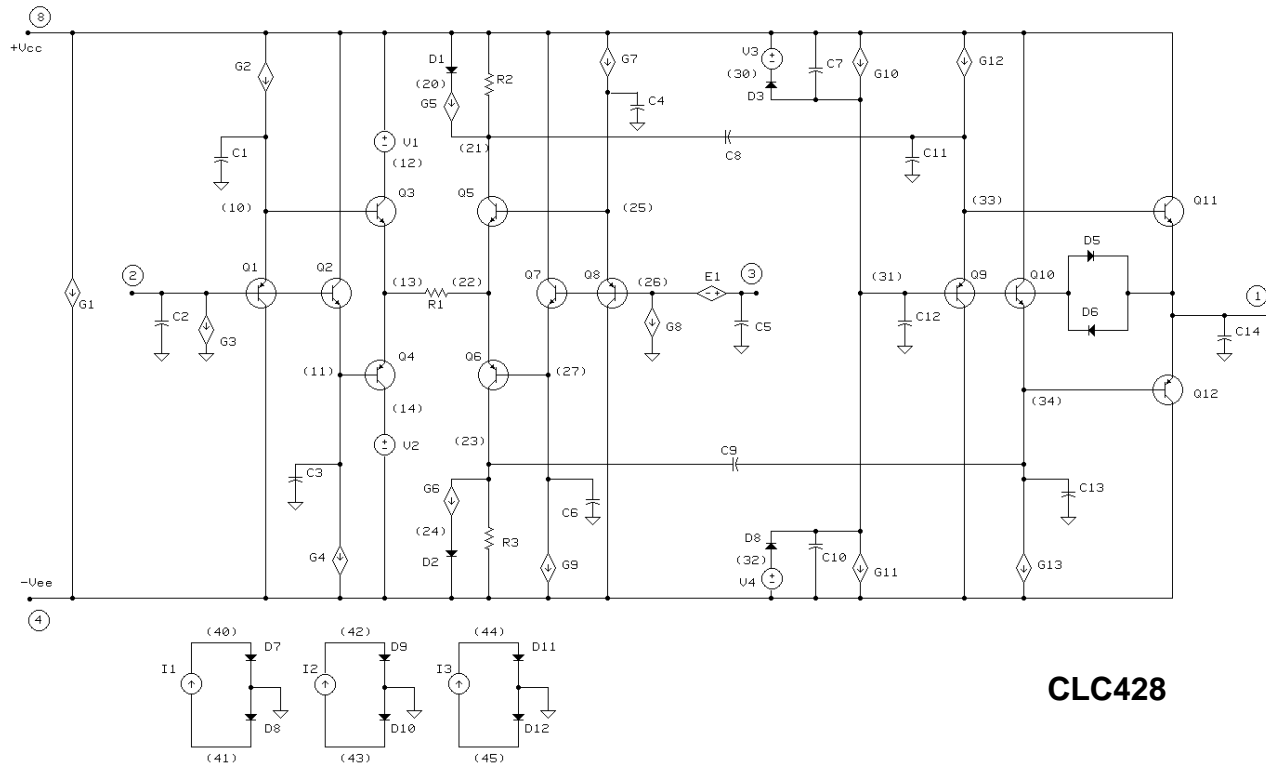


**CLC425**

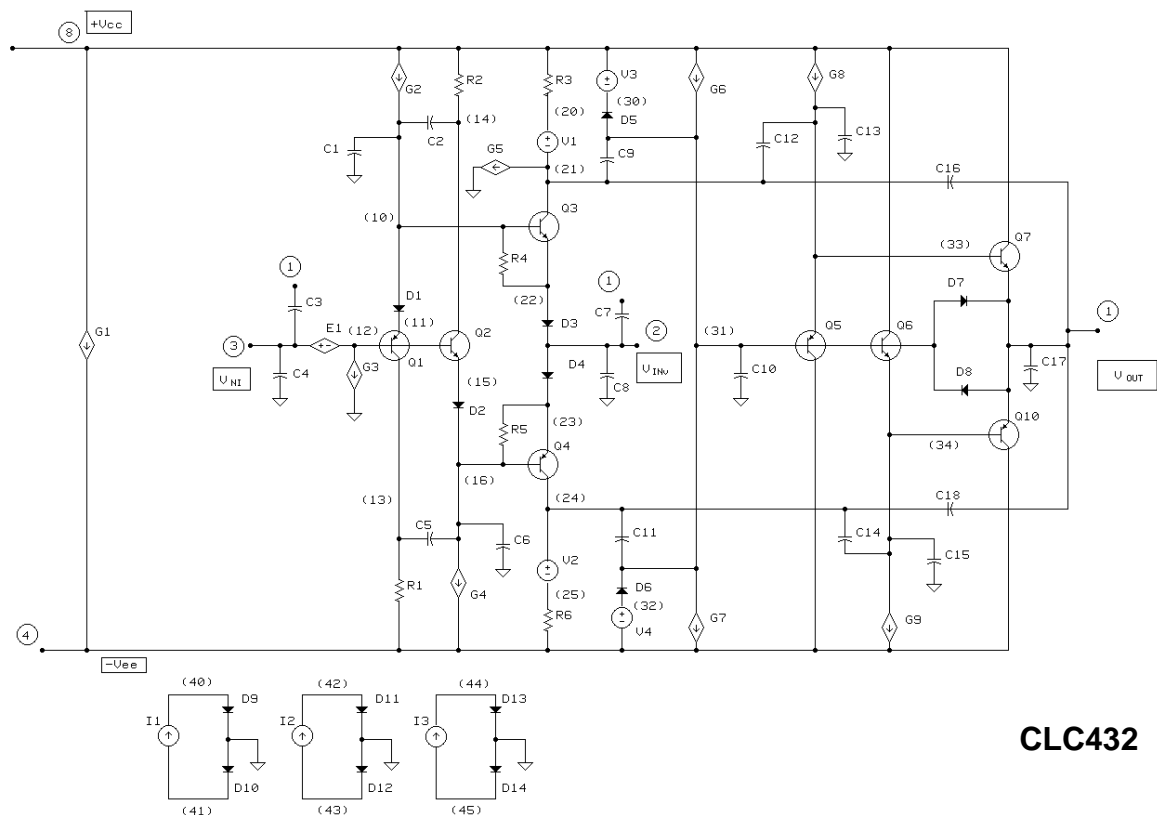
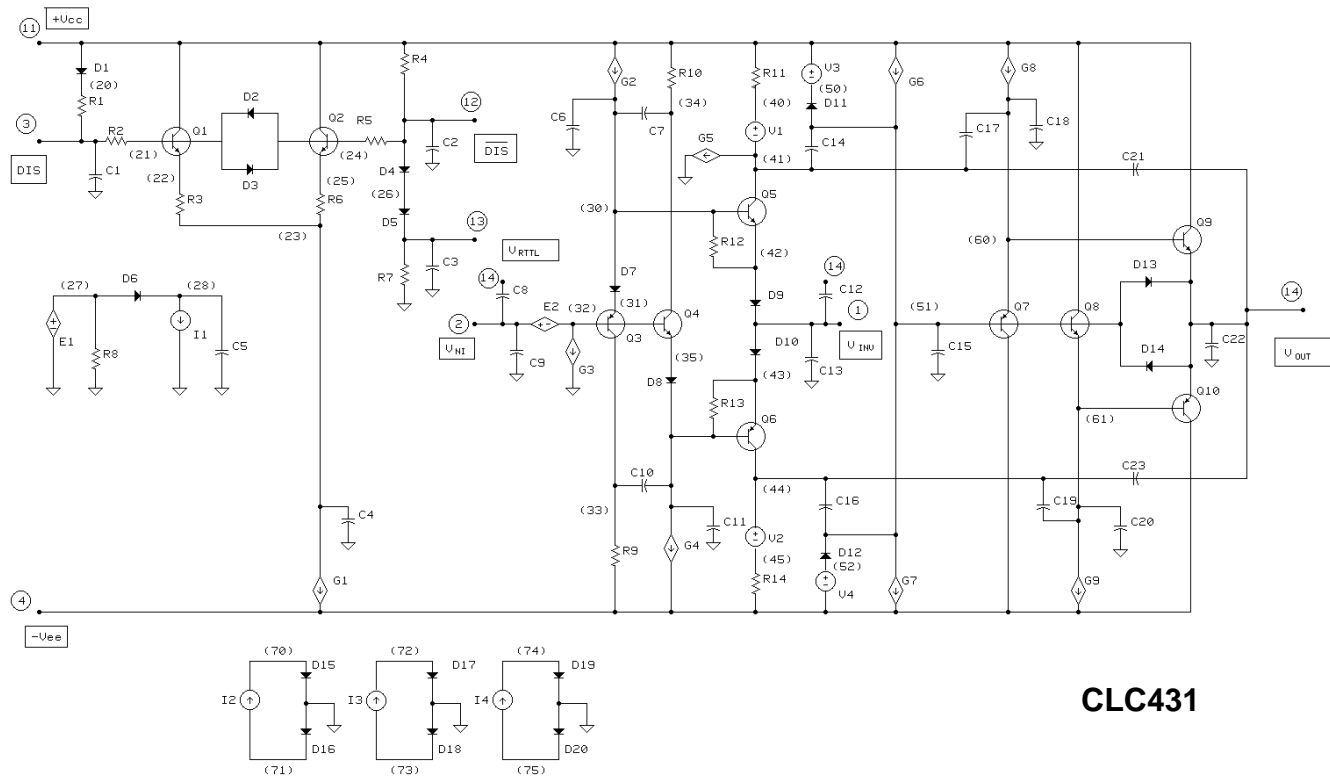


**CLC426**

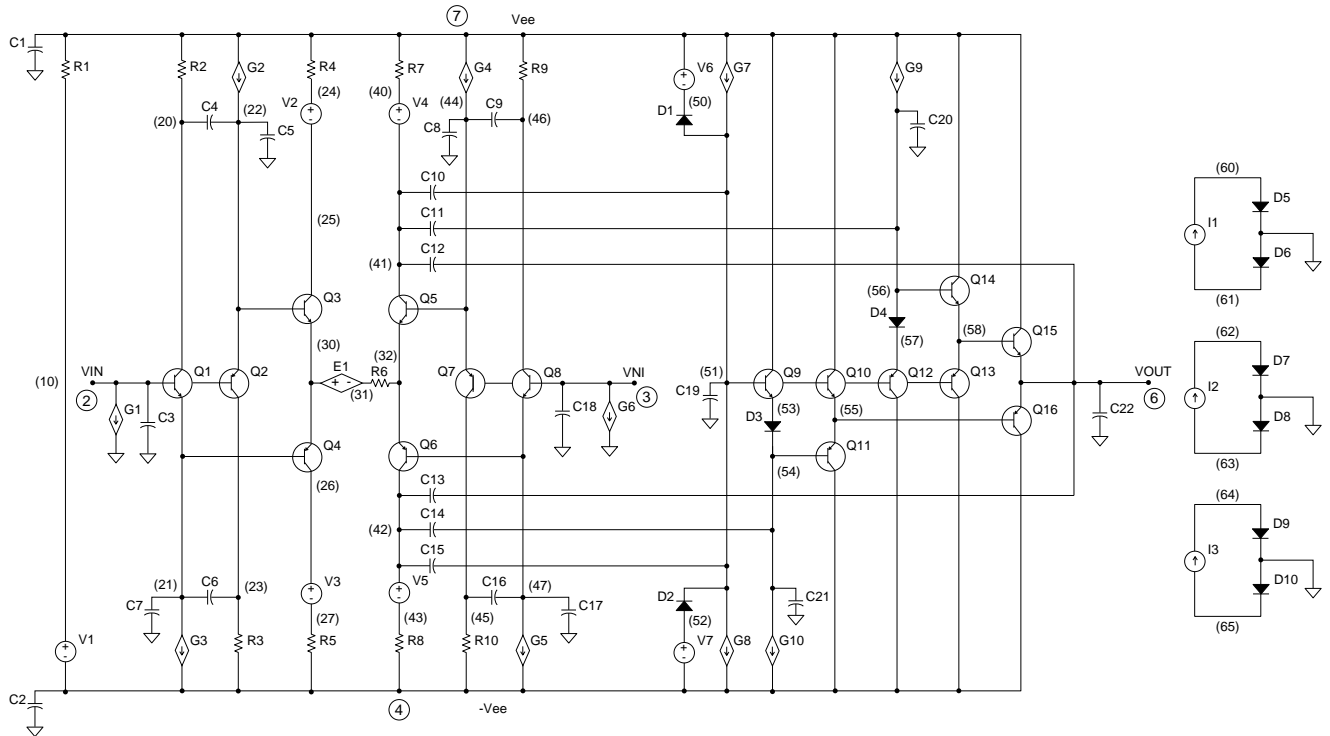
**NOTE:** circled number denotes PIN number and number in parenthesis denotes NODE number.



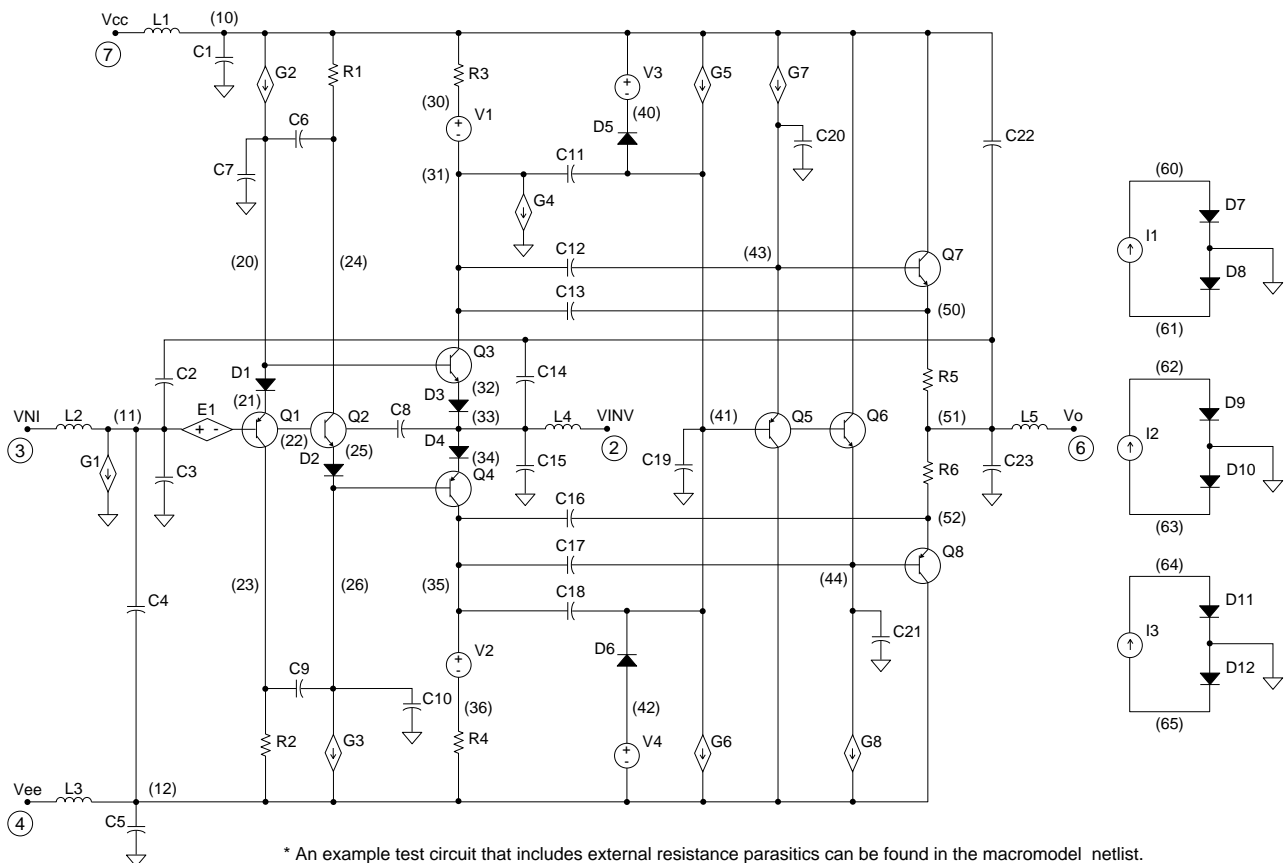
**NOTE:** circled number denotes PIN number and number in parenthesis denotes NODE number.



**NOTE:** circled number denotes PIN number and number in parenthesis denotes NODE number.



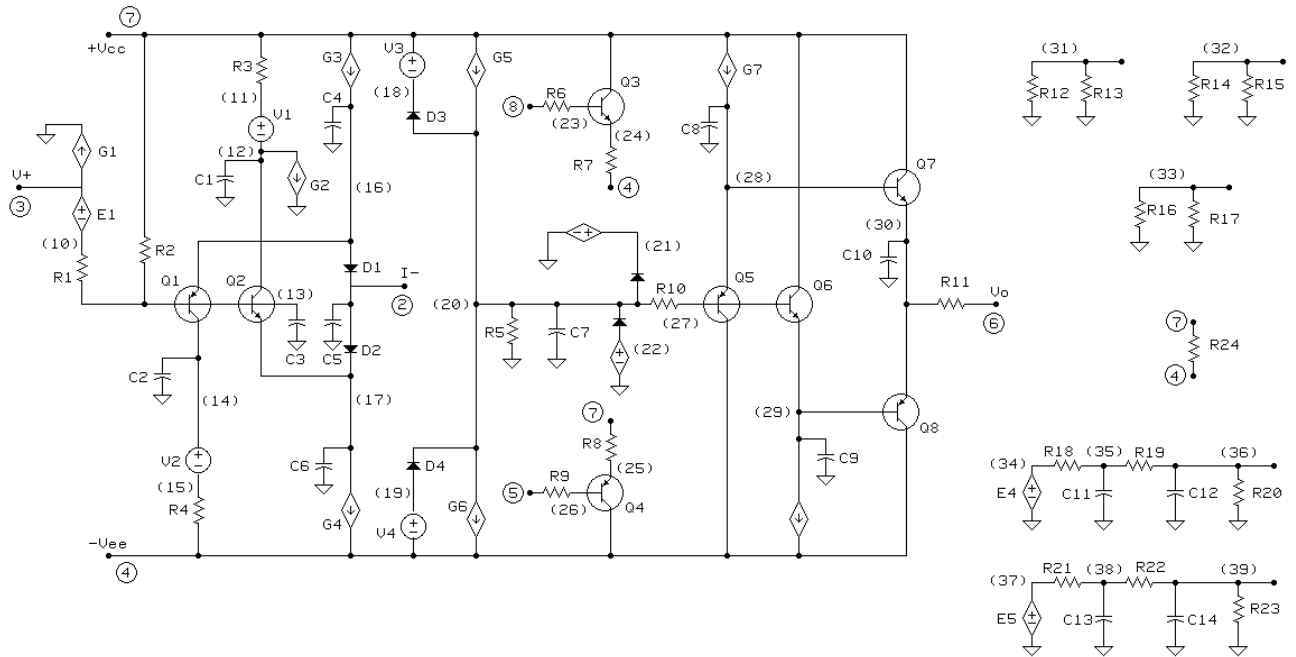
**CLC440**



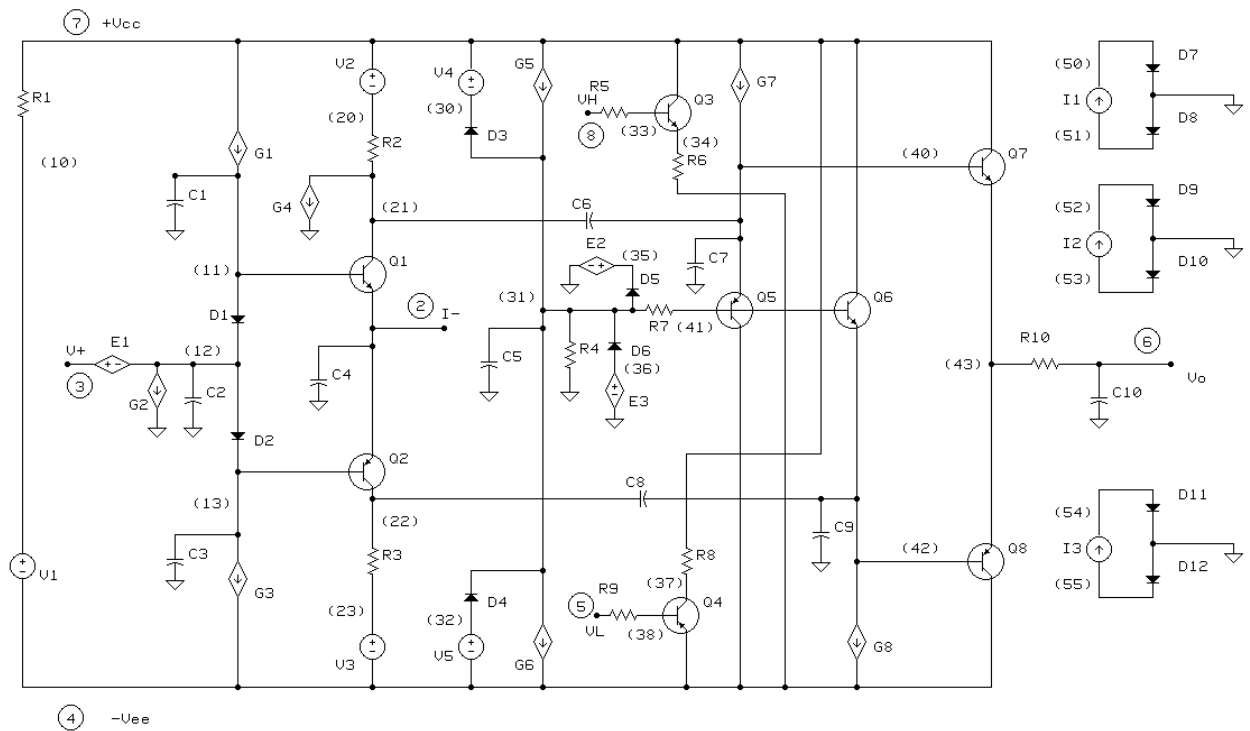
\* An example test circuit that includes external resistance parasitics can be found in the macromodel netlist.

**CLC449**

**NOTE:** circled number denotes PIN number and number in parenthesis denotes NODE number.

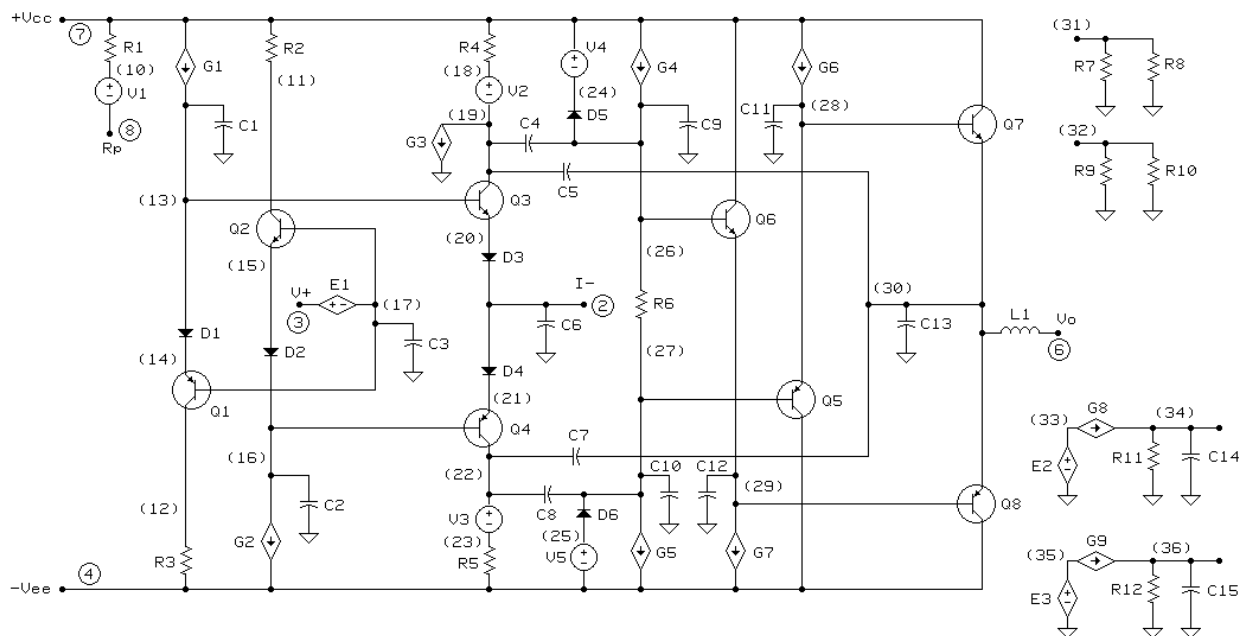


**CLC501**

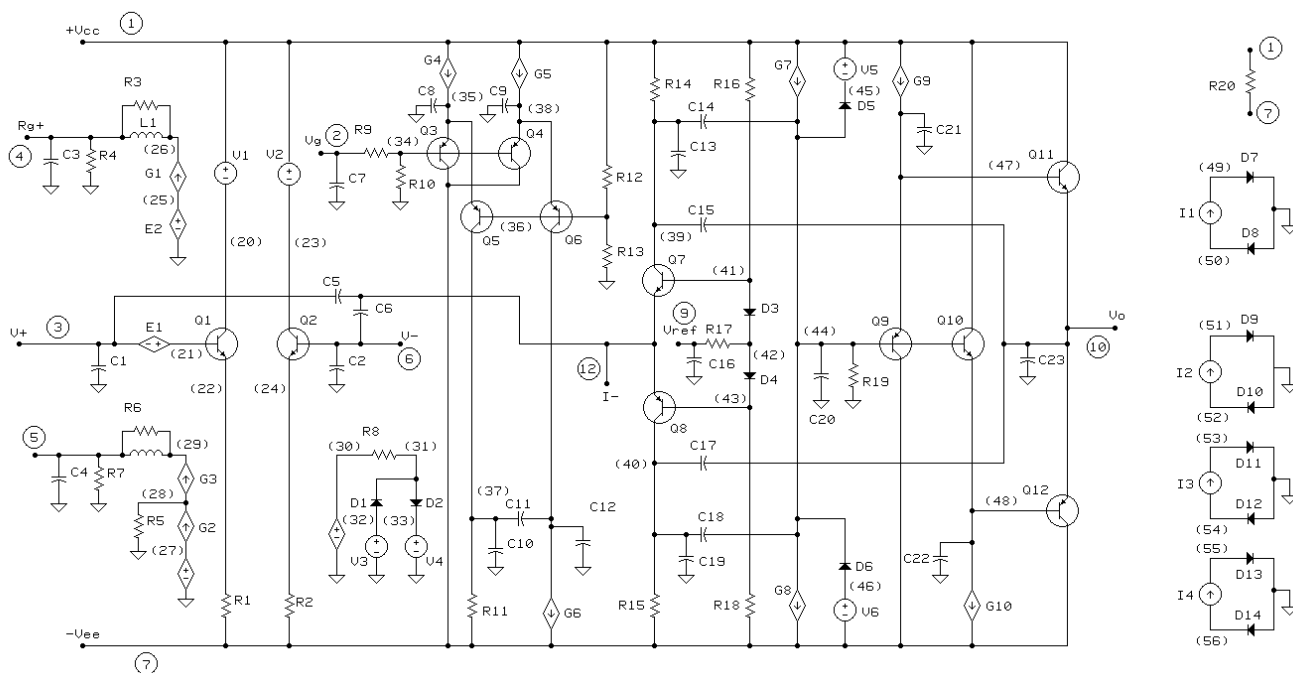


**CLC502**

**NOTE:** circled number denotes PIN number and number in parenthesis denotes NODE number.



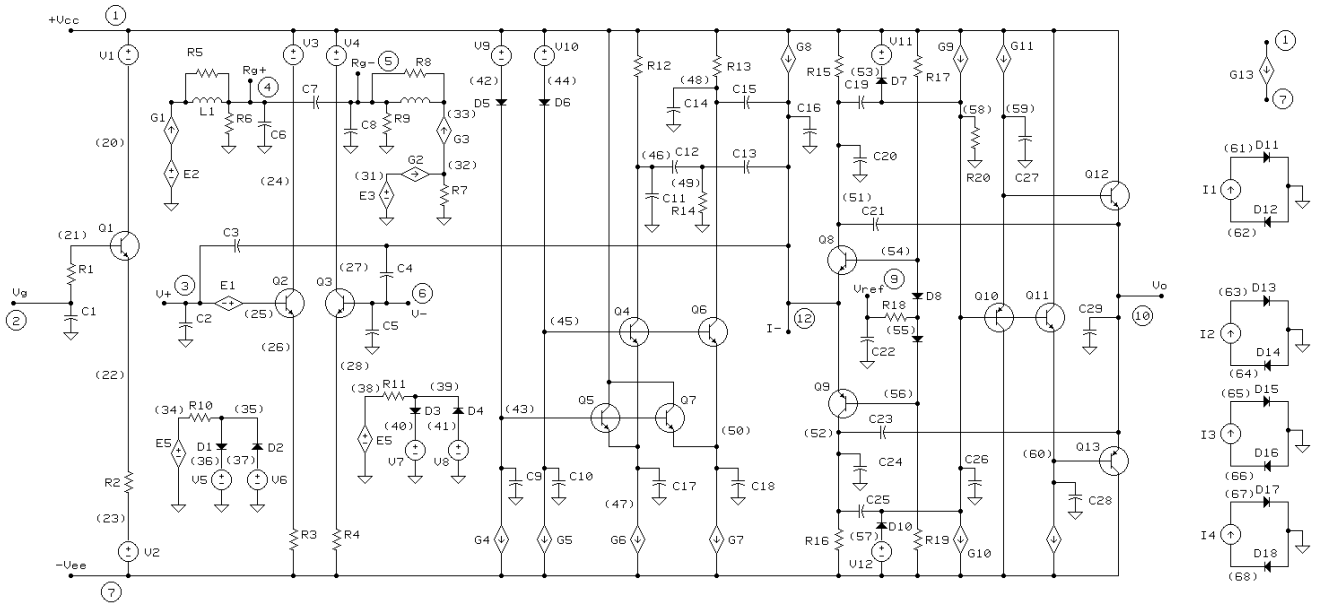
**CLC505**



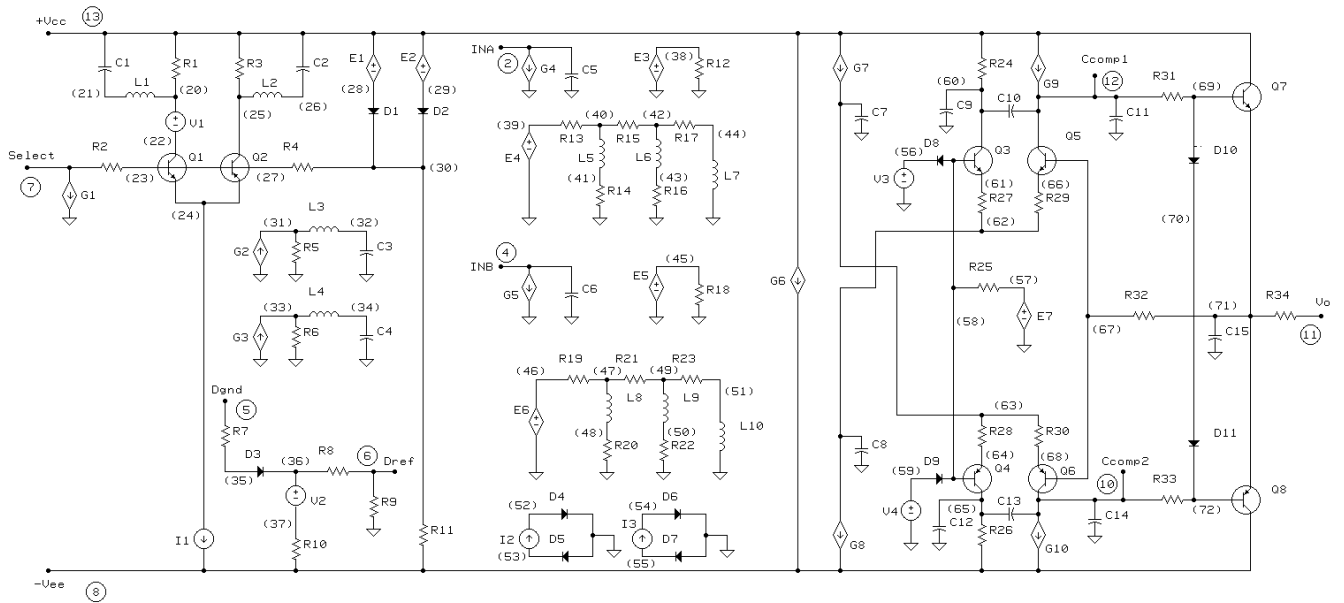
**CLC520**

**NOTE:** circled number denotes PIN number and number in parenthesis denotes NODE number.





**CLC522**



**CLC532**

**NOTE:** circled number denotes PIN number and number in parenthesis denotes NODE number.

**This page intentionally left blank.**

**This page intentionally left blank.**

---

## Customer Design Applications Support

National Semiconductor is committed to design excellence. For sales, literature and technical support, call the National Semiconductor Customer Response Group at **1-800-272-9959** or fax **1-800-737-7018**.

### Life Support Policy

National's products are not authorized for use as critical components in life support devices or systems without the express written approval of the president of National Semiconductor Corporation. As used herein:

1. Life support devices or systems are devices or systems which, a) are intended for surgical implant into the body, or b) support or sustain life, and whose failure to perform, when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.



#### National Semiconductor Corporation

1111 West Bardin Road  
Arlington, TX 76017  
Tel: 1(800) 272-9959  
Fax: 1(800) 737-7018

#### National Semiconductor Europe

Fax: (+49) 0-180-530 85 86  
E-mail: europe.support@nsc.com  
Deutsch Tel: (+49) 0-180-530 85 85  
English Tel: (+49) 0-180-532 78 32  
Francais Tel: (+49) 0-180-532 93 58  
Italiano Tel: (+49) 0-180-534 16 80

#### National Semiconductor Hong Kong Ltd.

13th Floor, Straight Block  
Ocean Centre, 5 Canton Road  
Tsimshatsui, Kowloon  
Hong Kong  
Tel: (852) 2737-1600  
Fax: (852) 2736-9960

#### National Semiconductor Japan Ltd.

Tel: 81-043-299-2309  
Fax: 81-043-299-2408

---

National does not assume any responsibility for use of any circuitry described, no circuit patent licenses are implied and National reserves the right at any time without notice to change said circuitry and specifications.