

Drawing Circles with the NS32CG16; NS32CG16 Note 1

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1.0 INTRODUCTION

The NS32CG16 is a 32-bit CMOS, graphics oriented processor. It is software compatible with other Series 32000® CPUs, with new instructions for high-speed graphics. The NS32CG16 is designed specifically for page-oriented printing technologies such as laser, LCS, LED, Ion-Deposition, and Ink Jet.

In this applications note, a method for high-speed circle generation will be described, using an optimized version of Bresenham's circle algorithm.

2.0 DESCRIPTION

A circle can be described by the center coordinates (xc, yc), the radius (r), and the width (w). With the Pythagorean theorem, pixels along the path described by the equation:

$$(x - xc)^2 + (y - yc)^2 = r^2$$

can be set for a width of w perpendicular to the tangent of the arc.

This, however, involves substantial computation for each point on the line. Even taking advantage of the symmetry of circles, a large number of instructions must be executed to calculate the path.

Bresenham's circle algorithm works by determining which of two pixels are nearer the actual circle at each step. Then, using symmetry, eight points on the circle's path can be determined. Applying the width (w) to each of these eight points yields a displayed (or imaged) circle. For the actual derivation of Bresenham's algorithm, see *Reference 1*, and *Reference 2*. This derivation was done by J. Michener.

Bresenham's algorithm can be implemented in the following manner:

1. Select the first position for display as

$$(x_1, y_1) = (0, r)$$

2. Calculate the first parameter as

$$p_1 = 3 - 2r$$

If $p_1 < 0$, the next position is $(x_1 + 1, y_1)$. Otherwise, the next position is $(x_1 + 1, y_1 - 1)$.

3. Continue to increment the x coordinate by unit steps, and calculate each succeeding parameter p from the preceding one. If for the previous parameter we found that $p_i < 0$ then

$$p_{i+1} = p_i + 4x_i + 6$$

Otherwise (for $p_i \geq 0$),

$$p_{i+1} = p_i + 4(x_i - y_i) + 10$$

Then, if $p_{i+1} < 0$ the next point selected is $(x_i + 2, y_{i+1})$. Otherwise, the next point is $(x_i + 2, y_{i+1} - 1)$. The y coordinate is $y_{i+1} = y_i$, if $p_i < 0$ or $y_{i+1} = y_i - 1$, if $p_i \geq 0$.

4. Repeat the procedures in step 3 until the x and y coordinates are equal.

3.0 IMPLEMENTATION

With the path of the circle described, the pixels along the path can be set using the basic symmetry of the circle. Following is an example of Bresenham's circle algorithm in the C language, based on Michener's derivation.

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```
circle(xc,yc,radius,width)
register unsigned int xc,yc,radius,width;
{
    register int y, x, p;
    x = 0;
    y = radius;
    p = 3 - 2 * radius;
    while (x < y) {
        setgrp(xc,yc,x,y,width);
        if (p < 0)
            p += 4 * x + 6;
        else {
            p += 4 * (x - y) + 10;
            y--;
        }
        x++;
    }
    if (y == x)
        setgrp(xc,yc,x,y,width);
}
```

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```
setgrp(xc,yc,x,y,width)
register int xc,yc,x,y,width;
{
    if ((y - x) <= (width / 2) {
        hset(xc + y, yc + x,width);
        hset(xc - y, yc + x,width);
        hset(xc + y, yc - x,width);
        hset(xc - y, yc - x,width);
        vset(xc + x, yc + y,width);
        vset(xc - x, yc + y,width);
        vset(xc + x, yc - y,width);
        vset(xc - x, yc - y,width);
    }
    vset(xc + y, yc + x,width);
    vset(xc - y, yc + x,width);
    vset(xc + y, yc - x,width);
    vset(xc - y, yc - x,width);
    hset(xc + x, yc + y,width);
    hset(xc - x, yc + y,width);
    hset(xc + x, yc - y,width);
    hset(xc - x, yc - y,width);
}
```

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The *setgrp* routine in the previous example uses symmetry to set eight points of the circle. *Setgrp* has a special case to handle the boundaries of the eight sections. When the distance between the boundaries is less than half the width of the circle, both vertical and horizontal lines are imaged for each section. The *vset* routine sets *width* pixels vertically in the image, centered around the second argument. The *hset* routine sets *width* pixels horizontally, centered around the first argument. Since these cases are so well defined, the NS32CG16 instructions *SBITPS* and *SBITS* are used for these routines.

The NS32CG16 implementation is very much like the C version, but is optimized for speed. Note the use of the *ADDR* instruction to do the two p_i computations, each in one line of 32000 assembly code.

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```

.data
xwarp: equ 2544          #bits of xwarp to get to next scan
.comm _page,4
hlfdwth:double 0
.text
#
#Bresenham's circle algorithm, as expressed in "Computer Graphics" by
#Donald Hearn and M. Pauline Baker (1986, Prentice-Hall,
#ISBN 0-13-165382-2)
#
# Inputs:
#      r0 = x coordinate of centre of circle
#      r1 = y coordinate of centre of circle
#      r2 = width (in pixels)
#      r3 = radius (in pixels)
#
# Outputs:
#
#      no registers altered
#      circle drawn in ram
#
#Notes:
#      This routine uses two special case line drawing routines:
#      a horizontal case (called HLINE)
#      a vertical case (called VLINE)
#      A general purpose line drawing algorithm could be used, however
#      the new 32CG16 instructions are much faster.
#      If the line is to have a width of > 25 pixels, the BIGSET algorithm
#      must be added to the HLINE routine. No other changes are required.
#
circle: save [r4,r5,r6,r7] #save our working registers
        movd r2,r7        #get current width
        lshd $-1,r7       #divide by two
        movd r7,hlfdwth   #and store it away
        movqd 0,r4        #x1 = 0
        movd r3,r5        #y1 = radius
        movqd 3,r6        #p = 3 - (radius * 2)
        subd r3,r6
        subd r3,r6
        br cirtest
        .align 4
cir1p: bsr setgrp         #set a group of points
        cmpqd 0,r6        #is P less than zero?
        blt pge0          #no, it is not. skip
        addr 6(r6)[r4:d],r6 #p += 4 * x1 + 6
        addqd 1,r4        #x1 ++
        cirtest:cmpd r4,r5 #is x1 <= y1 ?
        ble cir1p        #it is. Loop
        br cirot1

        .align 4
pge0: movd r4,r7          #t = x1
        subd r5,r7        #t = x1 - y1
        addr 10(r6)[r7:d],r6 #p += 4 * (x1 - y1) + 10
        addqd -1,r5       #y1 --
        addqd 1,r4        #x1 ++
        cmpd r4,r5        #is x1 <= y1 ?
        ble cir1p        #it is. Loop
        cirot1: bne cirot  #if x1 != y1, get out
        bsr setgrp        #else set last group
        cirot: restore [r4,r5,r6,r7] #restore working registers
        ret 0             #and return

#
#Setgrp sets eight points on a circle, given starting x and y, and the
#current xoffset and y offset.
#
# Inputs:
#      r0 = centerpoint of circle (x coordinate)

```

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```

#           r1 = centerpoint of circle (y coordinate)
#           r2 = line width
#           r4 = x offset
#           r5 = y offset
#
# Outputs:
#           all registers preserved.
#
.align 4
setgrp: movd r6,tos      #get two temporary values
        movd r7,tos
        movd r0,r6      #save old x
        movd r1,r7      #and y
        movd r5,r1
        subd r4,r1      #r1 = (y1 - x1)
        cmpd r1,hlfwidth #if the difference is less than
        ble sg1:w       #half the width, fill in the edges
        movd r7,r1      #restore y
        addd r4,r0      #x += x1
        addd r5,r1      #y += y1
        bsr vline      #do a vline
        movd r6,r0      #restore x and y
        movd r7,r1
        addd r4,r0      #x += x1
        subd r5,r1      #y -= y1
        bsr vline
        movd r6,r0      #restore x and y
        movd r7,r1
        subd r4,r0      #x -= x1
        addd r5,r1      #y += y1
        bsr vline
        movd r6,r0      #restore x and y
        movd r7,r1
        subd r4,r0      #x -= x1
        subd r5,r1      #y -= y1
        bsr vline

        movd r6,r0      #restore x and y
        movd r7,r1
        addd r5,r0      #x += y1
        addd r4,r1      #y += x1
        bsr hline
        movd r6,r0      #restore x and y
        movd r7,r1
        addd r5,r0      #x += y1
        subd r4,r1      #y -= x1
        bsr hline
        movd r6,r0      #restore x and y
        movd r7,r1
        subd r5,r0      #x -= y1
        addd r4,r1      #y += x1
        bsr hline
        movd r6,r0      #restore x and y
        movd r7,r1
        subd r5,r0      #x -= y1

```

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```

    subd    r4,r1      #y -= x1
    bsr     hline
    movd    r6,r0      #restore x and y
    movd    r7,r1
    movd    tos,r7     #and unstack
    movd    tos,r6
    ret     0

sg1:  movd    r7,r1      #restore y
      addd    r4,r0      #x += x1
      addd    r5,r1      #y += y1
      bsr     hline     #do a hline
      bsr     vline     #and a vline
      movd    r6,r0      #restore x and y
      movd    r7,r1
      addd    r4,r0      #x += x1
      subd    r5,r1      #y -= y1
      bsr     hline
      bsr     vline
      movd    r6,r0      #restore x and y
      movd    r7,r1
      subd    r4,r0      #x -= x1
      addd    r5,r1      #y += y1
      bsr     hline
      bsr     vline
      movd    r6,r0      #restore x and y
      movd    r7,r1
      addd    r5,r0      #x += y1
      addd    r4,r1      #y += x1
      bsr     vline
      bsr     hline
      movd    r6,r0      #restore x and y
      movd    r7,r1
      addd    r5,r0      #x += y1
      subd    r4,r1      #y -= x1
      bsr     vline
      bsr     hline
      movd    r6,r0      #restore x and y
      movd    r7,r1
      subd    r5,r0      #x -= y1
      addd    r4,r1      #y += x1
      bsr     vline
      bsr     hline
      movd    r6,r0      #restore x and y
      movd    r7,r1
      subd    r5,r0      #x -= y1
      subd    r4,r1      #y -= x1
      bsr     vline

```

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```

        bsr     hline
        movd    r6,r0          #restore x and y
        movd    r7,r1
        movd    tos,r7         #and unstack
        movd    tos,r6
        ret     0

#
#A vertical line drawing algorithm, making use of the SBITPS instruction
#
# Inputs:
#     r0 = x coordinate of line
#     r1 = centerpoint of y coordinate of line
#     r2 = line length
#
# Outputs:
#     no registers altered.
#     line drawn in memory.
#
        .align 4
vline: save    [r0,r1,r2,r3]    #save working registers
        subd    hlfwidth,r1     #y -= half of width to centre vline
        addr    @(xwarp-1),r3    #r3 = xwarp -1
        indexd  r1,r3,r0        #bit off = y * (xwarp) + x
        addqd   1,r3            #move to correct warp value
        movd    _page,r0        #page address in r0
        SBITPS                                     #set bit perpendicular string
        restore [r0,r1,r2,r3]    #restore registers
        ret     0

#
#A horizontal line drawing algorithm, using SBITS.
#
# Inputs:
#     r0 = centerpoint of x coordinate
#     r1 = y coordinate of line
#     r2 = line length
#
        .align 4
hline: save    [r0,r1,r3]       #save working registers
        subd    hlfwidth,r0     #x -= half of width to centre values
        indexd  r1,(xwarp - 1),r0 # bit off = (y * xwarp) + x
        movd    _page,r0        #page address in r0
        addr    stab,r3         #address of sbits table
        SBITS
        restore [r0,r1,r3]
        ret     0

```

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Figure 1 shows this algorithm 'at work'. 20 circles of radius 350 pixels, and widths of 1 to 20 pixels are shown. A full listing of this test program is shown in *Figure 2*.

4.0 TIMING

The execution speed of this algorithm is dependent on the radius of the circle, and the circle's width. The test program

supplied executes in 2.92 seconds on a NS32016 at 10 MHz with no wait states. The execution time on the NS32CG16 at 15 MHz with no wait states is 1.54 seconds. By using macros for the VLINE and HLINE routines, instead of subroutine calls, the time can be further reduced to 1.39 seconds.

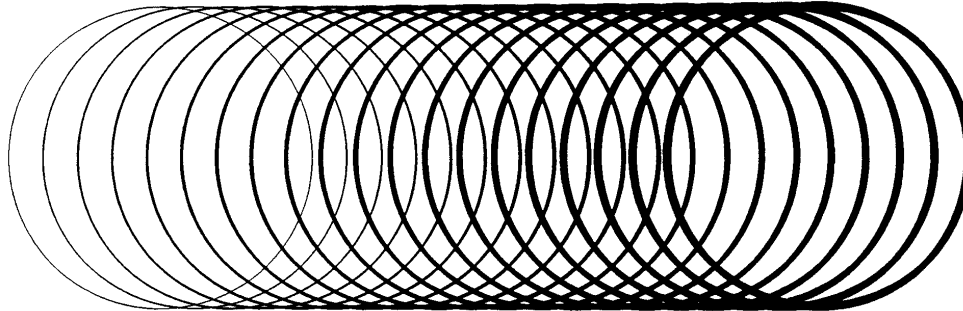


FIGURE 1

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```

        .data
        .set    xwarp,2544      #bits of xwarp to get to next scan
        .comm   _page,4
hlfdwth:.double 0
        .text
#
# Test is a C - callable function that creates Figure 1.
#
        .globl _test
_test:  save    [r3,r4,r5,r6,r7]
        addr    @400,r0         #start at x=400
        addr    @400,r1         #          y=400
        movq    1,r2            #width = 1
        addr    @350,r3         #radius = 350
        addr    @20,r7          #we want to do 20 circles
lp:     bsr     circle          #do a circle
        addr    80(r0),r0       #x += 80
        addq    1,r2            #width += 1
        acbq    -1,r7,lp        #loop for all 20 circles
        restore [r3,r4,r5,r6,r7]
        ret     0              #and return

#
#Bresenham's circle algorithm, as expressed in "Computer Graphics" by
#Donald Hearn and M. Pauline Baker (1986, Prentice-Hall,
#ISBN 0-13-165382-2)
#
#      Inputs:
#      r0 = x coordinate of centre of circle
#      r1 = y coordinate of centre of circle
#      r2 = width (in pixels)
#      r3 = radius (in pixels)
#
#      Outputs:
#      no registers altered
#      circle drawn in ram
#
#Notes:
#      This routine uses two special case line drawing routines:
#      a horizontal case (called HLINE)
#      a vertical case (called VLINE)
#      A general purpose line drawing algorithm could be used, however
#      the new 32CG16 instructions are much faster.
#      If the line is to have a width of > 25 pixels, the BIGSET algorithm
#      must be added to the HLINE routine. No other changes are required.
#
circle: save    [r4,r5,r6,r7]    #save our working registers
        movd    r2,r7           #get current width

```

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FIGURE 2

```

        lshd    $-1,r7        #divide by two
        movd    r7,hlfwidth  #and store it away
        movqd   0,r4         #x1 = 0
        movd    r3,r5        #y1 = radius
        movqd   3,r6         #p = 3 - (radius * 2)
        subd    r3,r6
        subd    r3,r6
        br      cirtest
        .align  4
cirlp:  bsr     setgrp        #set a group of points
        cmpqd   0,r6         #is P less than zero?
        blt     pge0         #no, it is not. skip
        addr    6(r6)[r4:d],r6 #p += 4 * x1 + 6
        addqd   1,r4         #x1 ++
cirtest:cmpd    r4,r5        #is x1 <= y1 ?
        ble     cirlp        #it is. Loop
        br      cirout

        .align  4
pge0:   movd    r4,r7        #t = x1
        subd    r5,r7        #t = x1 - y1
        addr    10(r6)[r7:d],r6 #p += 4 * (x1 - y1) + 10
        addqd   -1,r5        #y1 --
        addqd   1,r4         #x1 ++
        cmpd    r4,r5        #is x1 <= y1 ?
        ble     cirlp        #it is. Loop
cirout: restore [r4,r5,r6,r7] #restore working registers
        ret     0            #and return

#
#Setgrp sets eight points on a circle, given starting x and y, and the
#current xoffset and y offset.
#
#      Inputs:
#          r0 = centerpoint of circle (x coordinate)
#          r1 = centerpoint of circle (y coordinate)
#          r2 = line width
#          r4 = x offset
#          r5 = y offset
#
#      Outputs:
#          all registers preserved.
#
        .align  4
setgrp: movd    r6,tos        #get two temporary values
        movd    r7,tos
        movd    r0,r6        #save old x

```

FIGURE 2 (Continued)

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```

movd    r1,r7      #and y
movd    r5,r1
subd    r4,r1      #r1 = (y1 - x1)
cmpd    r1,hlfwidth #if the difference is less than
ble      sg1       #half the width, fill in the edges
movd    r7,r1      #restore y
addd    r4,r0      #x += x1
addd    r5,r1      #y += y1
bsr     vline      #do a vline
movd    r6,r0      #restore x and y
movd    r7,r1
addd    r4,r0      #x += x1
subd    r5,r1      #y -= y1
bsr     vline
movd    r6,r0      #restore x and y
movd    r7,r1
subd    r4,r0      #x -= x1
addd    r5,r1      #y += y1
bsr     vline
movd    r6,r0      #restore x and y
movd    r7,r1
subd    r4,r0      #x -= x1
subd    r5,r1      #y -= y1
bsr     vline

movd    r6,r0      #restore x and y
movd    r7,r1
addd    r5,r0      #x += y1
addd    r4,r1      #y += x1
bsr     hline
movd    r6,r0      #restore x and y
movd    r7,r1
addd    r5,r0      #x += y1
subd    r4,r1      #y -= x1
bsr     hline
movd    r6,r0      #restore x and y
movd    r7,r1
subd    r5,r0      #x -= y1
addd    r4,r1      #y += x1
bsr     hline
movd    r6,r0      #restore x and y
movd    r7,r1
subd    r5,r0      #x -= y1
subd    r4,r1      #y -= x1
bsr     hline
movd    r6,r0      #restore x and y
movd    r7,r1
movd    tos,r7     #and unstack

```

FIGURE 2 (Continued)

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```

movd    tos,r6
ret     0

sg1:    movd    r7,r1      #restore y
        addd    r4,r0      #x += x1
        addd    r5,r1      #y += y1
        bsr     hline      #do a hline
        bsr     vline      #and a vline
        movd    r6,r0      #restore x and y
        movd    r7,r1
        addd    r4,r0      #x += x1
        subd    r5,r1      #y -= y1
        bsr     hline
        bsr     vline
        movd    r6,r0      #restore x and y
        movd    r7,r1
        subd    r4,r0      #x -= x1
        addd    r5,r1      #y += y1
        bsr     hline
        bsr     vline
        movd    r6,r0      #restore x and y
        movd    r7,r1
        addd    r5,r0      #x += y1
        addd    r4,r1      #y += x1
        bsr     vline
        bsr     hline
        movd    r6,r0      #restore x and y
        movd    r7,r1
        addd    r5,r0      #x += y1
        subd    r4,r1      #y -= x1
        bsr     vline
        bsr     hline
        movd    r6,r0      #restore x and y
        movd    r7,r1
        subd    r5,r0      #x -= y1
        addd    r4,r1      #y += x1
        bsr     vline
        bsr     hline
        movd    r6,r0      #restore x and y
        movd    r7,r1
        subd    r5,r0      #x -= y1

```

FIGURE 2 (Continued)

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```

        subd    r4,r1        #y -= x1
        bsr     vline
        bsr     hline
        movd    r6,r0        #restore x and y
        movd    r7,r1
        movd    tos,r7        #and unstack
        movd    tos,r6
        ret     0

#
#A vertical line drawing algorithm, making use of the SBITPS instruction.
#
#   Inputs:
#       r0 = x coordinate of line
#       r1 = centerpoint of y coordinate of line
#       r2 = line length
#
#   Outputs:
#       no registers altered.
#       line drawn in memory.
#
        .align 4
vline:  save     [r0,r1,r2,r3]  #save working registers
        subd    hlfdwth,r1     #y -= half of width to centre vline
        addr    @xwarp-1),r3   #r3 = xwarp -1
        indexd  r1,r3,r0       #bit off = y * (xwarp) + x
        addqd   1,r3           #move to correct warp value
        movd    _page,r0       #page address in r0
#       SBITPS   #set bit perpendicular string

# - Start of SBITPS emulation code
        .align 4
sblp:   sbitd    r1,0(r0)       #set required bit
        addd     r3,r1          #add the bit warp
        acbd     -1,r2,sblp     #loop for the rll
# - End of SBITPS emulation code
        restore  [r0,r1,r2,r3]  #restore registers
        ret     0

#
#A horizontal line drawing algorithm, using SBITS.
#
#   Inputs:
#       r0 = centerpoint of x coordinate
#       r1 = y coordinate of line
#       r2 = line length
#
        .align 4

```

FIGURE 2 (Continued)

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```

hline: save    [r0,r1,r3]    #save working registers
        subd    hlfwidth,r0    #x -= half of width to centre values
        indexd  r1,$(xwarp - 1),r0 # bit off = (y * xwarp) + x
        movd    _page,r0      #page address in r0
        addr    stab,r3       #address of sbits table
#
#
# - start of SBITS emulation code
        movqd   7,r3
        andd    r1,r3

        addd    r3,r3    #* 2
        addd    r3,r3    #* 4
        addd    r3,r3    #* 8
        addd    r3,r3    #* 16
        addd    r3,r3    #* 32
        addd    r2,r3
        ashd    $-3,r1
        ord     stab[r3:d],0(r0)[r1:b]
# - end of SBITS emulation code
        restore [r0,r1,r3]
        ret     0

        .data
stab: .double h'00000000,h'00000001,h'00000003,h'00000007
      .double h'0000000f,h'0000001f,h'0000003f,h'0000007f
      .double h'000000ff,h'000001ff,h'000003ff,h'000007ff
      .double h'00000fff,h'00001fff,h'00003fff,h'00007fff
      .double h'0000ffff,h'0001ffff,h'0003ffff,h'0007ffff
      .double h'000fffff,h'01fffff,h'03fffff,h'07fffff
      .double h'0fffffff,h'1fffff,h'3fffff,h'7fffff
      .double h'00000000,h'00000002,h'00000006,h'0000000e
      .double h'0000001e,h'0000003e,h'0000007e,h'000000fe
      .double h'000001fe,h'000003fe,h'000007fe,h'00000ffe
      .double h'00001ffe,h'00003ffe,h'00007ffe,h'0000fffe
      .double h'0001ffe,h'0003ffe,h'0007ffe,h'000fffe
      .double h'001ffffe,h'003ffffe,h'007ffffe,h'00fffffe
      .double h'01fffffe,h'03fffffe,h'07fffffe,h'0fffffe
      .double h'00000000,h'00000004,h'0000000c,h'0000001c
      .double h'0000003c,h'0000007c,h'000000fc,h'000001fc
      .double h'000003fc,h'000007fc,h'00000ffc,h'00001ffc
      .double h'00003ffc,h'00007ffc,h'0000ffc,h'0001ffc
      .double h'0003ffc,h'0007ffc,h'000ffc,h'01ffc
      .double h'003ffc,h'007ffc,h'0ffc,h'1ffc

```

FIGURE 2 (Continued)

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```

.double h'3fffffc,h'7fffffc,h'fffffc,h'fffffc
.double h'0000000,h'0000008,h'0000018,h'0000038
.double h'0000078,h'00000f8,h'00001f8,h'00003f8
.double h'00007f8,h'0000ff8,h'0001ff8,h'0003ff8
.double h'0007ff8,h'000fff8,h'001fff8,h'003fff8
.double h'007fff8,h'00ffff8,h'01fff8,h'03fff8
.double h'07fff8,h'0ffff8,h'1fff8,h'3fff8
.double h'7fff8,h'ffff8,h'ffff8,h'ffff8
.double h'0000000,h'0000010,h'0000030,h'0000070
.double h'00000f0,h'00001f0,h'00003f0,h'00007f0
.double h'0000ff0,h'0001ff0,h'0003ff0,h'0007ff0
.double h'000fff0,h'001fff0,h'003fff0,h'007fff0
.double h'00ffff0,h'01fff0,h'03fff0,h'07fff0
.double h'0ffff0,h'1fff0,h'3fff0,h'7fff0
.double h'ffff0,h'ffff0,h'ffff0,h'ffff0
.double h'0000000,h'0000020,h'00000c0,h'00000e0
.double h'00001e0,h'00003e0,h'00007e0,h'0000fe0
.double h'0001fe0,h'0003fe0,h'0007fe0,h'000ffe0
.double h'0001ffe0,h'0003ffe0,h'0007ffe0,h'000ffe0
.double h'01ffe0,h'03ffe0,h'07ffe0,h'0ffe0
.double h'1ffe0,h'3ffe0,h'7ffe0,h'ffe0
.double h'fffe0,h'fffe0,h'fffe0,h'fffe0
.double h'0000000,h'0000040,h'00000c0,h'00001c0
.double h'00003c0,h'00007c0,h'0000fc0,h'0001fc0
.double h'0003fc0,h'0007fc0,h'000ffc0,h'001ffc0
.double h'003ffc0,h'007ffc0,h'00fffc0,h'01ffc0
.double h'03ffc0,h'07ffc0,h'0fffc0,h'1ffc0
.double h'3ffc0,h'7ffc0,h'fffc0,h'fffc0
.double h'ffffc0,h'ffffc0,h'ffffc0,h'ffffc0
.double h'0000000,h'0000080,h'0000180,h'0000380
.double h'0000780,h'0000f80,h'0001f80,h'0003f80
.double h'0007f80,h'000ff80,h'001ff80,h'003ff80
.double h'007ff80,h'00fff80,h'01ff80,h'03ff80
.double h'07ff80,h'0fff80,h'1ff80,h'3ff80
.double h'7ff80,h'fff80,h'fff80,h'fff80
.double h'ffff80,h'ffff80,h'ffff80,h'ffff80

```

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FIGURE 2 (Continued)

5.0 CONCLUSIONS

The NS32CG16 provides several instructions that increase the speed of imaging common graphic items such as circles, lines, and ellipses. The NS32CG16's high code density, and fast execution, make it ideal for intensive graphics processing.

This algorithm does, however, show an apparent 'thinning' on the 45° boundaries, when the width of the circle is greater than five pixels. An alternate algorithm will be presented

in a future applications note. This algorithm is optimized for speed.

6.0 REFERENCES

1. Hearn, Donald and M. Pauline Baker, (1986). *Computer Graphics*, Englewood Cliffs, N.J., Prentice-Hall, 65-69.
2. Foley, James D. and Van Dam, Andries, (1982). *Fundamentals of Interactive Computer Graphics*, Reading, Massachusetts, Addison-Wesley, 441-446.

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