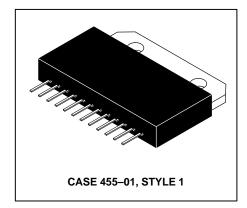
# The RF Line Triple Video Output Hybrid Amplifier

A high performance triple video output amplifier designed specially for use as the video channel final stage in high resolution color monitors.

- Typical 10-90% Transitions Times are 2.5 ns
- Supports Video Clock Rates up to 250 MHz
- Up to 60 Vp-p Output Swing with 70 V Supply Voltage
- Low Power Consumption
- Excellent Gray-Scale Linearity
- · Unconditional Stability
- · Gold Metallization System for the Ultimate in Reliability

## **MHW3828**

2.5 ns
TRIPLE VIDEO OUTPUT
HYBRID
AMPLIFIER



#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Supply Voltage	VCC	+80	Vdc
Operating Case Temperature Range	ТC	-20 to +100	°C
Storage Temperature Range	T <sub>stg</sub>	-40 to +100	°C

## **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ , $V_{CC} = 70$ V, $C_{LOAD} = 10$ pF, 40 V Peak–to–Peak Output Swing with 35 Vdc Offset; $R_1 = 390 \Omega$ , $C_1 = 39$ pF Tvp)

Characteristic	Symbol	Min	Тур	Max	Unit
Supply Current (With Input Open Circuited) Per Channel	lcc	29	33	37	mA
Input DC Voltage (With Input Open Circuited)	VinDC	1.2	1.5	1.8	V
Input DC Voltage (With Input Open Circuited)	VoutDC	32	35	38	V
Voltage Gain (1) (2)	AV	11	12.5	14	V/V
Transient Response (2)  — Rise Time (10% to 90%)  — Overshoot  — Fall Time (90% to 10%)  — Overshoot	t <sub>r</sub> VOS,r t <sub>f</sub> VOS,f	  -  -  -	2.3 5.0 2.5 5.0	2.7 10 2.9 10	ns % ns %
Operating Supply Current per Channel @ 50 MHz Square Wave (3) (Vout = 40 Vp–p and 35 V offset) (Vout = 50 Vp–p and 35 V offset)	Icc		68 78		mA
Linearity Error (V <sub>Out</sub> = 5.0 V to +65 V)	_	<u> </u>	_	5.0	%

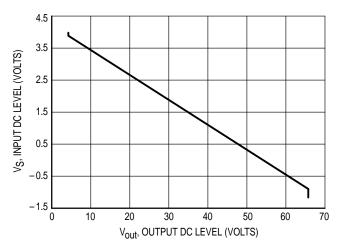
<sup>(1)</sup>  $A_V = V_{out}/V_S$ 



<sup>(2)</sup> Input Signal is normally a 62.5 kHz square wave of 3.2 V peak–to–peak with 1.5 Vdc offset. Input  $t_{\rm f}$ ,  $t_{\rm f}$  < 1.0 ns.

<sup>(3)</sup> Output is not short circuit protected.

## **TYPICAL CHARACTERISTICS**



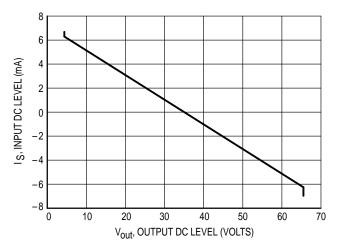


Figure 1. V<sub>S</sub> versus V<sub>out</sub>

Figure 2. Is versus Vout

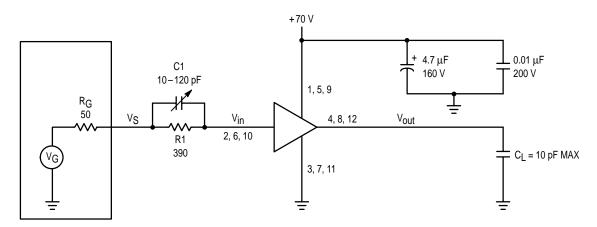
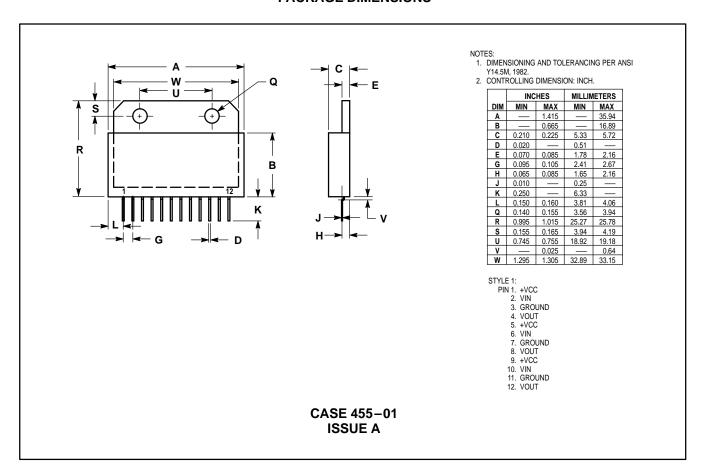


Figure 3. Hybrid Amplifier Test Circuit

## **PACKAGE DIMENSIONS**



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