

Errata Sheet for Rev. B PIC16C84 Silicon

The PIC16C84 (Rev. B) parts you have received conform functionally to the PIC16C84 data sheet (DS30445A), except for the anomalies described below.

1. I_{PD} is tested to 100μA. Present characterization is as follows:

V _{DD}	Typical @ 25°C	Maximum @ 70°C
5V	47μA	84μA
3V	34μA	60μA
2V	26μA	43μA

2. In LP mode, the operating I_{DD} specification is tested at 4.0V to 400μA. Present characterization (@ 32 kHz) is as follows:

V _{DD}	Typical @ 25°C	Maximum @ +85°C to -40°C
6V	151μA	415μA
5V	132μA	365μA
3V	87μA	237μA
2V	57μA	139μA

Note: Some of these characterization values exceed the tested maximum due to differences in voltage measurement points.

3. When the weak internal pull-ups on the PORTB pins are enabled, the maximum V_L on any PORTB pin is 0.6V.
4. For crystal oscillator configurations operating below 500 kHz, the device may generate a spurious internal Q-clock when PORTA<0>(RA0) switches. This does not occur with an external clock or in RC mode.

Work Around

RA0 should be kept static, i.e., in input/output mode, RA0 should not be toggled.

Clarifications/Corrections to the Data Sheet:

In the PIC16C84 Data Sheet (DS30445A), the following clarifications and corrections should be noted.

1. Erase/Write Cycle Time for Data EEPROM should be changed from 10 ms maximum to 10 ms typical, as shown in Table 1.

TABLE 1: DC SPECIFICATION LIMITS THAT VARY FROM DATA SHEET

Parameter No.	Sym.	Characteristic	Actual Data		Data Sheet		Units
			Typ	Max	Typ	Max	
D122	T _{DEW}	Erase Write Cycle Time	10	—	—	10	ms

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