

Errata Sheet of Rev. A PIC17C756 Silicon

The PIC17C756 (Rev. A) parts you have received conform functionally to the PIC17C75X preliminary data sheet (DS30264A), except for the following clarifications and corrections.

1. Module: MSSP

In I²C™ mode, the SSPBUF may receive either all 0's or all 1's independent of the data on the SDA pin

Work around

Both the SMP and CKE bits need to be in the same state (set or cleared) for proper operation. There is no possible mode conflict since the CKE bit is not defined for I²C operation.

2. Module: TCON3 Register

Bit 7 of the TCON3 register, which is unimplemented, may read a value other than the defined state of '0'.

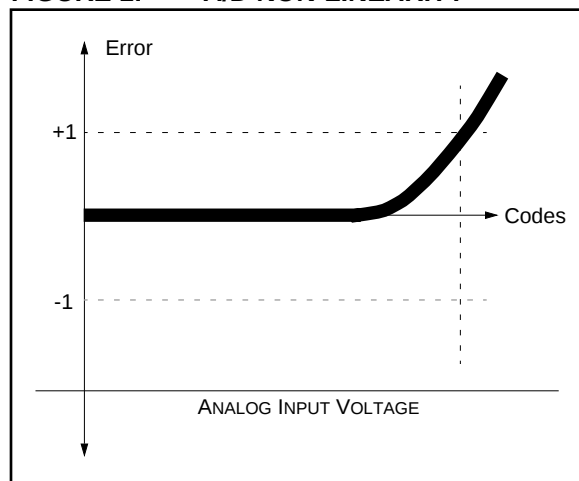
Work around

Since bit 7 is unimplemented, either ignore this bit, or read the TCON3 register into a temporary location and AND it with the value 7Fh.

3. Module: 10-Bit A/D

The least significant Analog-to-Digital (A/D) result codes may be lost when the device operates at a temperature > 25°C and the VREF+ > AVDD - 0.3V. Figure 1 shows the non-linearity issue.

FIGURE 1: A/D NON-LINEARITY



Work around

Under either of the following conditions, the device will retain the ± 1 LSb maximum error specifications:

1. $V_{REF+} \leq AV_{DD} - 0.3V$
2. Device Temperature $\leq 25^{\circ}C$

4. Module: 10-Bit A/D

The presence of code 511 is not guaranteed for the 10-bit A/D conversion result.

The 10-bit digital result gives 1024 codes (0 to 1023). The 10-bit A/D result is as expected for codes 0 to 510 and codes 512 to 1023. The analog value expected as code 511 may be represented as either code 510, code 511, or code 512. Code 511 is the midpoint of the analog voltage range.

Note: As with any windowed EPROM device, please cover the window at all times, except when erasing.

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PIC17C756

Clarifications/Corrections to the Data Sheet:

In the PIC17C75X Preliminary Data Sheet (document DS30264A) that you have received, requires the following clarifications and corrections.

NONE



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