



PIC12C671/PIC12C672

Errata Sheet for Rev. B Silicon for PIC12C671/PIC12C672

The PIC12C671/PIC12C672 parts conform functionally to the PIC12C67X data sheet (DS30561A), except for the anomalies described below.

None

Clarifications/Corrections to the Data Sheet:

Reference Section 4.2.2.7 OSCAL REGISTER in the PIC12C67X Data Sheet (DS30561A). This section should read:

The Oscillator Calibration (OSCCAL) register is used to calibrate the internal 4 MHz oscillator. It contains six bits for calibration. Increasing the cal value increases the frequency.

Reference Figure 4-9 OSCCAL REGISTER in the PIC12C67X Data Sheet (DS30561A). Bits <3:2> now used as part of a 6 bit calibration value (bits <7:2>). The correct Figure is shown on the next page.

PIC12C671/PIC12C672

FIGURE 4-9: OSCCAL REGISTER (ADDRESS 8Fh)

R/W-1	R/W-0	R/W-0	R/W-0	R/W-0	R/W-0	U-0	U-0
CAL5	CAL4	CAL3	CAL2	CAL1	CAL0	—	—
bit7							bit0
bit 7-2: CAL<5:0> : Calibration							

R = Readable bit
W = Writable bit
U = Unimplemented bit,
read as '0'
- n = Value at POR reset



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Microchip received ISO 9001 Quality System certification for its worldwide headquarters, design, and wafer fabrication facilities in January, 1997. Our field-programmable PICmicro™ 8-bit MCUs, Serial EEPROMs, related specialty memory products and development systems conform to the stringent quality standards of the International Standard Organization (ISO).

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