

Errata Sheet for Rev. B0 Silicon for PIC12CE519

The PIC12CE519 parts conform functionally to the PIC12CE5XX data sheet (DS40172A), except for the anomalies described below.

None

Clarifications/Corrections to the Data Sheet:

Section 6.0 EEPROM Peripheral Operation, subsection 6.1.5 titled ACKNOWLEDGE contains a note. The note reads *"Acknowledge bits are generated if an internal programming cycle is in progress."* This note is incorrect. It should read:

Acknowledge bits are not generated if an internal programming cycle is in progress.

Reference Section 4.5 OSCCAL Register found on page 16 of the PIC12CE5XX Data Sheet (DS40172A). This section should read:

The Oscillator Calibration (OSCCAL) register is used to calibrate the internal 4 MHz oscillator. It contains six bits for calibration. Increasing the cal value increases the frequency.

Reference Figure 4-6 of the PIC12CE5XX Data Sheet (DS40172A). Bits <3:2> now used as part of a 6 bit calibration value (bits <7:2>).

FIGURE 4-6: OSCCAL REGISTER (ADDRESS 8Fh)

R/W-1	R/W-0	R/W-0	R/W-0	R/W-0	R/W-0	U-0	U-0
CAL5	CAL4	CAL3	CAL2	CAL1	CAL0	—	—
bit7						bit0	

R = Readable bit
W = Writable bit
U = Unimplemented bit,
read as '0'
- n = Value at POR reset

bit 7-2: **CAL<5:0>**: Calibration



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Microchip received ISO 9001 Quality System certification for its worldwide headquarters, design, and wafer fabrication facilities in January, 1997. Our field-programmable PICmicro™ 8-bit MCUs, Serial EEPROMs, related specialty memory products and development systems conform to the stringent quality standards of the International Standard Organization (ISO).

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