

## Errata for HCS361

### Corrections/Modifications to the HCS 361 Device

The HCS361 parts conform functionally to the HCS361 Data Sheet (DS40146C), except for the anomalies described below:

None

### Clarifications/Corrections to the Data Sheet:

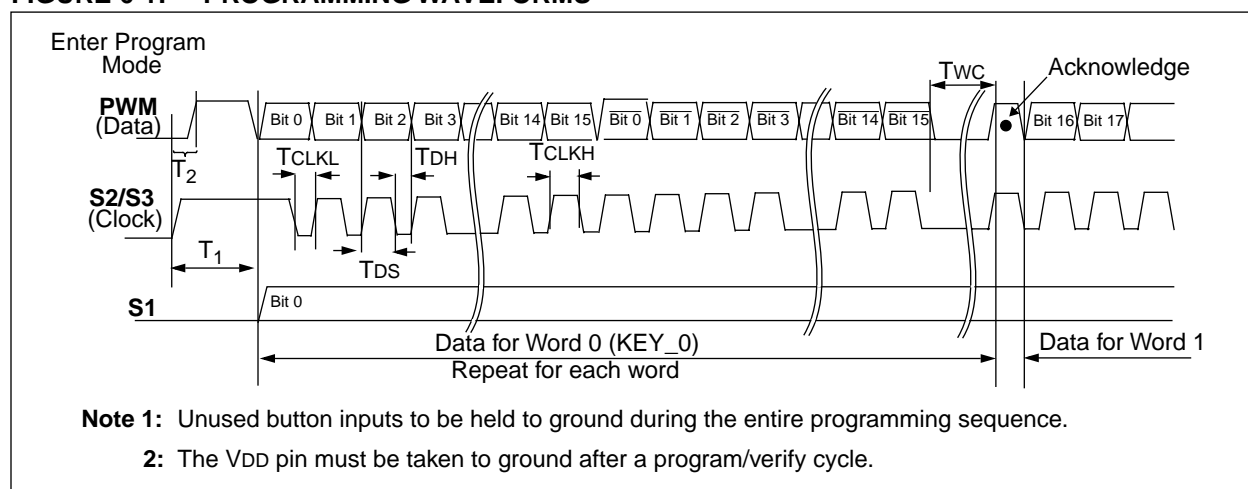
Referencing section 6.0 of the data sheet DS40146C:  
The sentence, *S0 and S1 should be held low during the entire program cycle (Table 6-1 and Figure 6-1)* should be replaced with the following:

*S0 should be held low during the entire program cycle. The S1 line on the HCS361 part needs to be set or cleared depending on the LS bit of the memory map (Key 0) before the key is clocked in to the HCS361. S1 must remain at this level for the duration of the programming cycle.*

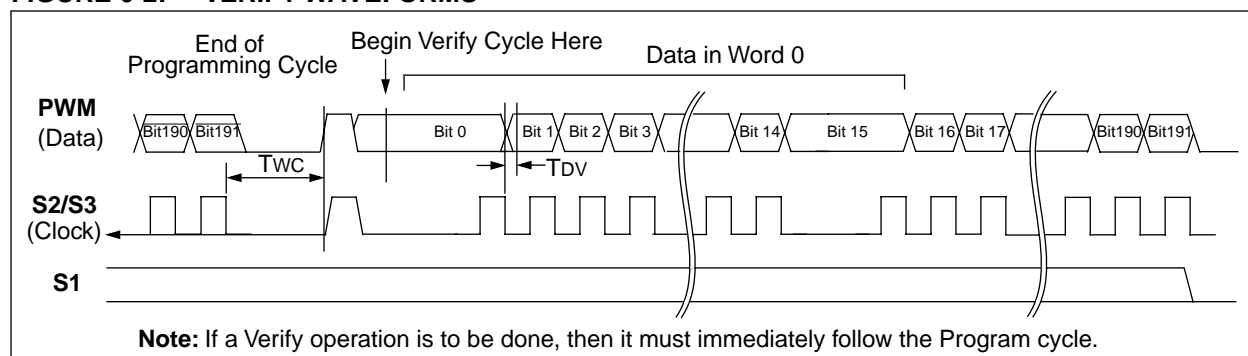
Referencing Figure 6.1 and 6.2 of the HCS361 data sheet (page 13 of DS40146C).

These figures will be replaced with the following figures when the data sheet is next revised. These figures show the revised programming wave form.

**FIGURE 6-1: PROGRAMMING WAVEFORMS**



**FIGURE 6-2: VERIFY WAVEFORMS**



# HCS361

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NOTES:

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