



PIC12C671/PIC12C672

Errata Sheet for Rev. A PIC12C671/PIC12C672 Engineering Sample Silicon

The PIC12C671/PIC12C672 (Rev A) engineering sample parts conform functionally to the PIC12C67X data sheet (DS30561A), except for the anomalies described below.

These problems will be addressed in the next revision of the PIC12C671/PIC12C672 silicon.

- Power on Reset

In order to ensure proper device operation upon power-up, the Power-up Timer (PWRT) must be enabled.

- A/D

If bits PCFG2:PCFG0 of ADCON1 register are configured in 111 mode (all digital inputs), then $V_{REF} = V_{SS}$. Performing an analog read will give erroneous results.

Clarifications/Corrections to the Data Sheet:

Reference Figure 7-2 in the PIC12C67X datasheet (DS30561A). For PCFG2:PCFG0 configured as 111, $V_{REF} = V_{SS}$.



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Microchip received ISO 9001 Quality System certification for its worldwide headquarters, design, and wafer fabrication facilities in January, 1997. Our field-programmable PICmicro™ 8-bit MCUs, Serial EEPROMs, related specialty memory products and development systems conform to the stringent quality standards of the International Standard Organization (ISO).

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