



HIGH OUTPUT CURRENT C-MOS POSITIVE VOLTAGE REGULATOR

PRELIMINARY

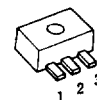
■ GENERAL DESCRIPTION

The NJU7202 series is a high output current 3-terminal positive voltage regulator which contains internal accurate voltage reference, error amplifier, control transistor, output voltage setting resistor and over load protection circuit.

The regulation voltage is fixed by internal circuits and the following line-up of different output voltages version are available.

This series is suitable for battery operated items and battery back-up systems because of low operating current and low dropout voltage.

■ PACKAGE OUTLINE



NJU7202L (TO-92) NJU7202U (SOT-89)

■ FEATURES

- High Output Current ($I_o = 100\text{mA}$)
- Low Current Consumption ($20\mu\text{A typ}$)
- Low Dropout Voltage ($\Delta V_{i_o} < 0.6\text{V}, I_o = 100\text{mA}$)
- Wide Operating Voltage Range
- Small Temperature Coefficient of Output Voltage
- Over Load Protection Circuit on Chip
- Package Outline TO-92 / SOT-89
- C-MOS Technology

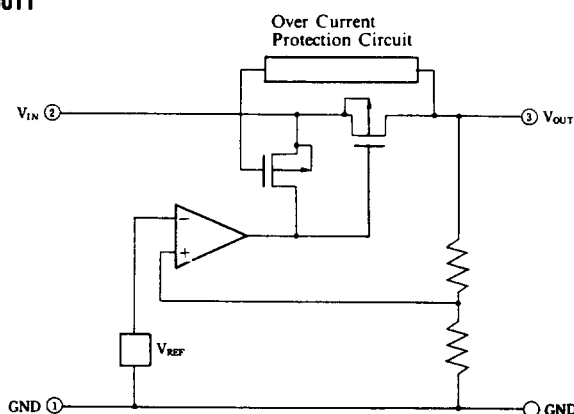
■ TERMINAL DESCRIPTION

NO	DESCRIPTION
1	GND
2	INPUT
3	OUTPUT

■ OUTPUT VOLTAGE LINE-UP

OUTPUT VOLTAGE	TO-92 TYPE	SOT-89 TYPE
3.0V	7202L30	7202U30
5.0V	7202L50	7202U50

■ EQUIVALENT CIRCUIT





■ ABSOLUTE MAXIMUM RATINGS

($T_a=25^\circ\text{C}$)

PARAMETER	SYMBOL	RATINGS	UNIT
Input Voltage	V_{IN}	15	V
Output Voltage	V_{OUT}	$\text{GND}-0.3 \sim V_{IN}+0.3$	V
Output Current	I_{OUT}	200	mA
Power Dissipation	P_D	(T0-92) 500 (SOT-89) 300	mW
Operating Temperature Range	T_{OPR}	$-25 \sim +75$	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	$-40 \sim +125$	$^\circ\text{C}$

■ ELECTRICAL CHARACTERISTICS

● +3.0V VERSION

($C_{IN}=C_{OUT}=0.1\mu\text{F}$, $T_a=25^\circ\text{C}$)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	V_{OUT}	$V_{IN}=5.0\text{V}$, $I_{OUT}=70\text{mA}$	2.85	3.00	3.15	V
Dropout Voltage	ΔV_{IO}	$I_{OUT}=50\text{mA}$		0.40	0.60	V
Input Voltage	V_{IN}				14	V
Operating Current	I_O	$V_{IN}=5.0\text{V}$		20	30	μA
Load Regulation	$\Delta V_{OUT}/\Delta I_{OUT}$	$V_{IN}=5.0\text{V}$, $I_{OUT}=1 \sim 100\text{mA}$		120	160	mV
Line Regulation	$\Delta V_{OUT}/(\Delta V_{IN} \cdot V_{OUT})$	$V_{IN}=4.0\text{V} \sim 12.0\text{V}$		0.1		%/V

● +5.0V VERSION

($C_{IN}=C_{OUT}=0.1\mu\text{F}$, $T_a=25^\circ\text{C}$)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	V_{OUT}	$V_{IN}=7.0\text{V}$, $I_{OUT}=70\text{mA}$	4.75	5.00	5.25	V
Dropout Voltage	ΔV_{IO}	$I_{OUT}=100\text{mA}$		0.30	0.60	V
Input Voltage	V_{IN}				14	V
Operating Current	I_O	$V_{IN}=7.0\text{V}$		20	30	μA
Load Regulation	$\Delta V_{OUT}/\Delta I_{OUT}$	$V_{IN}=7.0\text{V}$, $I_{OUT}=1 \sim 100\text{mA}$		120	160	mV
Line Regulation	$\Delta V_{OUT}/(\Delta V_{IN} \cdot V_{OUT})$	$V_{IN}=6.0\text{V} \sim 12.0\text{V}$		0.1		%/V

■ MEASUREMENT CIRCUIT

