

HIGH PERFORMANCE LOW-NOISE OPERATIONAL AMPLIFIER

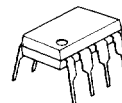
■ GENERAL DESCRIPTION

The NJM5534 is a high performance low noise operational amplifier. This amplifier features popular pin-out, superior noise performance, and high output drive capability.

The amplifier also features guaranteed noise performance with substantially higher gain-bandwidth product, power bandwidth, and slew rate which far exceeds that of the NJM741 type amplifiers. The NJM5534 is internally compensated for a gain of three or higher and may be externally compensated for optimizing specific performance requirements of various applications such as unity-gain voltage followers, drivers for capacitive loads or fast setting.

The specially designed low noise input transistors allow the NJM5534 to be used in very low noise signal processing applications such as audio pre-amplifiers and servo error amplifiers.

■ PACKAGE OUTLINE



NJM5534D



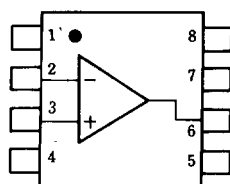
NJM5534M

■ FEATURES

- Operating Voltage $(\pm 3V \sim \pm 22V)$
- Single Circuit
- With Vio Trim Terminal
- Low Input Noise Voltage $(3.3nV/\sqrt{Hz} \text{ typ. } \odot 1kHz)$
- Power Bandwidth $(200kHz \text{ typ.})$
- Slew Rate $(13V/\mu s \text{ typ.})$
- Package Outline DIP8, DMP8
- Bipolar Technology

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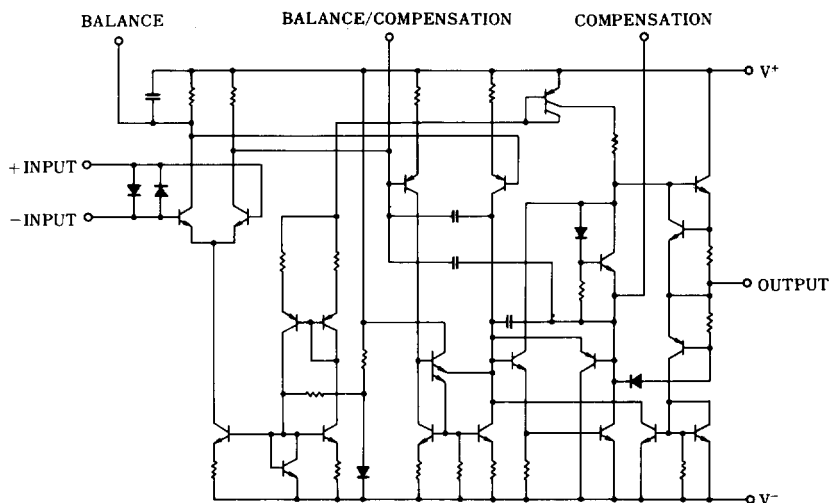
■ PIN CONFIGURATION

NJM5534D
NJM5534M

PIN FUNCTION

1. BALANCE
2. -INPUT
3. +INPUT
4. V^-
5. COMPENSATION
6. OUTPUT
7. V^+
8. BALANCE/COMPENSATION

■ EQUIVALENT CIRCUIT





■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	(V ⁺ /V ⁻)	±22	V
Differential Input Voltage	V _{IO}	±0.5	V
Input Voltage	V _{ID}	V ⁺ /V ⁻	V
Power Dissipation	P _D	(DIP8) 500	mW
		(DMP8) 300	mW
Operating Temperature Range	T _{opr}	-20~+75	°C
Storage Temperature Range	T _{stg}	-40~+125	°C

■ ELECTRICAL CHARACTERISTICS

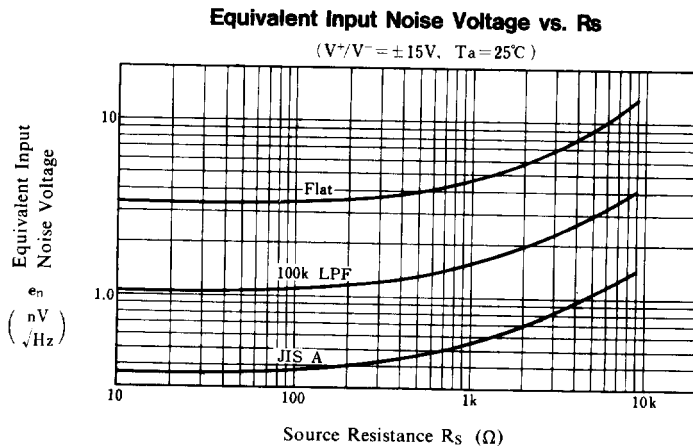
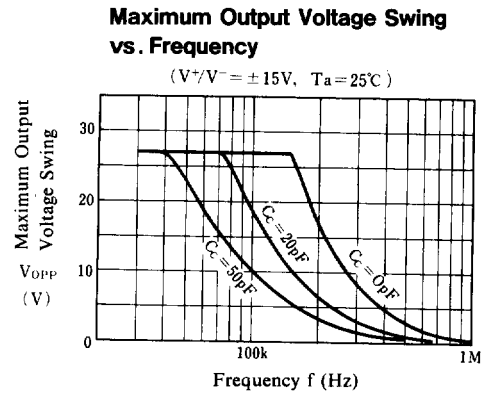
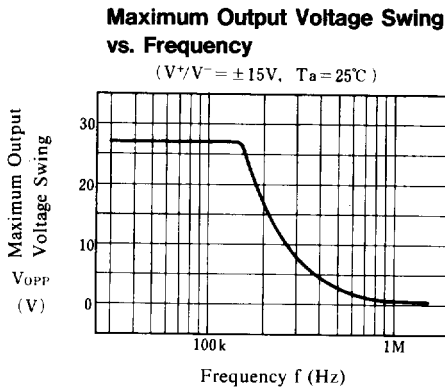
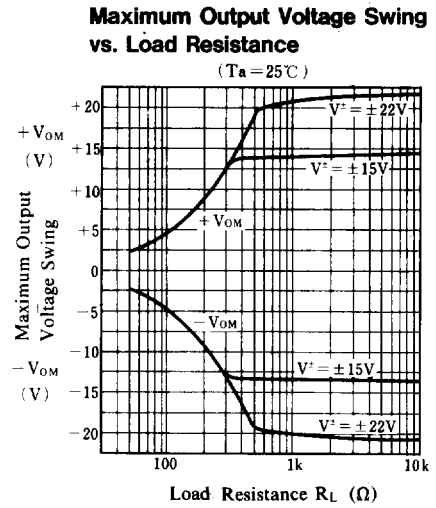
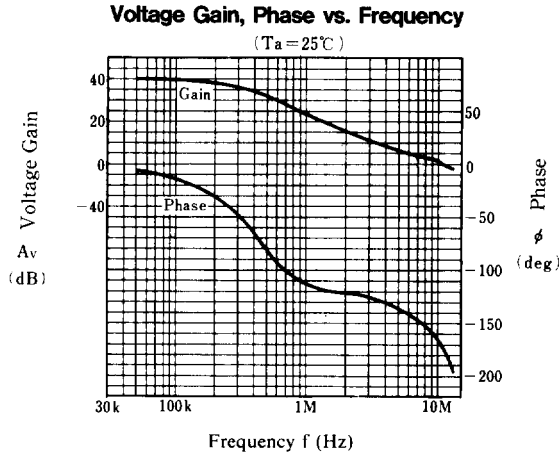
(Ta=25°C, V⁺/V⁻=±15V)

PARAMETER	SYMBOL	TEST CONDITION	NJM5534			UNIT
			MIN.	TYP.	MAX.	
Input Offset Voltage	V _{IO}	R _S ≤10kΩ	—	0.5	4	mV
Input Offset Current	I _{IO}		—	20	300	nA
Input Bias Current	I _B		—	500	1,500	nA
Input Resistance	R _{IN}		30	100	—	kΩ
Large Signal Voltage Gain	A _v	R _L ≥2kΩ, V _o =±10V	88	100	—	dB
Maximum Output Voltage Swing	V _{OM}	R _L ≥600Ω	±12	±13	—	V
Input Common Mode Voltage Range	V _{ICM}		±12	±13	—	V
Common Mode Rejection Ratio	CMR	R _S ≤10kΩ	70	100	—	dB
Supply Voltage Rejection Ratio	SVR	R _S ≤10kΩ	80	100	—	dB
Operating Current	I _{CC}	R _L =∞	—	4	8	mA
Transient Response Rise Time	t _R	V _{IN} =50mV, R _L =600Ω, C _L =100pF, C _c =22pF	—	35	—	nsec
Overshoot		V _{IN} =50mV, R _L =600Ω, C _L =100pF, C _c =22pF	—	17	—	%
Slew Rate	SR	C _c =0	—	13	—	V/μS
Gain Bandwidth Product	GB	C _c =22pF, C _L =100pF	—	10	—	MHz
Power Bandwidth	W _{PG}	V _o =20V _{p-p} , C _c =0	—	200	—	kHz
Equivalent Input Noise Voltage	V _{NI}	f=20Hz~20kHz	—	1.0	—	μVrms
Equivalent Input Noise Current	I _{NI}	f=20Hz~20kHz	—	25	—	pArms
Equivalent Input Noise Voltage 1	e _n	f _o =30Hz	—	5.5	—	nV/√Hz
Equivalent Input Noise Voltage 2	e _n	f _o =1kHz	—	3.3	—	nV/√Hz
Equivalent Input Noise Current 1	i _n	f _o =30Hz	—	1.5	—	pA/√Hz
Equivalent Input Noise Current 2	i _n	f _o =1kHz	—	0.4	—	pA/√Hz
Broadband Noise Figure	NF	f=10Hz~20kHz, R _S =5kΩ	—	0.9	—	dB

Note: JRC's general selected products D rank are also prepared for the noise standard (R_S=2.2kΩ, RIAA, V_N=1.4μV Max.)



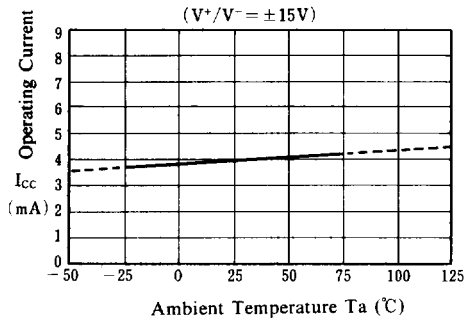
■ TYPICAL CHARACTERISTICS



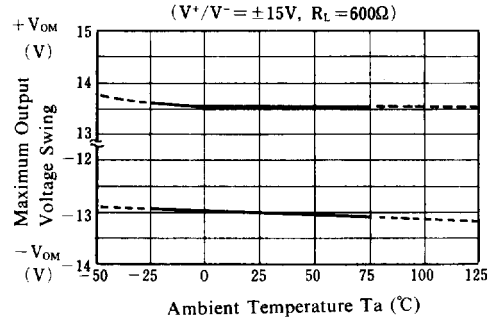


TYPICAL CHARACTERISTICS

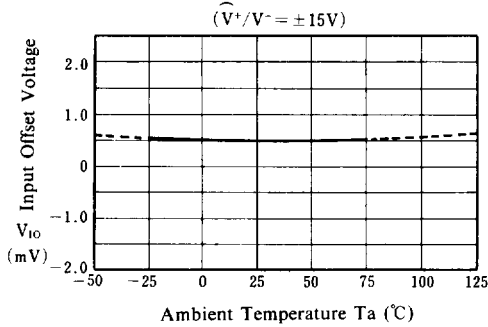
Operating Current vs. Temperature



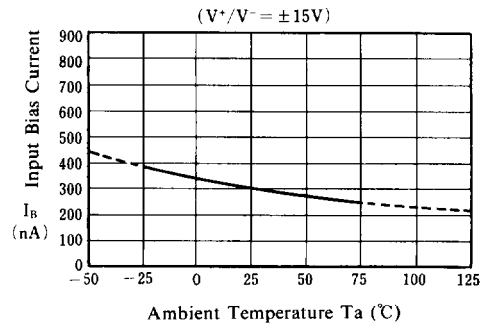
Maximum Output Voltage Swing vs. Temperature



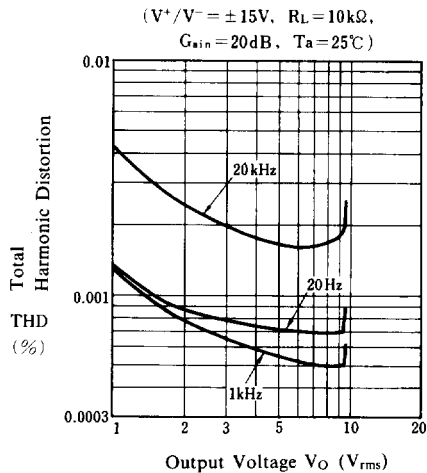
Input Offset Voltage vs. Temperature



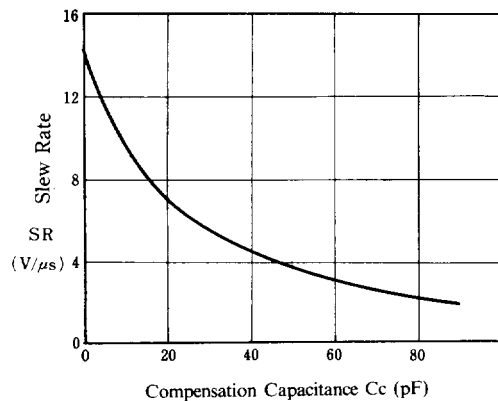
Input Bias Current vs. Temperature



Total Harmonic Distortion vs. Output Voltage



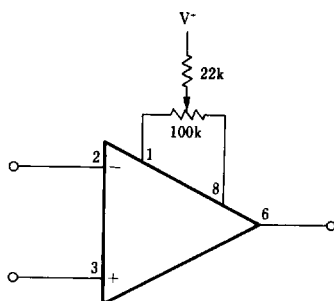
Slew Rate



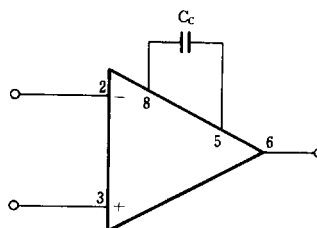


■ ADJUSTMENT METHOD

Offset Adjustment



Frequency Compensation



■ NOTICE

When used in voltage follower circuit, put a current limit resistor into non-inverting input terminal in order to avoid inside input diode destruction when the power supply is turned on. (ref. Fig. 1)

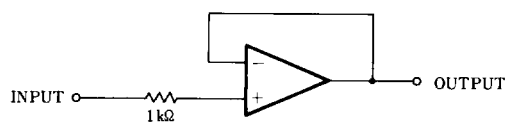


Fig. 1