



DC/DC CONVERTER CONTROL IC

■ GENERAL DESCRIPTION

The NJM2360 is a DC to DC converter control IC. Due to the internalization of a high current output switch, 1.5A switching operations are available. The NJM2360 is designed to be incorporated in step-up, step-down and inverting applications with a minimum number of external components. Output current is limited by an external resistor.

■ PACKAGE OUTLINE



NJM2360D

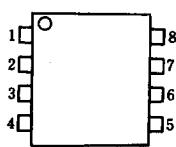


NJM2360M

■ FEATURES

- Operating Voltage (2.5V~40V)
- Low Standby Current
- Current Limiting
- Output Switch Current to 1.5A
- Supply Voltage V⁺ 2.5~40V
- Output Voltage V_{OR} 1.25~40V
- Oscillator Frequency f_{OSC} 100Hz~100kHz
- Package Outline DIP8, DMP8
- Bipolar Technology

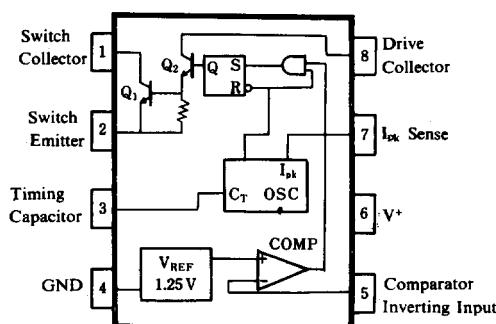
■ PIN CONFIGURATION

NJM2360D
NJM2360M

PIN FUNCTION

1. C_S
2. E_S
3. C_T
4. GND
5. INV_{IN}
6. V⁺
7. S_I
8. C_O

■ BLOCK DIAGRAM





■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

| PARAMETER | SYMBOL | RATINGS | UNIT |
|--------------------------------|------------------|---------------------------------|------|
| Supply Voltage | V ⁺ | 40 | V |
| Comparator Input Voltage Range | V _{IR} | -0.3~V ⁺ | V |
| Power Dissipation | P _D | (DIP8) 875 (DMP8) 700 (note) | mW |
| Switch Current | I _{sw} | 1.5 | A |
| Operating Temperature Range | T _{opr} | -20~+75 | °C |
| Storage Temperature Range | T _{stg} | -40~+125 | °C |

(note) At on PC board

■ ELECTRICAL CHARACTERISTICS

- DC Characteristics (V⁺=5V, Ta=25°C)

| PARAMETER | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNIT |
|-------------------|-----------------|--|------|------|------|------|
| Operating Current | I _{CC} | 5V≤V ⁺ ≤40V, C _T =0.001μF S _I =V ⁺ , INV _{IN} >V _{th} , E _S =GND | — | 2.4 | 3.5 | mA |

Oscillator

| | | | | | | |
|-----------------------------------|---------------------------------------|---------------------------------------|-----|-----|-----|------------------|
| Charge Current | I _{chg} | 5V≤V ⁺ ≤40V | 20 | 35 | 50 | μA |
| Discharge Current | I _{dischg} | 5V≤V ⁺ ≤40V | 150 | 200 | 250 | μA |
| Voltage Swing | V _{OSC} | — | — | 0.5 | — | V _{p-p} |
| Discharge to Charge Current Ratio | I _{dischg} /I _{chg} | S _I =V ⁺ | — | 6 | — | — |
| Peak Current Sense Voltage | V _{IPK(sense)} | I _{chg} =I _{dischg} | 250 | 300 | 350 | mV |

Output Switch (Note 1)

| | | | | | | |
|-----------------------------|-----------------------|--|----|-----|-----|----|
| Saturation Voltage 1 | V _{CE(sat)1} | Darlington Connection (C _S =C _D) I _{sw} =1.0A | — | 1.0 | 1.3 | V |
| Saturation Voltage 2 | V _{CE(sat)2} | I _{sw} =1.0A, I _{C(driver)} =50mA (Forced β=20) | — | 0.5 | 0.7 | V |
| DC Current Gain | h _{FE} | I _{sw} =1.0A, V _{CE} =5.0V | 35 | 120 | — | — |
| Collector Off-State Current | I _{C(off)} | V _{CE} =40V | — | 10 | — | nA |

Comparator

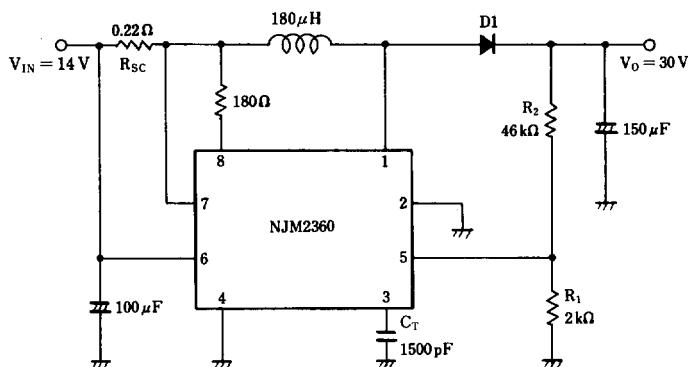
| | | | | | | |
|--------------------|-----------------|---------------------|------|------|------|----|
| Threshold Voltage | V _{th} | V _{IN} =0V | 1.18 | 1.25 | 1.32 | V |
| Input Bias Current | I _{IB} | | — | 40 | 400 | nA |

Note 1 : Output switch tests are performed under pulsed conditions to minimize power dissipation.



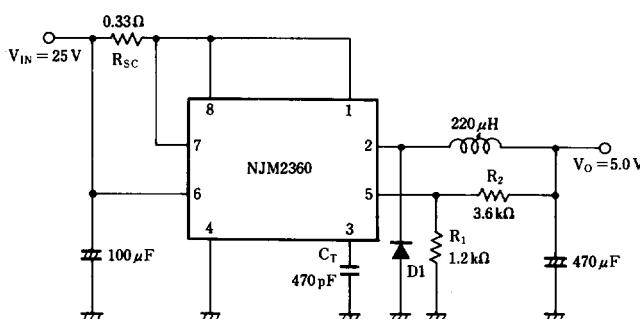
■ TYPICAL APPLICATIONS

1. Step-Up Converter



* D1 : SBD(EK14)

2. Step-Down Converter



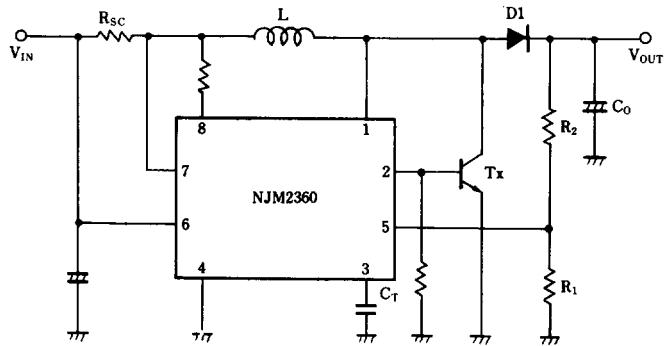
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* D1 : SBD(EK14)

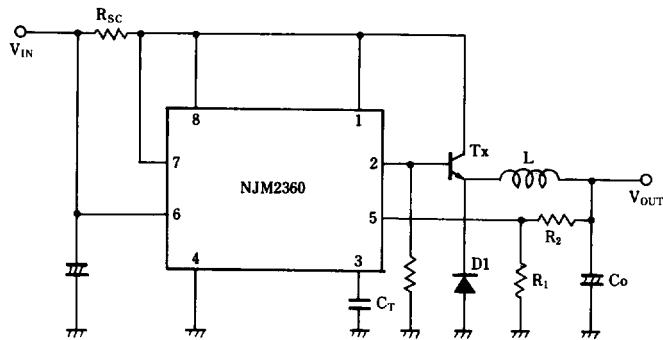


■ TYPICAL APPLICATIONS

3. Step-Up Converter (High Current)

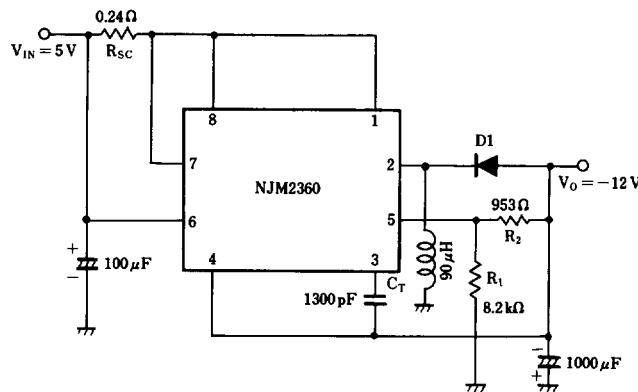


4. Step-Down Converter (High Current)



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5. Inverting Converter



* D1 : SBD(EK14)

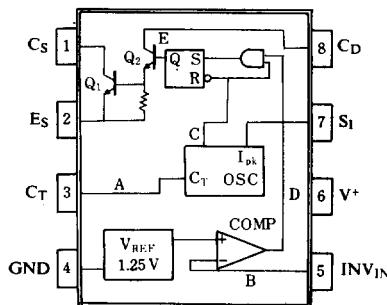


Fig.1 Block Diagram

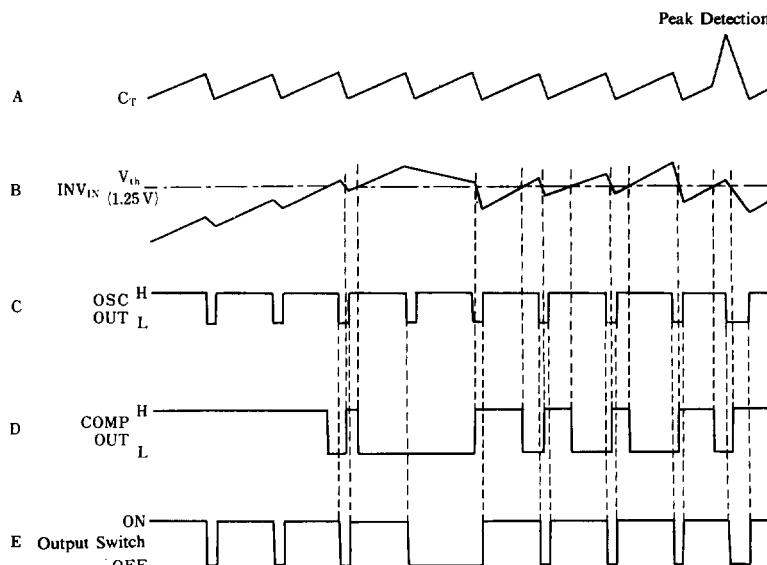
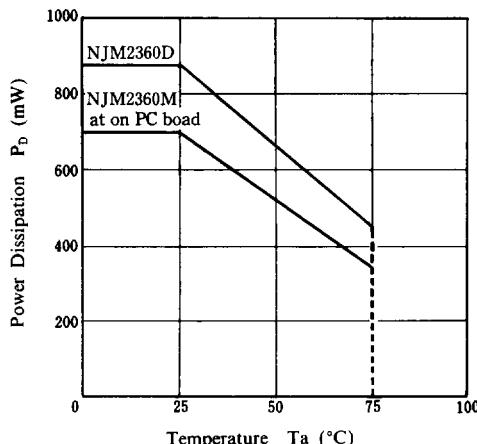


Fig. 2 Timing Chart

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■ POWER DISSIPATION VS. TEMPERATURE

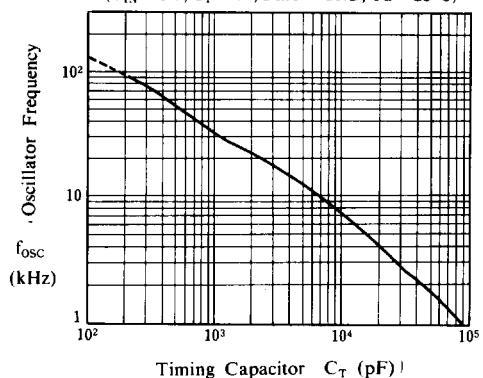




■ TYPICAL CHARACTERISTICS

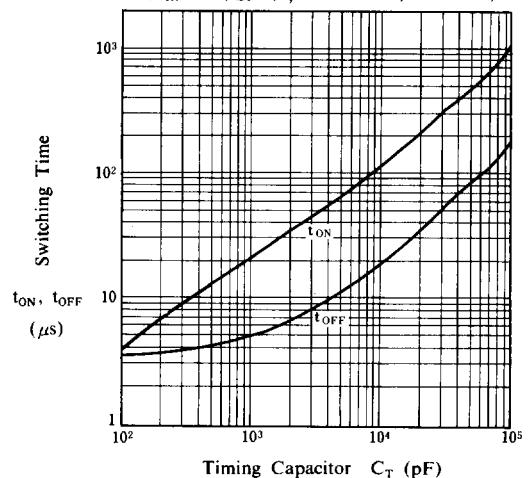
Oscillator Frequency vs. Timing Capacitor

($V_{IN} = 5V$, $S_1 = V^+$, Pin 5 = GND, $T_a = 25^\circ C$)



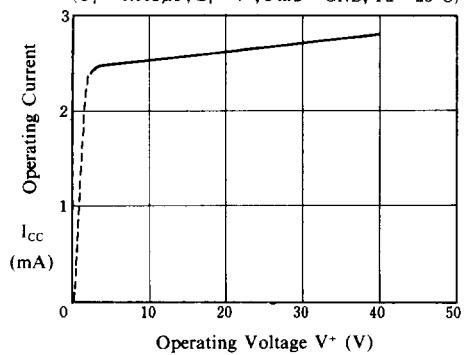
Switching Time vs. Timing Capacitor

($V_{IN} = 5V$, $S_1 = V^+$, Pin 5 = GND, $T_a = 25^\circ C$)



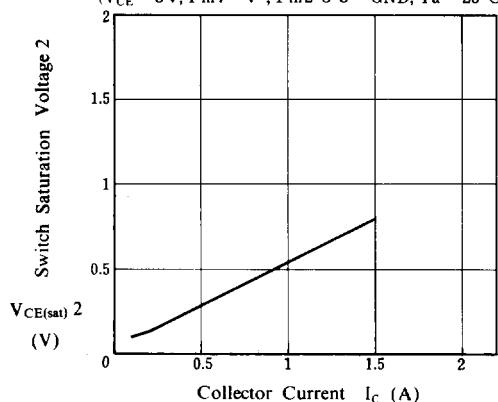
Operating Current vs. Operating Voltage

($C_T = 0.001\mu F$, $S_1 = V^+$, Pin 2 = GND, $T_a = 25^\circ C$)



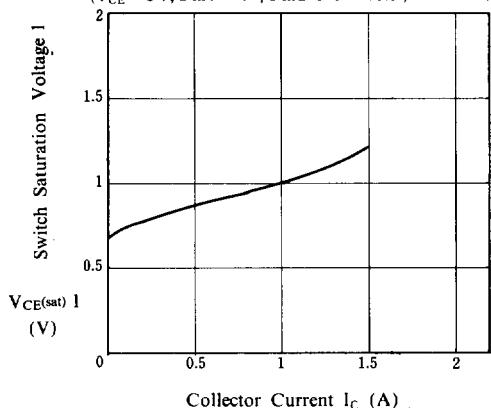
Switch Saturation Voltage 2 vs. Collector Current

($V_{CE} = 5V$, Pin 7 = V^+ , Pin 2·3·5 = GND, $T_a = 25^\circ C$)



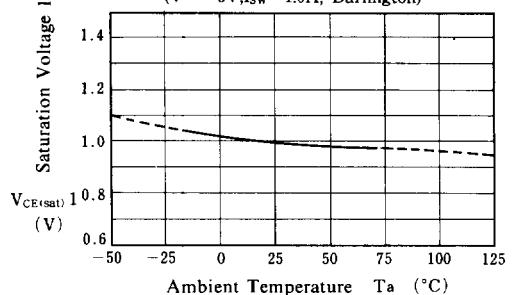
Switch Saturation Voltage 1 vs. Collector Current (Darlington)

($V_{CE} = 5V$, Pin 7 = V^+ , Pin 2·3·5 = GND, $T_a = 25^\circ C$)



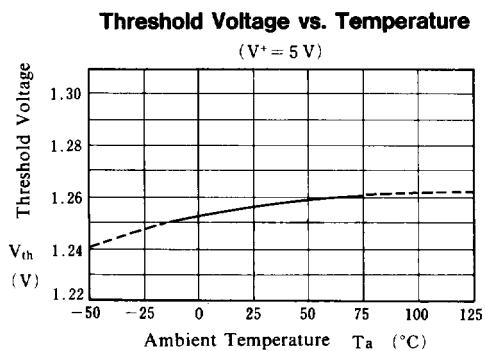
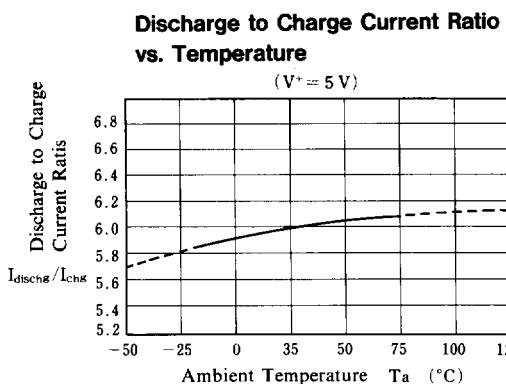
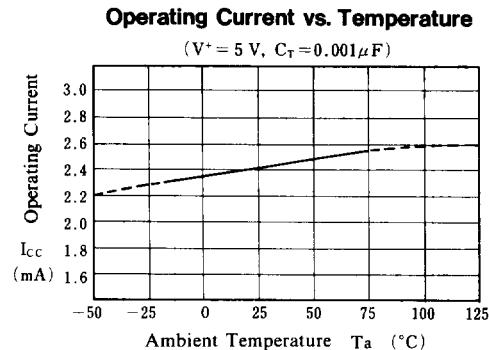
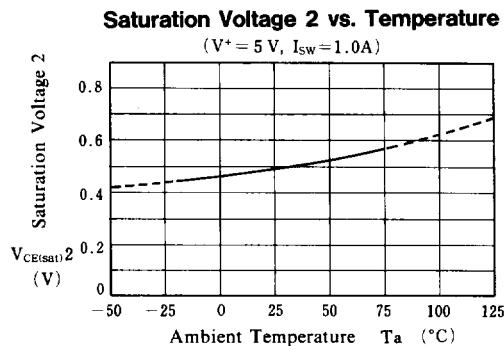
Saturation Voltage 1 vs. Temperature

($V^+ = 5V$, $I_{SW} = 1.0A$, Darlington)





■ TYPICAL CHARACTERISTICS



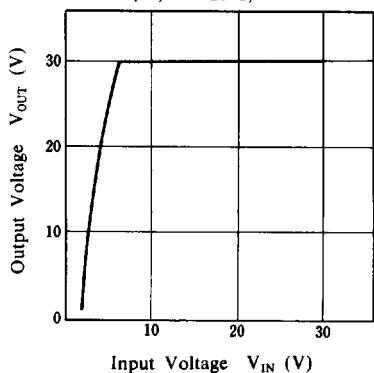


■ TYPICAL CHARACTERISTICS (Application)

1. Step-Up Converter

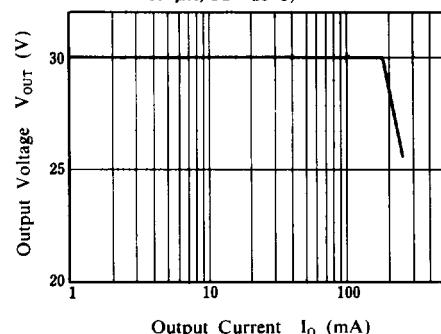
Output Voltage vs. Input Voltage

($V_O = 30V$, $I_O = 100mA$, $C_T = 1500pF$,
 $L = 180\mu H$, $T_a = 25^\circ C$)



Output Voltage vs. Output Current

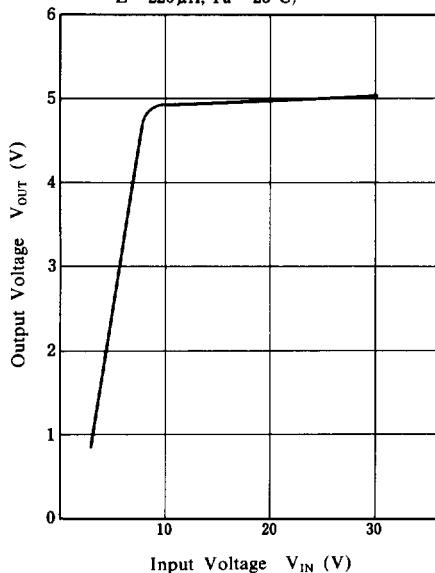
($V_{IN} = 14V$, $V_O = 30V$, $C_T = 1500pF$,
 $L = 180\mu H$, $T_a = 25^\circ C$)



2. Step-Down Converter

Output Voltage vs. Input Voltage

($V_O = 5V$, $I_O = 500mA$, $C_T = 470pF$,
 $L = 220\mu H$, $T_a = 25^\circ C$)



Output Voltage vs. Output Current

($V_{IN} = 25V$, $V_O = 5V$, $C_T = 470pF$,
 $L = 220\mu H$, $T_a = 25^\circ C$)

