

GaAs POWER AMPLIFIER IC

■ GENERAL DESCRIPTION

NJG1304E is a GaAs MMIC designed mainly for driver amplifier of PHS base station in Japan. This is a variable gain type with 20dB dynamic range. It features very low distortion and P_{acp} is less than -70dBc at 17 dBm output power , and enables low voltage and high efficiency operation. Small Plastic mold Package are adopted.

■ PACKAGE OUTLINE



NJG1304E

■ APPLICATIONS

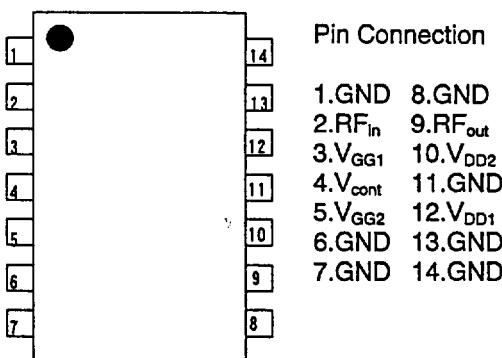
- PHS base station etc.

■ FEATURES

- Variable gain under low distortion
- Low voltage operation ($V_{DD}=3.0V$)
- Input and output internal matching circuits
- High gain (32dB Typ.)
- Low distortion ($P_{acp}=-70$ dBc Typ.@1.9GHz,17dBm)
- Low current consumption ($I_{DD}=180mA$ Typ.@1.9GHz,17dBm)
- Reduction of Parasitic oscillation
- EMP 14 Maximum Power Dissipation: 1000mW($T_j=150^{\circ}\text{C}$,PCB: 24*30*1.0mm FR4)

■ PIN CONFIGURATION

(Top View)





■ ABSOLUTE MAXIMUM RATINGS

(Z_s=Z_o=50ohm, T_a=25°C)

PARAMETER	SYMBOL	CONDITIONS	RATINGS	UNIT
Drain Voltage	V _{DD1} , V _{DD2}	V _{GG1,2} =-0.9V	6	V
Gate Voltage	V _{GG1} , V _{GG2}	V _{DD1,2} =3.0V	-4	V
Gain control voltage	V _{cont}	V _{DD1,2} =3.0V	-4	V
Input Power	P _{in}	V _{DD1,2} =3.0V, V _{GG1,2} =-0.9V	3	dBm
Power Dissipation	P _D	24*30*1.0mm PCB : FR4, T _j =150°C	1000	mW
Operating Temperature	T _{opr}		-30~+85	°C
Storage Temperature	T _{stg}		-40~+150	°C

■ ELECTRICAL CHARACTERISTICS

(f=1.9GHz, Z_s=Z_o=50ohm, Ta=25°C)

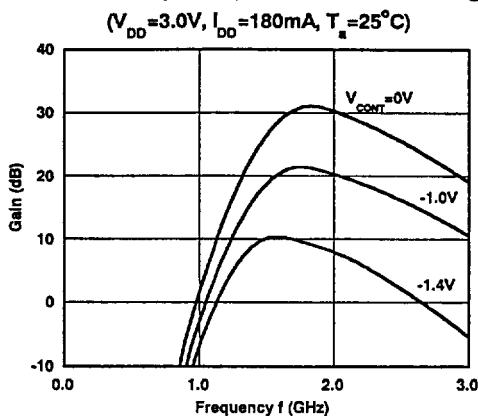
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Operating Frequency	freq	V _{DD1,2} =3.0V	1.89	-	1.92	GHz
Drain Voltage	V _{DD1,2}		2.9	3.0	5.3	V
Gate Voltage	V _{GG1,2}	V _{DD1,2} =3.0V, I _{idle} =180mA	-1.25	-0.9	-0.6	V
Idle Current *1	I _{idle}	V _{DD1,2} =3.0V, No RF Signal	175	180	185	mA
Operating Current *1	I _{DD}	P _{out} =17dBm, V _{DD1,2} =3.0V	170	180	190	mA
Gate Current *2	I _{GG}	P _{out} =17dBm, V _{DD1,2} =3.0V	-150	-70	-	uA
Gain Control Terminal Current	I _{cont}	P _{out} =17dBm, V _{DD1,2} =3.0V -2.0V < V _{cont} < 0.0V	-5	-2	-	uA
Gain Control Voltage	Gain	V _{DD1,2} =3.0V, I _{idle} =180mA	-2.0	-	0	V
Small Signal Gain	G _{flat}	V _{DD1,2} =3.0V, I _{idle} =180mA	29	32	35	dB
Gain Flatness	G _{cont}	V _{cont} =2~0V, V _{DD1,2} =3.0V I _{idle} =180mA	0.0	0.5	1.0	dB
Gain Control Range			18	20	23	dB
Pout at 1dB Compression	P _{-1dB}	P _{out} =17dBm, V _{DD1,2} =3.0V	22	23	-	dBm
Adjacent Channel Leakage Power ①	P _{acp1}	P _{out} =17dBm, offset=600kHz, P _{in} ; π/4 QPSK, V _{DD1,2} =3.0V	-	-	-70	dBc
Adjacent Channel Leakage Power ②	P _{acp2}	P _{out} =17dBm, offset=900kHz, P _{in} ; π/4 QPSK, V _{DD1,2} =3.0V	-	-	-70	dBc
Adjacent Channel Leakage Power ③	P _{acp3}	P _{in} =-7dBm, offset=600kHz P _{out} =13dBm (Adjust V _{cont})	-	-	-70	dBc
Adjacent Channel Leakage Power ④	P _{acp4}	P _{in} ; π/4 QPSK, V _{DD1,2} =3.0V P _{in} =-7dBm, offset=900kHz P _{out} =13dBm (Adjust V _{cont})	-	-	-70	dBc
Harmonics Input VSWR	P _{sp} VSWR _i	P _{out} =21dBm, V _{DD1,2} =3.0V V _{DD1,2} =3.0V	-	-35	-30	dBc
Load VSWR Tolerance		P _{out} =21dBm, V _{DD1,2} =3.0V Load VSWR=4:1, All Phase	-	-	2.2	
			Parasitic Oscillation for Fundamental Signal Level : ≤-60dBc			

*1 ; V_{DD1} Terminal and V_{DD2} Terminal Total Current*2 ; V_{GG1} Terminal and V_{GG2} Terminal Total Current

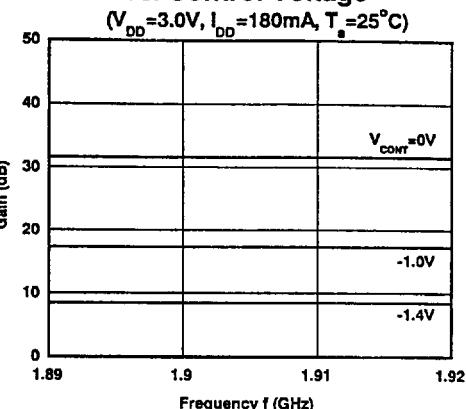


■TYPICAL CHARACTERISTICS

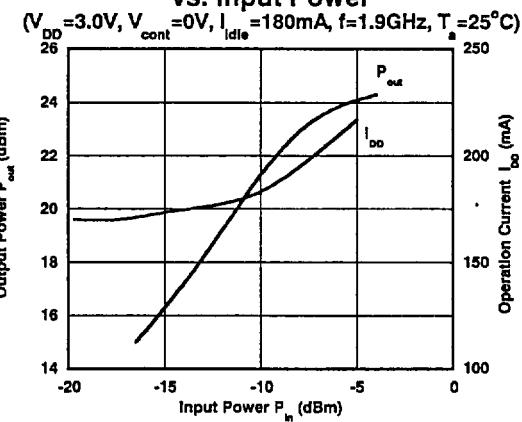
Gain vs. Frequency vs. Control Voltage



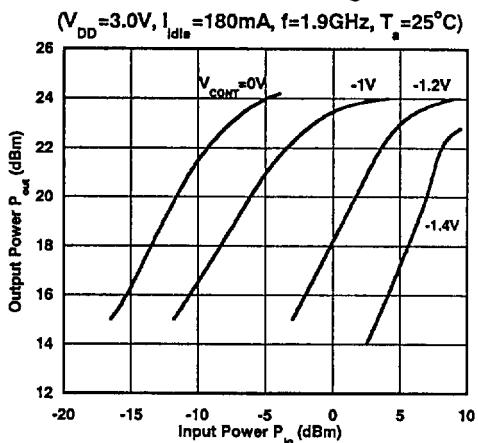
Gain vs PHS Band Frequency vs. Control Voltage



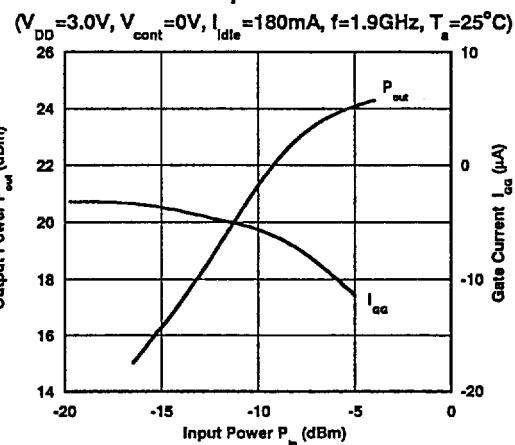
Output Power, Operating Current vs. Input Power



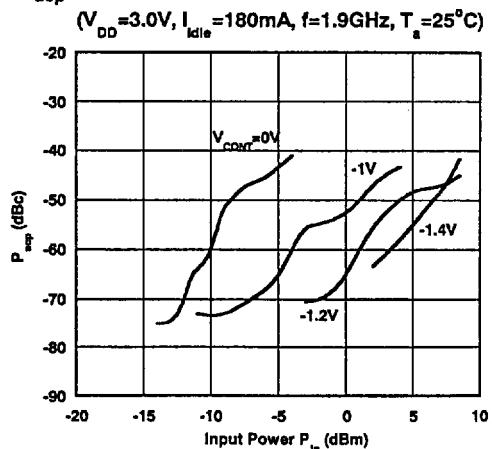
Output Power vs. Input Power vs. Control Voltage



Output Power, Gate Current vs. Input Power



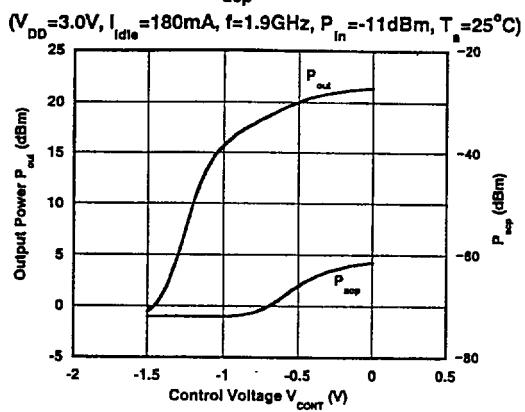
P_{acp} vs. Input Power vs. Control Voltage



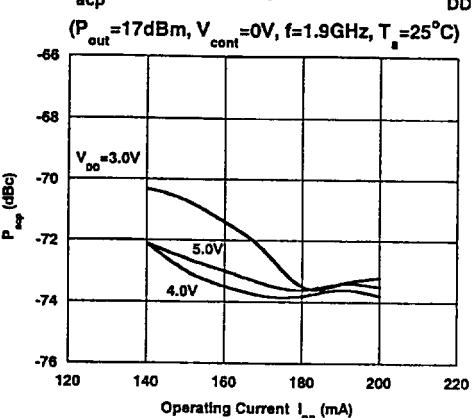


■TYPICAL CHARACTERISTICS

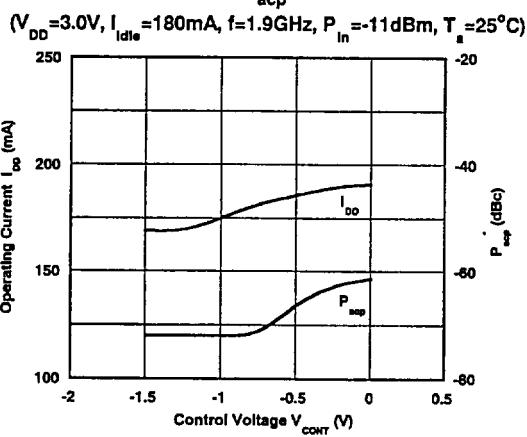
Output Power, P_{acp} vs. Control Voltage



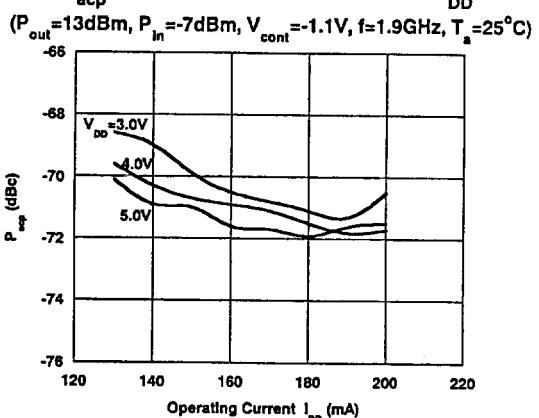
P_{acp} vs. Operating Current vs. V_{DD}



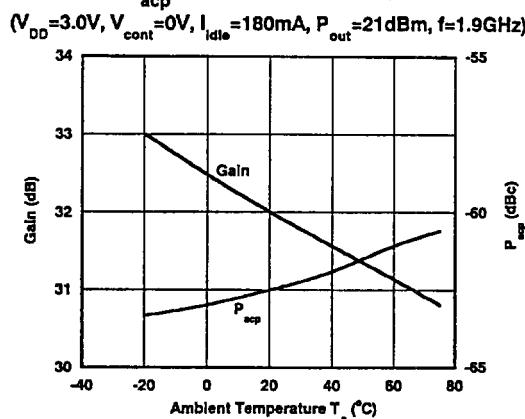
Operating Current, P_{acp} vs Control Voltage



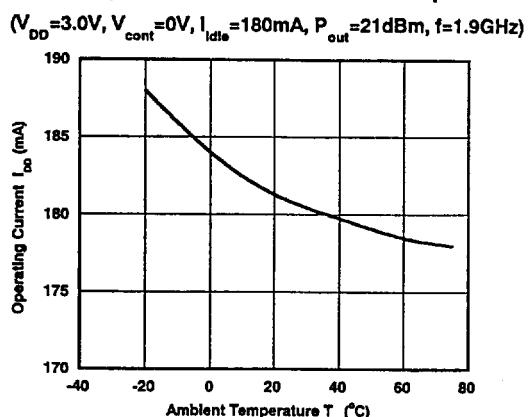
P_{acp} vs. Operating Current vs. V_{DD}



Gain, P_{acp} vs. Ambient Temperature

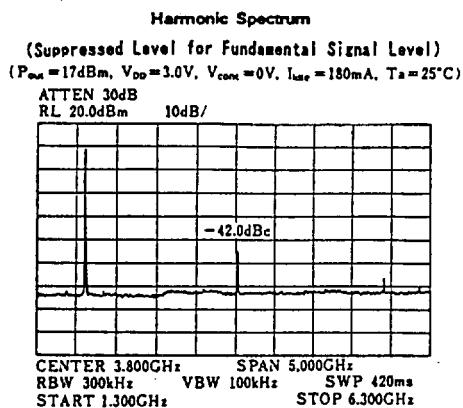
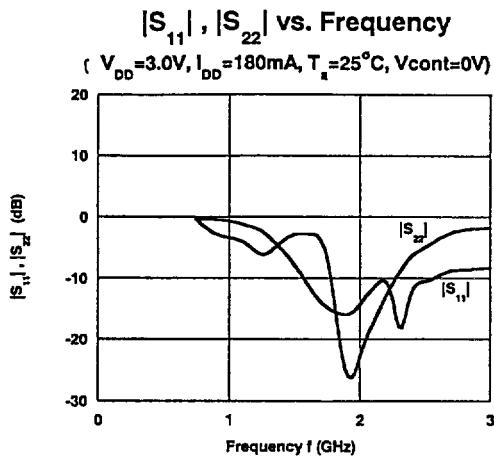


Operating Current vs. Ambient Temperature





TYPICAL CHARACTERISTICS

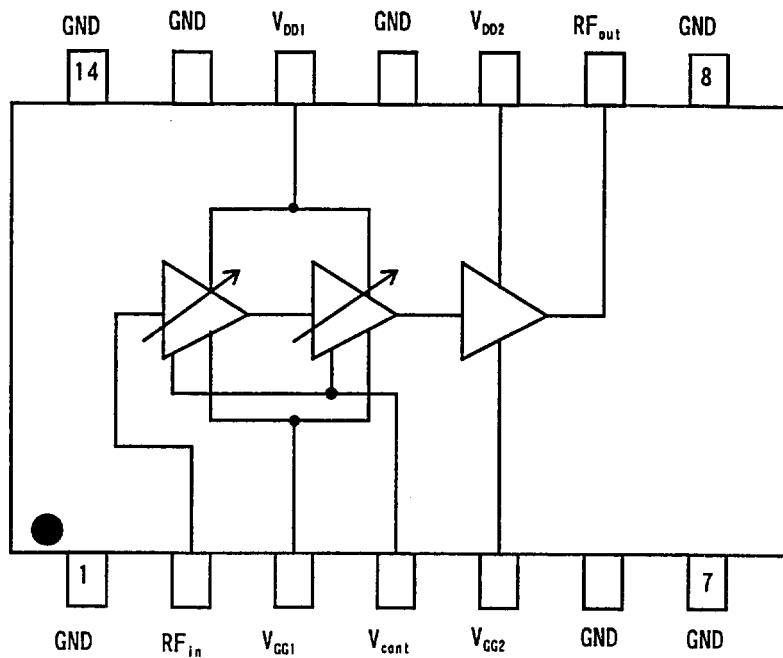


All adjacent channel leakage power used in these characteristics are those of 600KHz offset for fundamental wave at PHS operating condition($\pi/4$ QPSK)

New Japan Radio Co., Ltd.

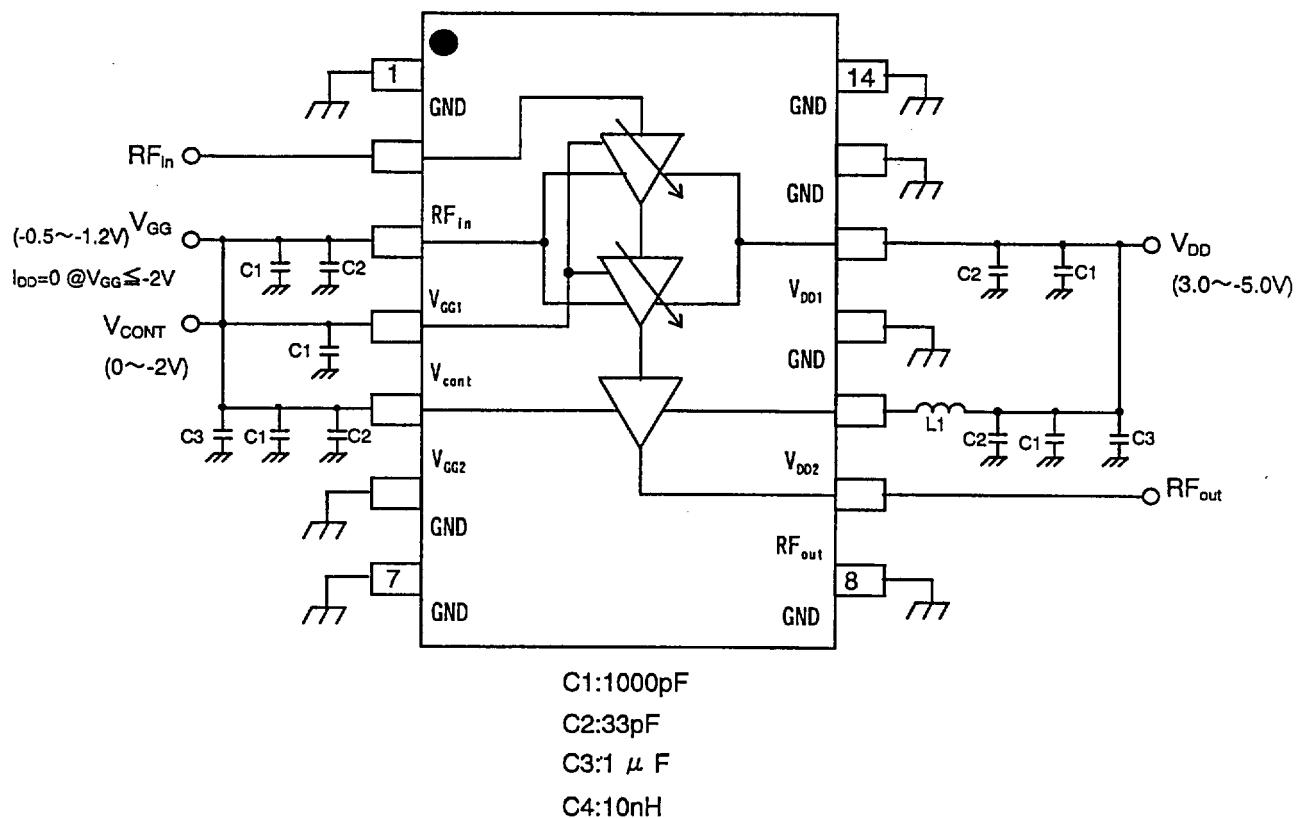


■ BLOCK DIAGRAM

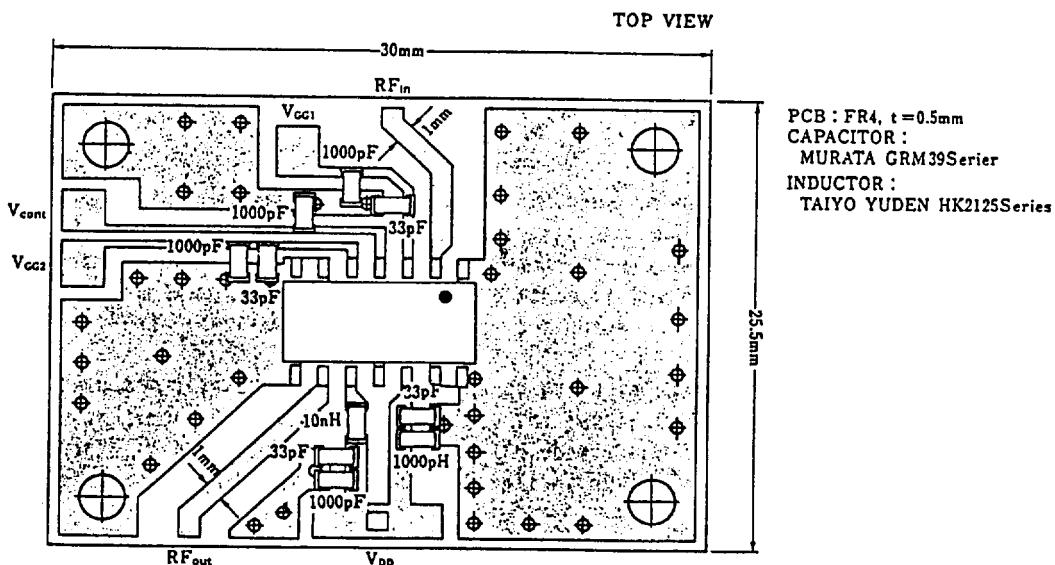




■ RECOMMENDED CIRCUIT



■ RECOMMENDED PCB

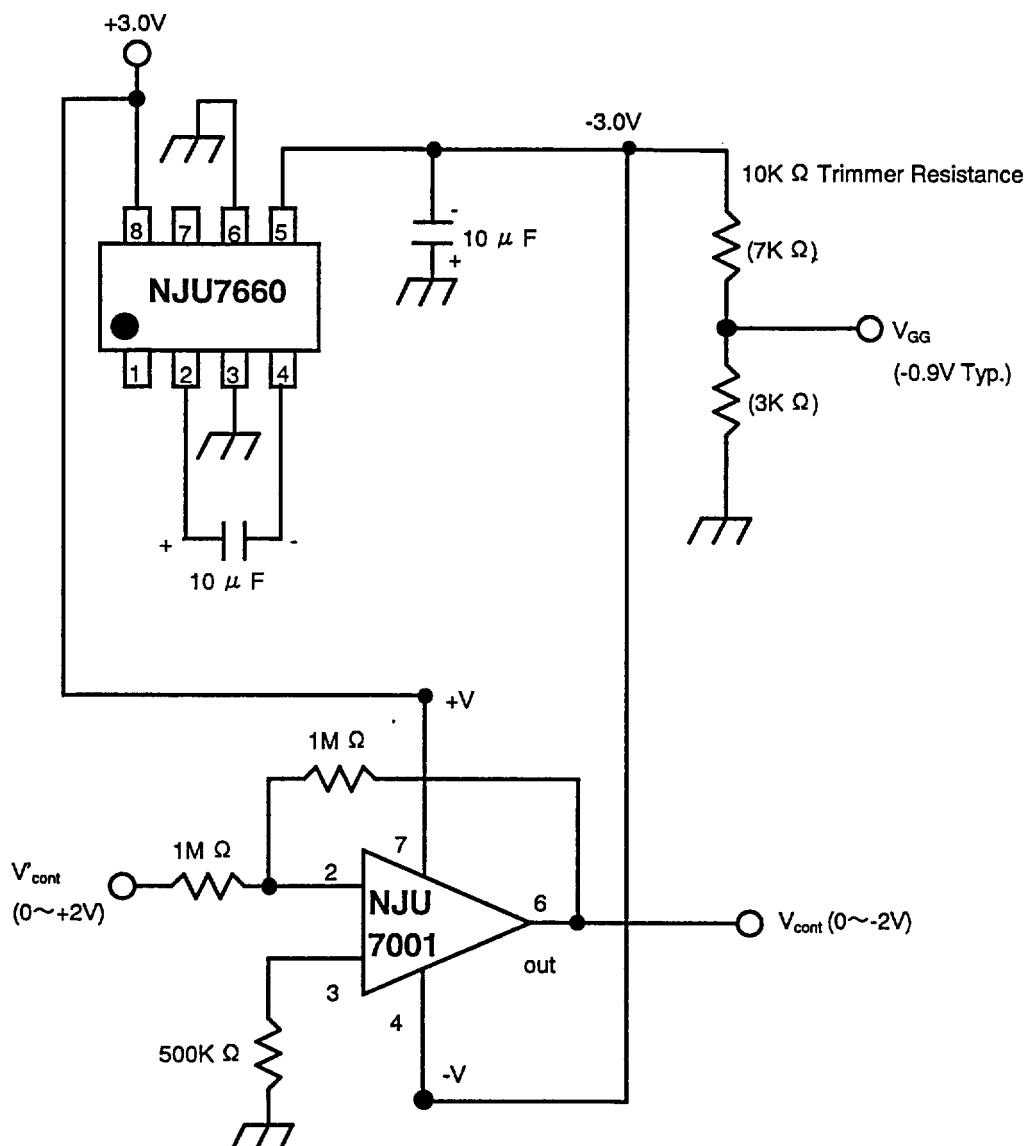


The reflow method is recommended to install this device to PCB.

New Japan Radio Co., Ltd.



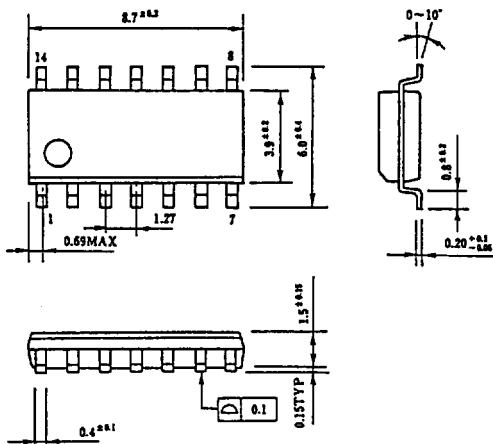
■RECOMMENDED PCB





■ PACKAGE OUTLINE

EMP14



UNIT:mm

Caution on using the products

A GaAs is used in this product. A GaAs is a harmful material.

- Don't eat or in the mouth.
- Don't dispose in fire or break up the products.
- Don't make a gas or a powdered with the chemical reaction.
- In the case of wasting the products, please obey the relation rule in the each country.

This product may be broken with static electric discharge or surge voltage. Therefore, please note a handling.

The other caution item

- The product specifications and descriptions listed in this catalog are subject to change at any time, without notice.
- We don't take upon ourselves the responsibilities that infringe on other people's rights of a patents bringing about the information and drawing in this catalog.
- It is not purpose to be equipped with the system needs a high reliability as air system, submarine cable system, atomic energy control system and medical instrument for keeping life.
- If you think the above system, please ask for the sales office before.