QUICKSWITCH® PRODUCTS HIGH-SPEED CMOS 32-BIT LOW RESISTANCE QUICKSWITCH WITH PRECHARGED OUTPUTS

IDTQS34XR800

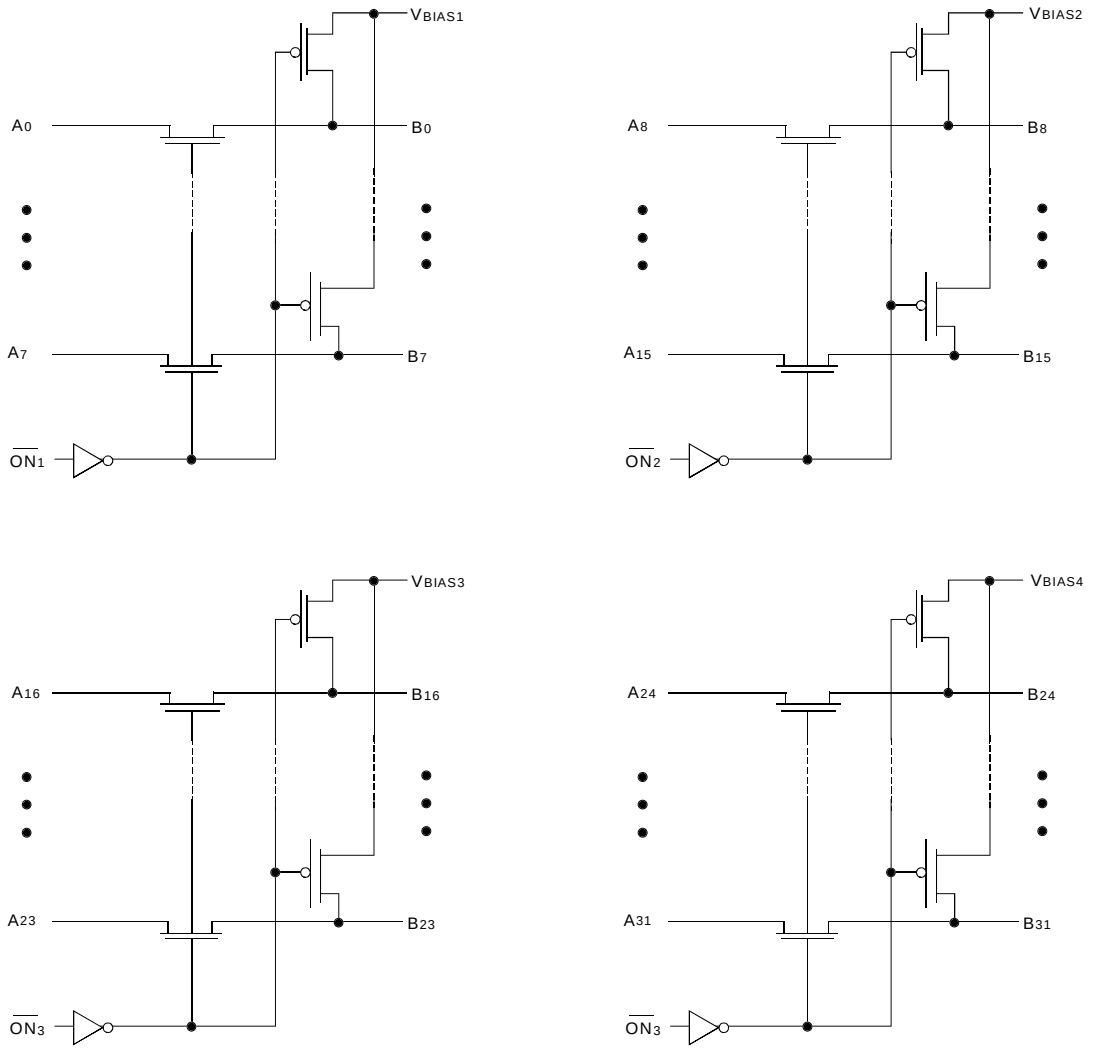
FEATURES:

- 2.5Ω bidirectional switches connect inputs to outputs
- Zero propagation delay
- Undershoot clamp diodes on all switch and control pins
- Outputs precharge voltage to minimize signal distortion during live insertion
- TTL-compatible input and output levels
- Zero ground bounce
- Flow-through pinout for easy layout
- Available in 80-pin MillipaQ (Q3)

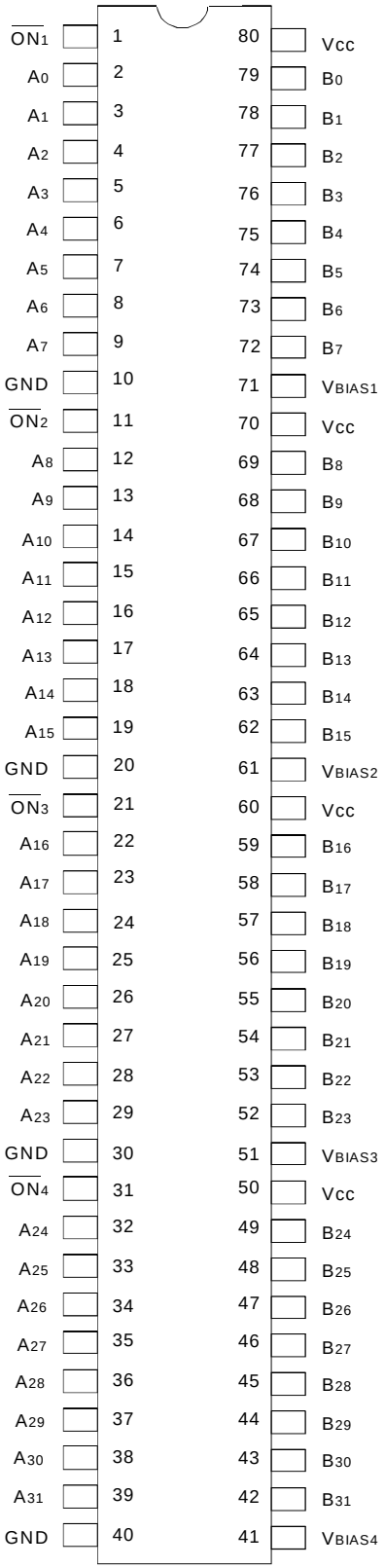
DESCRIPTION:

The QS34XR800 is a 32-bit high-speed CMOS bus switch controlled by a four enable ($\overline{ON}x$) input in a flow-through pinout. When $\overline{ON}x$ is low, the switch is on and port A is connected to port B. When $\overline{ON}x$ is high, the switch between port A and port B is open and port B is precharged to the bias voltage. The low ON resistance (2.5Ω) of the QS34XR800 allows inputs to be connected to outputs without adding propagation delay and without generating additional noise. The QS34XR800 also precharges the B port to a user-selectable bias voltage to minimize live-insertion noise, which is useful in VME bus applications.

FUNCTIONAL BLOCK DIAGRAM



PIN CONFIGURATION



MILLIPAQ
TOP VIEW

ABSOLUTE MAXIMUM RATINGS (1)

Symbol	Description	Max.	Unit
V_{TERM}(2)	Supply Voltage to Ground	– 0.5 to +7	V
V_{TERM}(3)	DC Switch Voltage V_S	– 0.5 to +7	V
V_{TERM}(3)	DC Input Voltage V_{IN}	– 0.5 to +7	V
V_{AC}	AC Input Voltage (pulse width ≤20ns)	-3	V
I_{OUT}	DC Output Current	120	mA
P_{MAX}	Maximum Power Dissipation (T_A = 85°C)	.5	W
T_{STG}	Storage Temperature	– 65 to +150	°C

NOTES:
1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
2. Vcc Terminals.
3. All terminals except Vcc.

CAPACITANCE

(T_A = +25°C, f = 1.0MHz, V_{IN} = 0V, V_{OUT} = 0V)

Pins	Typ.	Max. (1)	Unit
Control Inputs	3	4	pF
Quickswitch Channels (Switch OFF)	5	8	pF

NOTE:
1. This parameter is guaranteed at characterization but not tested.

PIN DESCRIPTION

Pin Names	I/O	Description
A_0 - A_{31}	I/O	Bus A
B_0 - B_{31}	I/O	Bus B
$\overline{ON}$x	I	Bus Switch Enable
V_{BIASX}	I	Bias Voltage

FUNCTION TABLE(1)

$\overline{ON}$x (2)	Bm-Bn	Function
L	Am - An	Connect
H	V_{BIASX}	Precharge

NOTES:
1. H = HIGH Voltage Level
   L = LOW Voltage Level
2. For X = 1 m = 0 n = 7
   X = 2 m = 8 n = 15
   X = 3 m = 16 n = 23
   X = 4 m = 24 n = 31

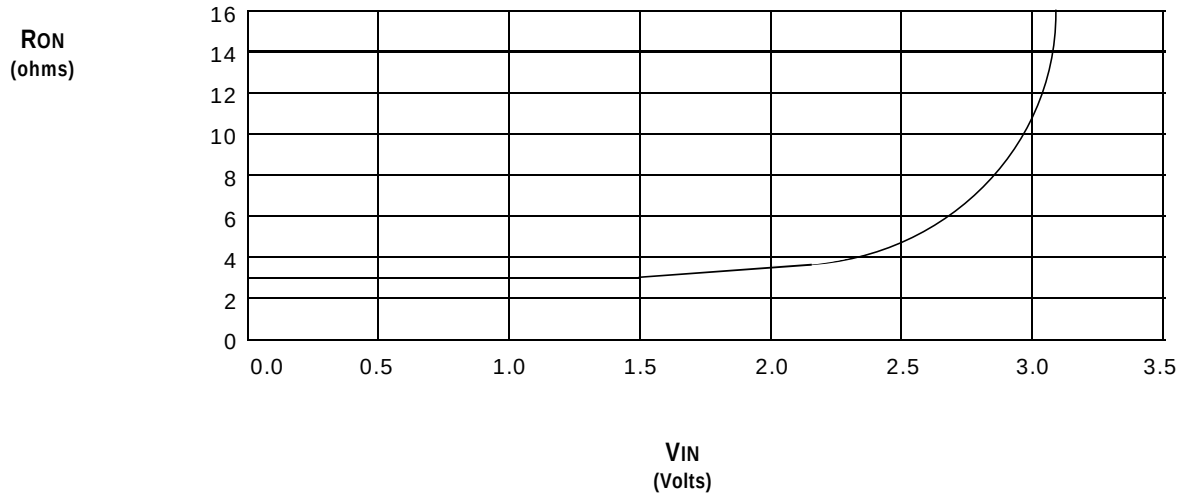
DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:
Industrial: T_A = -40°C to +85°C, V_{CC} = 5.0V ± 10%

Symbol	Parameter	Test Conditions	Min.	Typ.(1)	Max.	Unit
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH for Control Pins	2	—	—	V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW for Control Pins	—	—	0.8	V
V_{BIAS}	Bias Voltage	Vcc = 5V, $\overline{ON}$x = HIGH	1.3	—	Vcc	V
I_O	Bias Current	Vcc = 4.5V, V_{BIAS} = 2.4V, V_{OUT} = 0, $\overline{ON}$x = HIGH	0.25	—	—	mA
I_{IN}	Input Leakage Current (Control Inputs)	0V ≤ V_{IN} ≤ Vcc	—	—	±1	µA
I_{OZ}	Off-State Current (Hi-Z)	0V ≤ V_{OUT} ≤ Vcc	—	—	±1	µA
R_{ON}	Switch On Resistance (2)	Vcc = Min., V_{IN} = 0V, I_{ON} = 30mA	—	2.5	5	Ω
R_{ON}	Switch On Resistance (2)	Vcc = Min., V_{IN} = 2.4V, I_{ON} = 15mA	—	4	8.5	Ω

NOTES:
1. Typical values are at V_{CC} = 5.0V, T_A = 25°C.
2. During input/output leakage, testing all pins are at HIGH or LOW state.

TYPICAL ON RESISTANCE vs V_{IN} AT Vcc = 5V



POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions[1]	Typ.[2]	Max.	Unit
I_{CCQ}	Quiescent Power Supply Current	V_{CC} = Max., V_{IN} = GND or Vcc, f = 0	0.2	6	µA
ΔI_{CC}	Power Supply Current per Control Input HIGH	V_{CC} = Max., V_{IN} = 3.4V [3], f = 0	—	6.5	mA
I_{CCD}	Dynamic Power Supply Current per MHz[4]	V_{CC} = Max., A and B pins open Data inputs = GND Control Input Toggling at 50% Duty Cycle	—	0.25	mA/MHz

NOTES:
1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
2. Typical values are at V_{CC} = 5.0V, T_A = 25°C.
3. Per TLL driven input (V_{IN} = 3.4V, control inputs only). A and B pins do not contribute to ΔI_{CC}.
4. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

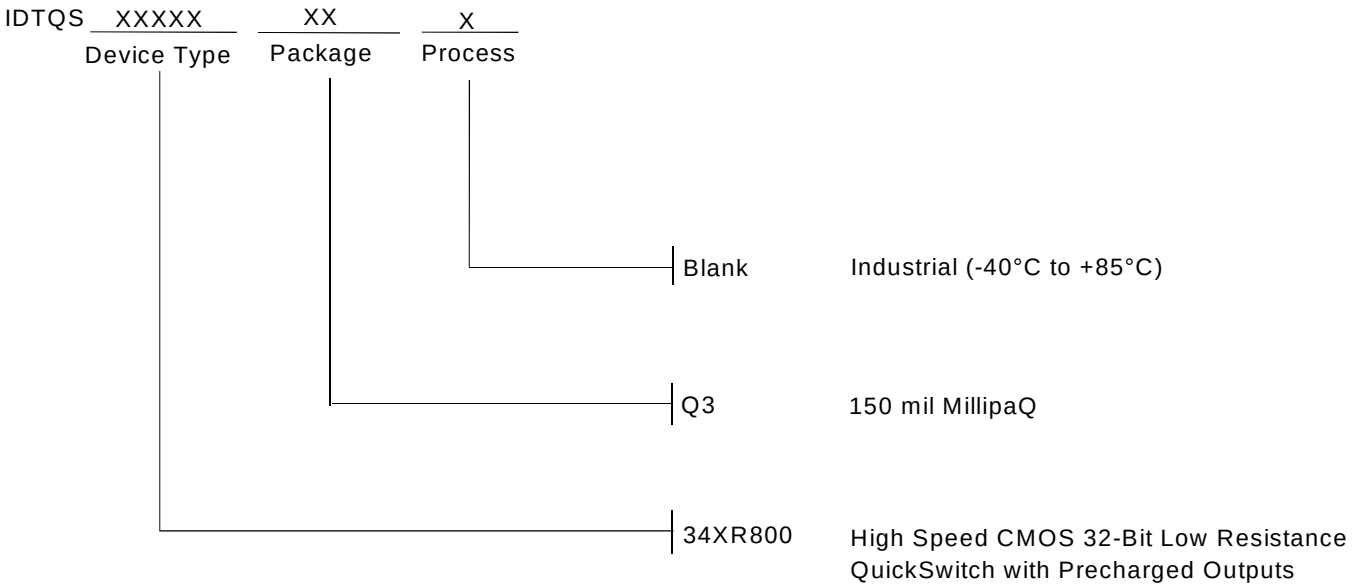
SWITCHING CHARACTERISTICS OVER OPERATING RANGE

T_A = -40°C to +85°C, V_{CC} = 5.0V ± 10%
C_{LOAD} = 50pF, R_{LOAD} = 500Ω unless otherwise noted.

Symbol	Parameter	Min. [1]	Typ.	Max.	Unit
t_{PLH} t_{PHL}	Data Propagation Delay [1,2] A to B or B to A	—	—	0.12	ns
t_{PZL} t_{PZH}	Switch Turn-On Delay $\overline{ON}$x to A or B	1.5	—	7.5	ns
t_{PLZ} t_{PHZ}	Switch Turn-Off Delay [1] $\overline{ON}$x to A or B	1.5	—	6.5	ns

NOTES:
1. This parameter is guaranteed but not tested
2. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.12ns for C_L = 50pF. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

ORDERING INFORMATION



CORPORATE HEADQUARTERS
2975 Stender Way
Santa Clara, CA 95054

for SALES:
800-345-7015 or 408-727-6116
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www.idt.com*

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