

# QUICKSWITCH® PRODUCTS HIGH-SPEED CMOS QUICKSWITCH QUAD 2:1 MUX/DEMUX

IDTQS3257

### **FEATURES:**

- Enhanced N channel FET with no inherent diode to Vcc
- 5 $\Omega$  bidirectional switches connect inputs to outputs
- Pin compatible with the 74F257, 74FCT257, and 74FCT257T
- Zero propagation delay, zero ground bounce
- Undershoot clamp diodes on all switch and control inputs
- TTL-compatible control inputs
- Available in QSOP, SOIC, and S1 Packages

### **APPLICATIONS:**

- Logic replacement
- Video, audio, graphics switching, muxing
- Hot-swapping, hot-docking
- Voltage translation (5V to 3.3V)
- Bus funneling

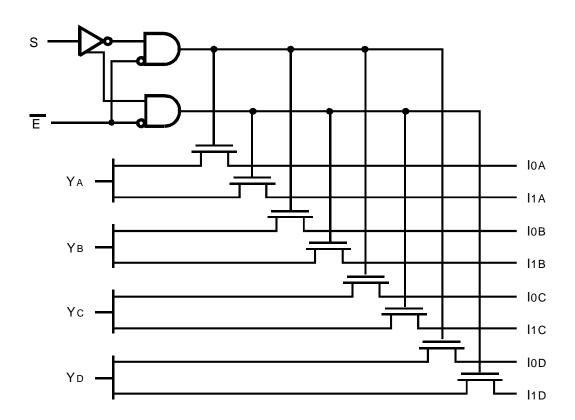
### **DESCRIPTION:**

The QS3257 is a high-speed CMOS LVTTL-compatible Quad 2:1 multiplexer/demultiplexer. The QS3257 is a function and pinout compatible QuickSwitch version of the 74F257, 74FCT257, and the 74ALS/AS/LS257 Quad 2:1 multiplexers. The low ON resistance of the QS3257 allows inputs to be connected to outputs without adding propagation delay and without generating additional ground bounce noise.

Mux/Demux devices provide an order of magnitude faster speed than equivalent logic devices.

The QS3257 is characterized for operation at -40°C to +85°C.

# **FUNCTIONAL BLOCK DIAGRAM**

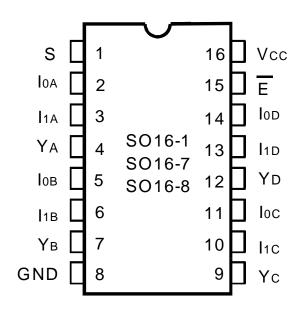


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INDUSTRIAL TEMPERATURE RANGE

**NOVEMBER 2001** 

# **PIN CONFIGURATION**



SOIC/ QSOP/ S1 TOP VIEW

# **ABSOLUTE MAXIMUM RATINGS (1)**

Symbol	Description	Max.	Unit
VTERM <sup>(2)</sup>	Supply Voltage to Ground	- 0.5 to +7	V
VTERM <sup>(3)</sup>	DC Switch Voltage Vs	- 0.5 to +7	V
V <sub>TERM</sub> (3)	DC Input Voltage VIN	- 0.5 to +7	V
VAC	AC Input Voltage (pulse width ≤20ns)	-3	V
lout	DC Output Current	120	mA
Рмах	Maximum Power Dissipation (TA = 85°C)	0.5	W
Tstg	Storage Temperature	- 65 to +150	°C

#### NOTES:

- Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
- 2. Vcc Terminals.
- 3. All terminals except Vcc.

## **CAPACITANCE**

 $(TA = +25^{\circ}C, f = 1.0MHz, VIN = 0V, VOUT = 0V)$ 

Pins		Тур.	Max. (1)	Unit
Control Pins		4	5	pF
Quickswitch Channels	Demux	5	7	pF
(Switch OFF)	Mux	8	9	pF

#### NOTE:

1. This parameter is guaranteed but not production tested.

### PIN DESCRIPTION

Pin Names	I/O	Description
lxx	1	Data Inputs
S	I	Select Input
Ē	I	Enable Input
YA - YD	0	Data Outputs

## **FUNCTION TABLE**(1)

Inp	uts	Outputs				
Ē	S	YA	Υв	Yc	YD	Function
Н	Χ	Z	Z	Z	Z	Disconnected
L	L	loa	lob	loc	lod	Select 0
L	Н	l <sub>1A</sub>	I <sub>1B</sub>	I1C	l <sub>1D</sub>	Select 1

#### NOTE:

- 1. H = HIGH Voltage Level
  - L = LOW Voltage Level
  - X = Don't Care
  - Z = High-Impedence

# DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:

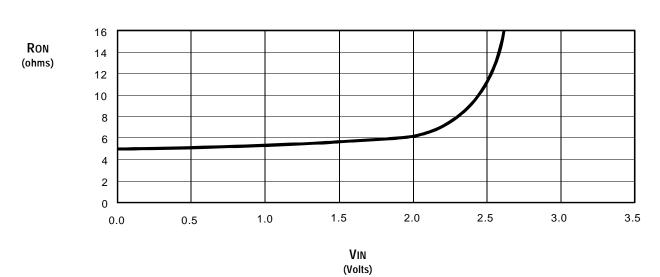
Industrial: TA =  $-40^{\circ}$ C to  $+85^{\circ}$ C, Vcc = 5.0V ± 5%

Symbol	Parameter	Test Conditions	Min.	Typ. <sup>(1)</sup>	Max.	Unit
VIH	Input HIGH Voltage	Guaranteed Logic HIGH for Control Pins	2	_	_	V
VIL	Input LOW Voltage	Guaranteed Logic LOW for Control Pins	_	_	0.8	٧
lin	Input Leakage Current (Control Inputs)	0V ≤ VIN ≤ Vcc	_	_	±1	μΑ
loz	Off-State Current (Hi-Z)	0V ≤ Vout ≤ Vcc	_	_	±1	μΑ
Ron	Switch ON Resistance	Vcc = Min., VIN = 0V, ION = 30mA	_	5	7	Ω
Ron	Switch ON Resistance	Vcc = Min., VIN = 2.4V, ION = 15mA	_	10	15	Ω
VP	Pass Voltage (2)	$VIN = VCC = 5V$ , $IOUT = -5\mu A$	3.7	4	4.2	V

### NOTES:

- 1. Typical values are at Vcc = 5.0V, TA = 25°C.
- 2. Pass voltage is guaranteed but not production tested.

# TYPICAL ON RESISTANCE vs Vin AT Vcc = 5V



## **POWER SUPPLY CHARACTERISTICS**

Symbol	Parameter	Test Conditions <sup>(1)</sup>	Max.	Unit
Icco	Quiescent Power Supply Current	Vcc = Max., Vin = GND or Vcc, f = 0	3	μΑ
Δlcc	Power Supply Current per Control Input HIGH (2)	Vcc = Max., Vin = 3.4V, f = 0	1.5	mA
ICCD	Dynamic Power Supply Current per MHz <sup>(3)</sup>	Vcc = Max., I and Y pins open	0.25	mA/MHz
		Control Inputs Toggling at 50% Duty Cycle		

#### NOTES:

- 1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
- 2. Per TLL driven input (VIN = 3.4V, control inputs only). I and Y pins do not contribute to Δlcc.
- 3. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The I and Y inputs generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

## **SWITCHING CHARACTERISTICS OVER OPERATING RANGE**

 $TA = -40^{\circ}C \text{ to } +85^{\circ}C, VCC = 5.0V \pm 5\%$ 

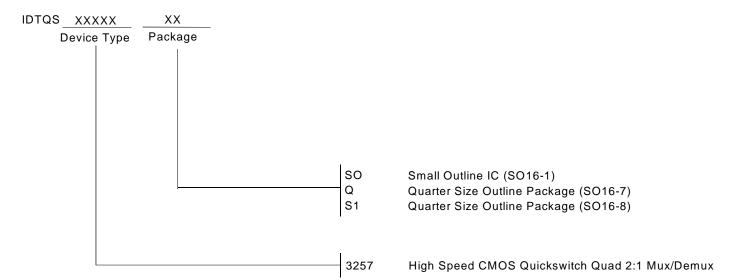
CLOAD = 50pF, RLOAD =  $500\Omega$  unless otherwise noted.

Symbol	Parameter	Min. <sup>(1)</sup>	Тур.	Max.	Unit
tplh tphl	Data Propagation Delay <sup>(2,3)</sup> In to Y	_	_	0.25	ns
tpzh tpzl	Switch Turn-on Delay Sn to Y	0.5	_	5.2	ns
tpzh tpzl	Switch Turn-on Delay  EN to Y	0.5	_	4.8	ns
tphz tplz	Switch Turn-off Delay <sup>(2)</sup> EN to Y, Sn to Y	0.5	_	5	ns

#### NOTES:

- 1. Minimums are guaranteed but not production tested.
- 2. This parameter is guaranteed but not production tested.
- 3. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for CL = 50pF. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

# **ORDERING INFORMATION**





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