

# International Rectifier

## HEXFET® POWER MOSFET

Provisional Data Sheet No. PD-9.431B

**JANTX2N6798**  
**JANTXV2N6798**  
**[REF:MIL-PRF-19500/557]**  
**[GENERIC:IRFF230]**  
**N-CHANNEL**

### 200 Volt, 0.40Ω HEXFET

HEXFET technology is the key to International Rectifier's advanced line of power MOSFET transistors. The efficient geometry achieves very low on-state resistance combined with high transconductance.

HEXFET transistors also feature all of the well-established advantages of MOSFETs, such as voltage control, very fast switching, ease of paralleling and electrical parameter temperature stability. They are well-suited for applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers, and high energy pulse circuits, and virtually any application where high reliability is required.

### Product Summary

Part Number	BVDSS	RDS(on)	ID
JANTX2N6798	200V	0.40Ω	5.5A
JANTXV2N6798			

### Features:

- Avalanche Energy Rating
- Dynamic dv/dt Rating
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed

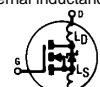
### Absolute Maximum Ratings

	Parameter	JANTX2N6798, JANTXV2N6798	Units
ID @ VGS = 10V, TC = 25°C	Continuous Drain Current	5.5	A
ID @ VGS = 10V, TC = 100°C	Continuous Drain Current	3.5	
IMD	Pulsed Drain Current ①	22	
PD @ TC = 25°C	Max. Power Dissipation	25	W
	Linear Derating Factor	0.20	W/K ⑤
VGS	Gate-to-Source Voltage	±20	V
dv/dt	Peak Diode Recovery dv/dt ③	4.5	V/ns
TJ TSTG	Operating Junction Storage Temperature Range	-55 to 150	°C
	Lead Temperature	300 (0.063 in. (1.6mm) from case for 10.5 seconds)	
	Weight	0.98 (typical)	

## JANTX2N6798, JANTXV2N6798 Device

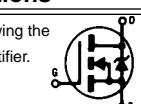
### Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (Unless Otherwise Specified)

	Parameter	Min.	Typ.	Max.	Units	Test Conditions
BVDSS	Drain-to-Source Breakdown Voltage	200	—	—	V	$V_{GS} = 0\text{V}, I_D = 1.0\text{ mA}$
$\Delta BVDSS/\Delta T_J$	Temperature Coefficient of Breakdown Voltage	—	0.25	—	$\text{V}/^\circ\text{C}$	Reference to $25^\circ\text{C}$ , $I_D = 1.0\text{ mA}$
RDS(on)	Static Drain-to-Source On-State Resistance	—	—	0.40	$\Omega$	$V_{GS} = 10\text{V}, I_D = 3.5\text{A}$ ④
		—	—	0.46		$V_{GS} = 10\text{V}, I_D = 5.5\text{A}$
VGS(th)	Gate Threshold Voltage	2.0	—	4.0	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
gfs	Forward Transconductance	2.5	—	—	S (Ω)	$V_{DS} > 15\text{V}, I_{DS} = 3.5\text{A}$ ④
IDSS	Zero Gate Voltage Drain Current	—	—	25	$\mu\text{A}$	$V_{DS} = 0.8 \times \text{Max Rating}, V_{GS} = 0\text{V}$
		—	—	250		$V_{DS} = 0.8 \times \text{Max Rating}$ $V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$
IGSS	Gate-to-Source Leakage Forward	—	—	100	nA	$V_{GS} = 20\text{V}$
IGSS	Gate-to-Source Leakage Reverse	—	—	-100		$V_{GS} = -20\text{V}$
Qg	Total Gate Charge	7.4	—	42.1	nC	$V_{GS} = 10\text{V}, I_D = 5.5\text{A}$
Qgs	Gate-to-Source Charge	2.5	—	5.3		$V_{DS} = \text{Max. Rating} \times 0.5$
Qgd	Gate-to-Drain ("Miller") Charge	6.0	—	28	ns	see figures 6 and 13
td(on)	Turn-On Delay Time	—	—	30		$V_{DD} = 100\text{V}, I_D = 5.5\text{A}, R_G = 7.5\Omega, V_{GS} = 10\text{V}$
tr	Rise Time	—	—	50	ns	see figure 10
td(off)	Turn-Off Delay Time	—	—	50		
tf	Fall Time	—	—	40	nH	Measured from the drain lead, 6mm (0.25 in.) from package to center of die.
LD	Internal Drain Inductance	—	5.0	—		
LS	Internal Source Inductance	—	15	—	nH	Measured from the source lead, 6mm (0.25 in.) from package bonding pad to source bonding pad.
		—	—	—		
Ciss	Input Capacitance	—	600	—	pF	$V_{GS} = 0\text{V}, V_{DS} = 25\text{V}$ $f = 1.0\text{ MHz}$ see figure 5
Coss	Output Capacitance	—	250	—		
Crss	Reverse Transfer Capacitance	—	80	—		



### Source-Drain Diode Ratings and Characteristics

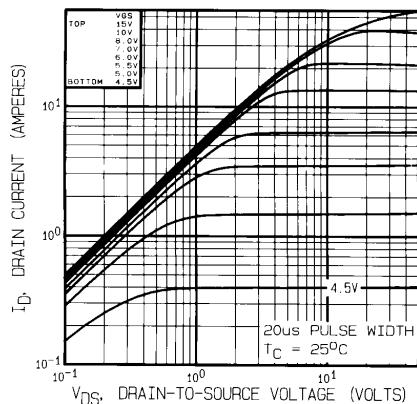
	Parameter	Min.	Typ.	Max.	Units	Test Conditions
IS	Continuous Source Current (Body Diode)	—	—	5.5	A	Modified MOSFET symbol showing the integral reverse p-n junction rectifier.
ISM	Pulse Source Current (Body Diode) ①	—	—	22	A	
VSD	Diode Forward Voltage	—	—	1.4	V	$T_j = 25^\circ\text{C}, I_S = 5.5\text{A}, V_{GS} = 0\text{V}$ ④
t <sub>rr</sub>	Reverse Recovery Time	—	—	500	ns	$T_j = 25^\circ\text{C}, I_F = 5.5\text{A}, dI/dt \leq 100\text{A}/\mu\text{s}$
QRR	Reverse Recovery Charge	—	—	6.0	$\mu\text{C}$	$V_{DD} \leq 50\text{V}$ ④
t <sub>on</sub>	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by $L_S + L_D$ .				



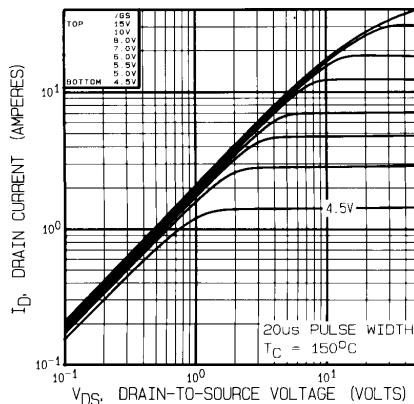
### Thermal Resistance

	Parameter	Min.	Typ.	Max.	Units	Test Conditions
R <sub>thJC</sub>	Junction-to-Case	—	—	5.0	K/W	
R <sub>thJA</sub>	Junction-to-Ambient	—	—	175		Typical socket mount

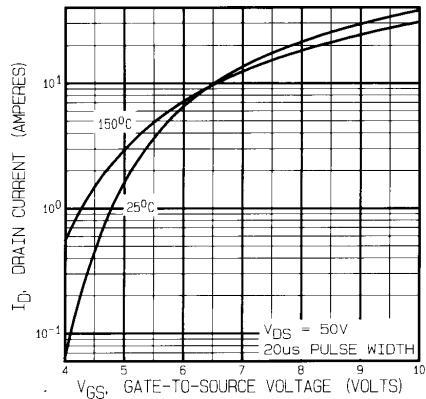
## JANTX2N6798, JANTXV2N6798 Device



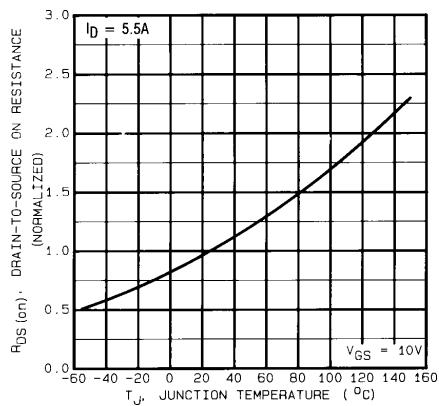
**Fig. 1 — Typical Output Characteristics**  
 $T_C = 25^\circ\text{C}$



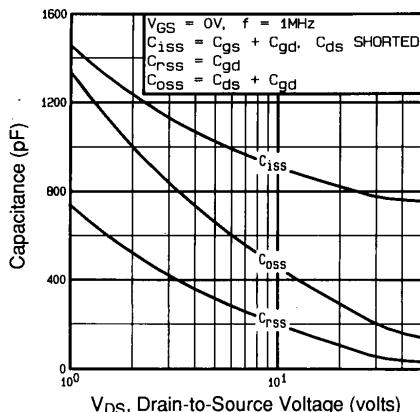
**Fig. 2 — Typical Output Characteristics**  
 $T_C = 150^\circ\text{C}$



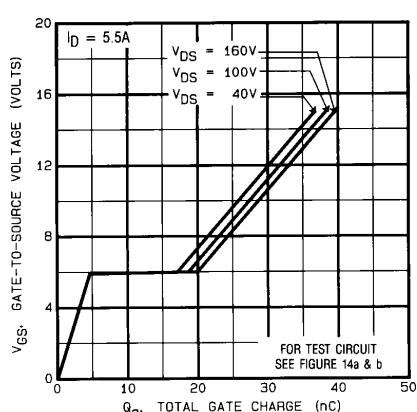
**Fig. 3 — Typical Transfer Characteristics**



**Fig. 4 — Normalized On-Resistance Vs. Temperature**



**Fig. 5 — Typical Capacitance Vs. Drain-to-Source Voltage**



**Fig. 6 — Typical Gate Charge Vs. Gate-to-Source Voltage**

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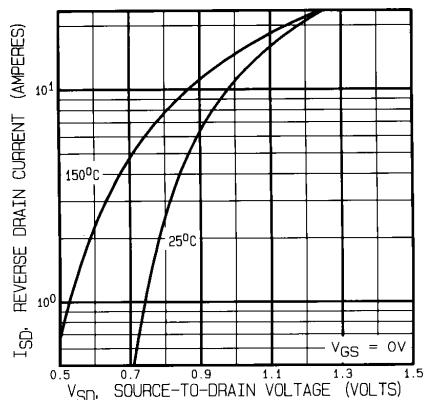


Fig. 7 — Typical Source-to-Drain Diode Forward Voltage

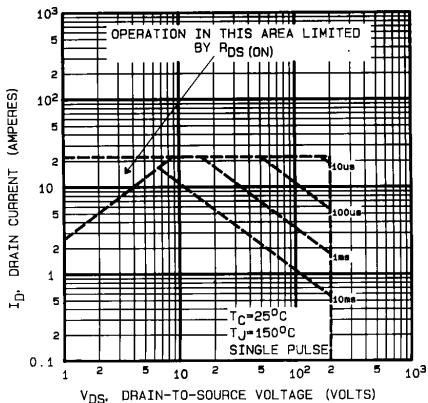


Fig. 8 — Maximum Safe Operating Area

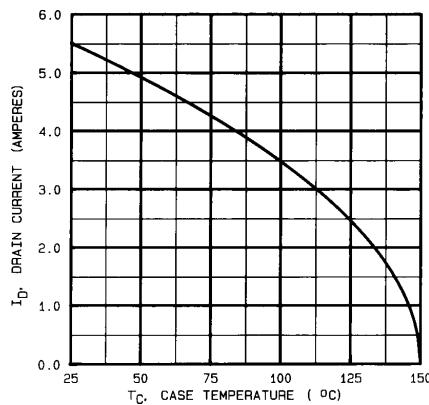


Fig. 9 — Maximum Drain Current Vs. Case Temperature

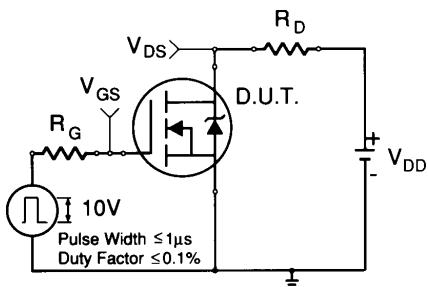


Fig. 10a — Switching Time Test Circuit

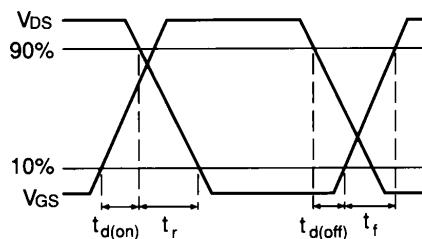


Fig. 10b — Switching Time Waveforms

## JANTX2N6798, JANTXV2N6798 Device

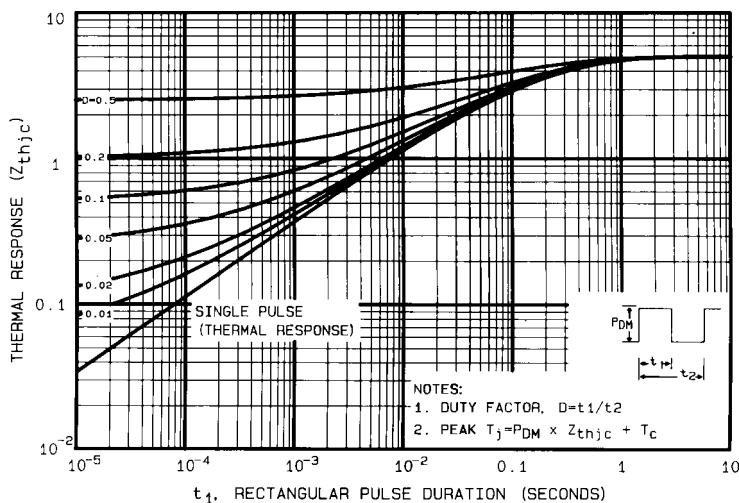


Fig. 11 — Maximum Effective Transient Thermal Impedance, Junction-to-Case Vs. Pulse Duration

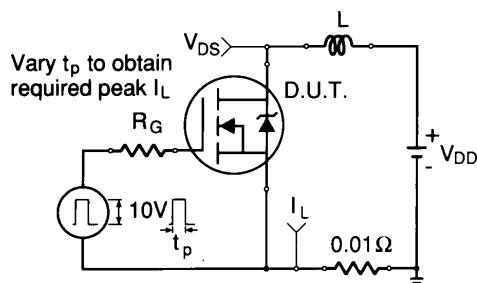


Fig. 12a — Unclamped Inductive Test Circuit

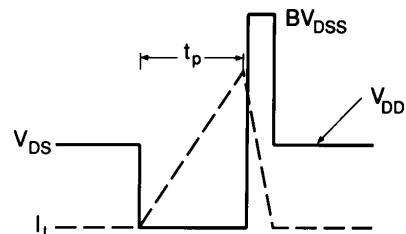


Fig. 12b — Unclamped Inductive Waveforms

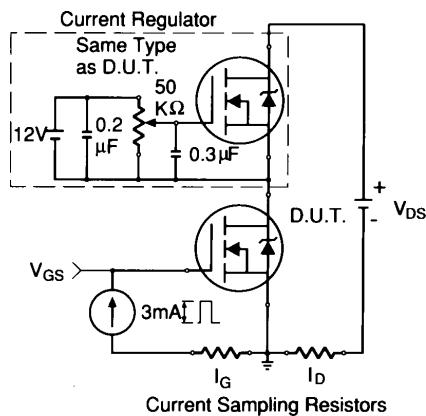


Fig. 13a — Gate Charge Test Circuit

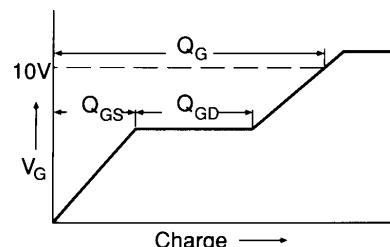
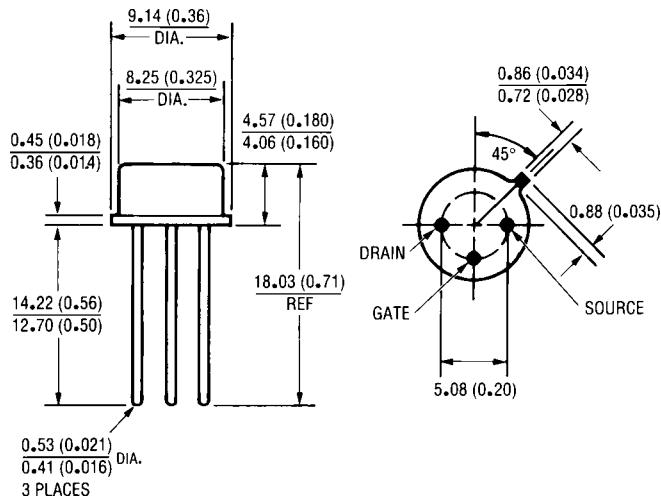
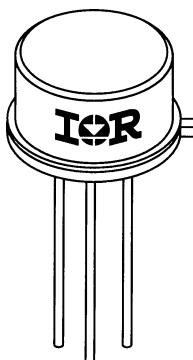


Fig. 13b — Basic Gate Charge Waveform

## JANTX2N6798, JANTXV2N6798 Device

- ① Repetitive Rating; Pulse width limited by maximum junction temperature.  
(see figure 11)
- ② @ V<sub>DD</sub> = 50V, Starting T<sub>J</sub> = 25°C,  
EAS = [0.5 \* L \* (I<sub>L</sub><sup>2</sup>) \* [BV<sub>DSS</sub>/(BV<sub>DSS</sub>-V<sub>DD</sub>)]  
Peak I<sub>L</sub> = 5.5A, V<sub>GS</sub> = 10V, 25 ≤ R<sub>G</sub> ≤ 200Ω
- ③ I<sub>SD</sub> ≤ 5.5A, di/dt ≤ 120A/μs,  
V<sub>DD</sub> ≤ BV<sub>DSS</sub>, T<sub>J</sub> ≤ 150°C
- ④ Pulse width ≤ 300 μs; Duty Cycle ≤ 2%
- ⑤ K/W = °C/W  
W/K = W/°C

## Case Outline and Dimensions — TO-205AF (Modified TO-39)



All dimensions are shown millimeters (inches)

International  
**IR** Rectifier

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