

# International IR Rectifier

## RADIATION HARDENED POWER MOSFET THRU-HOLE (MO-036AB)

PD - 93819

**IRHG9110**  
**100V, 4P-CHANNEL**  
**RAD-Hard™ HEXFET®**  
**MOSFET TECHNOLOGY**

### Product Summary

Part Number	Radiation Level	R <sub>Ds(on)</sub>	I <sub>D</sub>
IRHG9110	100K Rads (Si)	1.1Ω	-0.75A
IRHG93110	300K Rads (Si)	1.1Ω	-0.75A



International Rectifier's RAD-Hard™ HEXFET® MOSFET Technology provides high performance power MOSFETs for space applications. This technology has over a decade of proven performance and reliability in satellite applications. These devices have been characterized for both Total Dose and Single Event Effects (SEE). The combination of low R<sub>Ds(on)</sub> and low gate charge reduces the power losses in switching applications such as DC to DC converters and motor control. These devices retain all of the well established advantages of MOSFETs such as voltage control, fast switching, ease of paralleling and temperature stability of electrical parameters.

### Features:

- Single Event Effect (SEE) Hardened
- Low R<sub>Ds(on)</sub>
- Low Total Gate Charge
- Proton Tolerant
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Ceramic Package
- Light Weight

### Absolute Maximum Ratings

### Pre-Irradiation

	Parameter		Units
I <sub>D</sub> @ V <sub>GS</sub> = 12V, T <sub>C</sub> = 25°C	Continuous Drain Current	-0.75	A
I <sub>D</sub> @ V <sub>GS</sub> = 12V, T <sub>C</sub> = 100°C	Continuous Drain Current	-0.5	
I <sub>DM</sub>	Pulsed Drain Current ①	-3.0	
P <sub>D</sub> @ T <sub>C</sub> = 25°C	Max. Power Dissipation	1.4	W
	Linear Derating Factor	0.011	W/°C
V <sub>GS</sub>	Gate-to-Source Voltage	±20	V
E <sub>AS</sub>	Single Pulse Avalanche Energy	75 ②	mJ
I <sub>AR</sub>	Avalanche Current ①	-0.75	A
E <sub>AR</sub>	Repetitive Avalanche Energy ①	0.14	mJ
dV/dt	Peak Diode Recovery dV/dt	2.4 ③	V/ns
T <sub>J</sub>	Operating Junction	-55 to 150	°C
T <sub>TSG</sub>	Storage Temperature Range		
	Lead Temperature	300 (0.63in./1.6mm from case for 10s)	
	Weight	1.3 (Typical)	g

For footnotes refer to the last page

# IRHG9110

# Pre-Irradiation

## Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (Unless Otherwise Specified)

	Parameter	Min	Typ	Max	Units	Test Conditions
BVDSS	Drain-to-Source Breakdown Voltage	-100	—	—	V	$V_{GS} = 0V, I_D = -1.0\text{mA}$
$\Delta BVDSS/\Delta T_J$	Temperature Coefficient of Breakdown Voltage	—	-0.11	—	$^\circ\text{C}$	Reference to $25^\circ\text{C}, I_D = -1.0\text{mA}$
RDS(on)	Static Drain-to-Source On-State Resistance	—	—	1.2	$\Omega$	$V_{GS} = -12V, I_D = -0.75\text{A}$ ④
	Resistance	—	—	1.1		$V_{GS} = -12V, I_D = -0.5\text{A}$
VGS(th)	Gate Threshold Voltage	-2.0	—	-4.0	V	$V_{DS} = V_{GS}, I_D = -1.0\text{mA}$
gfs	Forward Transconductance	0.6	—	—	S ( $\text{O}$ )	$V_{DS} > -15V, I_{DS} = -0.5\text{A}$ ④
IDSS	Zero Gate Voltage Drain Current	—	—	-25	$\mu\text{A}$	$V_{DS} = -80V, V_{GS} = 0V$
		—	—	-250		$V_{DS} = -80V,$ $V_{GS} = 0V, T_J = 125^\circ\text{C}$
IGSS	Gate-to-Source Leakage Forward	—	—	-100	nA	$V_{GS} = -20V$
IGSS	Gate-to-Source Leakage Reverse	—	—	100		$V_{GS} = 20V$
Qg	Total Gate Charge	—	—	19	nC	$V_{GS} = -12V, I_D = -0.75\text{A},$ $V_{DS} = -50V$
Qgs	Gate-to-Source Charge	—	—	4.0		
Qgd	Gate-to-Drain ('Miller') Charge	—	—	4.3		
td(on)	Turn-On Delay Time	—	—	22	ns	$V_{DD} = -50V, I_D = -0.75\text{A},$ $R_G = 24\Omega$
tr	Rise Time	—	—	19		
td(off)	Turn-Off Delay Time	—	—	66		
tf	Fall Time	—	—	51		
LS + LD	Total Inductance	—	10	—	nH	Measured from Drain lead (6mm /0.25in. from package) to Source lead (6mm /0.25in. from package) with Source wires internally bonded from Source Pin to Drain Pad
Ciss	Input Capacitance	—	335	—	pF	$V_{GS} = 0V, V_{DS} = 25V$ $f = 1.0\text{MHz}$
Coss	Output Capacitance	—	100	—		
Crss	Reverse Transfer Capacitance	—	22	—		

## Source-Drain Diode Ratings and Characteristics

	Parameter	Min	Typ	Max	Units	Test Conditions
IS	Continuous Source Current (Body Diode)	—	—	-0.75	A	
ISM	Pulse Source Current (Body Diode) ①	—	—	-3.0		
VSD	Diode Forward Voltage	—	—	-2.5	V	$T_j = 25^\circ\text{C}, I_S = -0.75\text{A}, V_{GS} = 0V$ ④
trr	Reverse Recovery Time	—	—	90	nS	$T_j = 25^\circ\text{C}, I_F = -0.75\text{A}, dI/dt \geq -100\text{A}/\mu\text{s}$
QRR	Reverse Recovery Charge	—	—	257	nC	$V_{DD} \leq -25V$ ④
ton	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by LS + LD.				

## Thermal Resistance

	Parameter	Min	Typ	Max	Units	Test Conditions
RthJC	Junction-to-Case	—	—	17	$^\circ\text{C/W}$	
RthJA	Junction-to-Ambient	—	—	90		Typical socket mount

For footnotes refer to the last page

## Radiation Characteristics

**IRHG9110**

International Rectifier Radiation Hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at International Rectifier is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 5 and 6) using the TO-39 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

**Table 1. Electrical Characteristics @  $T_j = 25^\circ\text{C}$ , Post Total Dose Irradiation (5)(6)**

	Parameter	100K Rads(Si) <sup>1</sup>		300K Rads (Si) <sup>2</sup>		Units	Test Conditions
		Min	Max	Min	Max		
$\text{BV}_{\text{DSS}}$	Drain-to-Source Breakdown Voltage	-100	—	-100	—	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = -1.0\text{mA}$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage (4)	-2.0	-4.0	-2.0	-5.0		$\text{V}_{\text{GS}} = \text{V}_{\text{DS}}, \text{I}_D = -1.0\text{mA}$
$\text{I}_{\text{GSS}}$	Gate-to-Source Leakage Forward	—	-100	—	-100	nA	$\text{V}_{\text{GS}} = -20\text{V}$
$\text{I}_{\text{GSS}}$	Gate-to-Source Leakage Reverse	—	100	—	100		$\text{V}_{\text{GS}} = 20\text{V}$
$\text{I}_{\text{DSS}}$	Zero Gate Voltage Drain Current	—	-25	—	-25	$\mu\text{A}$	$\text{V}_{\text{DS}} = -80\text{V}, \text{V}_{\text{GS}} = 0\text{V}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source (4) On-State Resistance (TO-39)	—	1.06	—	1.06	$\Omega$	$\text{V}_{\text{GS}} = -12\text{V}, \text{I}_D = -0.5\text{A}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source (4) On-State Resistance (MO-036AB)	—	1.1	—	1.1	$\Omega$	$\text{V}_{\text{GS}} = -12\text{V}, \text{I}_D = -0.5\text{A}$
$\text{V}_{\text{SD}}$	Diode Forward Voltage (4)	—	-2.5	—	-2.5	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_S = -0.75\text{A}$

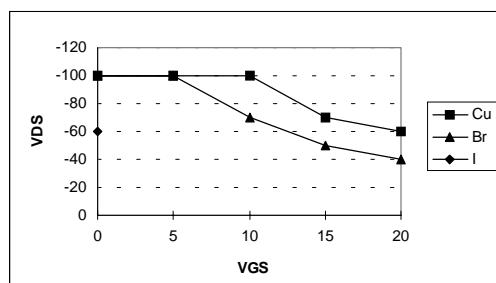
1. Part number IRHG9110

2. Part number IRHG93110

International Rectifier radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. a and Table 2.

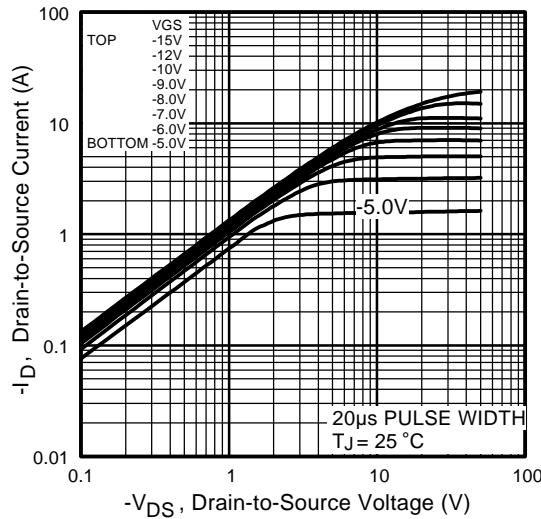
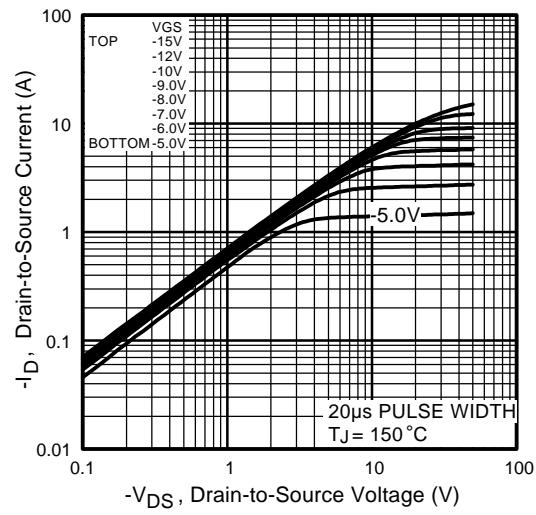
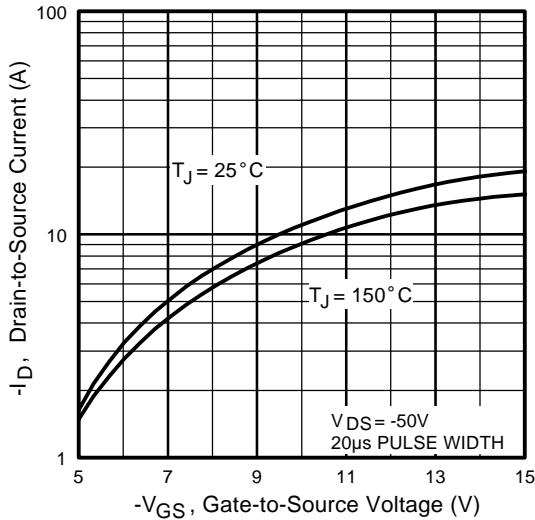
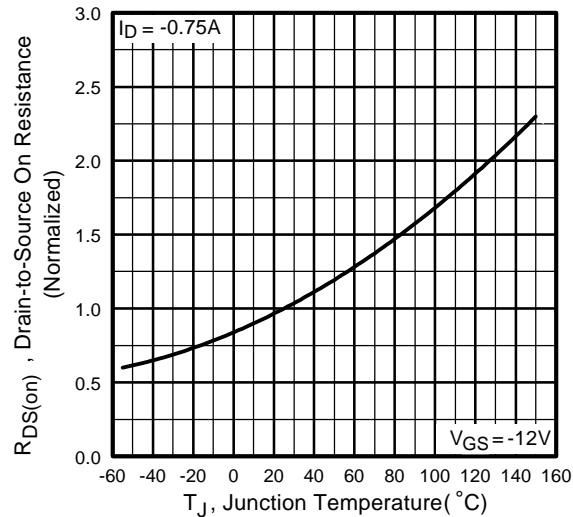
**Table 2. Single Event Effect Safe Operating Area**

Ion	LET MeV/(mg/cm <sup>2</sup> )	Energy (MeV)	Range ( $\mu\text{m}$ )	$\text{V}_{\text{DS}} (\text{V})$				
				@ $\text{V}_{\text{GS}} = 0\text{V}$	@ $\text{V}_{\text{GS}} = 5\text{V}$	@ $\text{V}_{\text{GS}} = 10\text{V}$	@ $\text{V}_{\text{GS}} = 15\text{V}$	@ $\text{V}_{\text{GS}} = 20\text{V}$
Cu	28.0	285	43.0	-100	-100	-100	-70	-60
Br	36.8	305	39.0	-100	-100	-70	-50	-40
I	59.8	343	32.6	-60	—	—	—	—



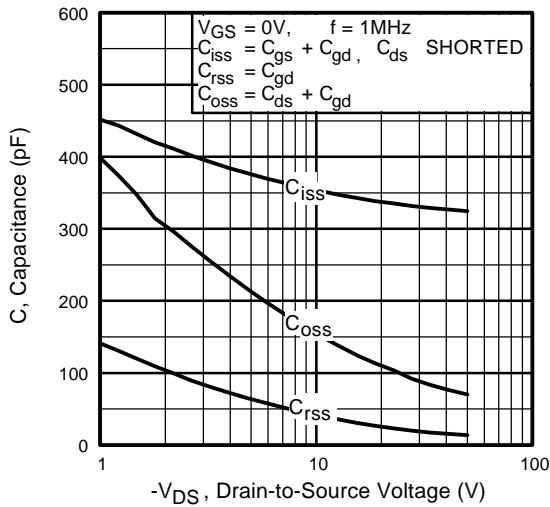
**Fig a. Single Event Effect, Safe Operating Area**

For footnotes refer to the last page

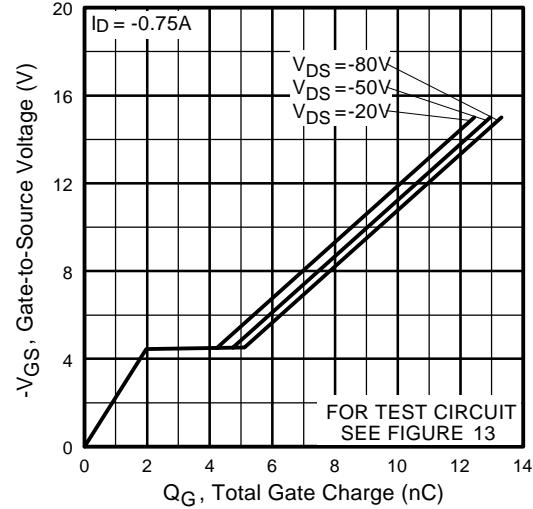
**IRHG9110****Pre-Irradiation****Fig 1.** Typical Output Characteristics**Fig 2.** Typical Output Characteristics**Fig 3.** Typical Transfer Characteristics**Fig 4.** Normalized On-Resistance Vs. Temperature

## Pre-Irradiation

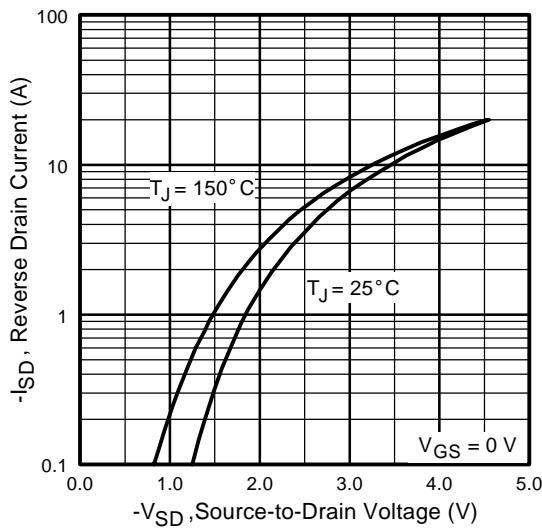
**IRHG9110**



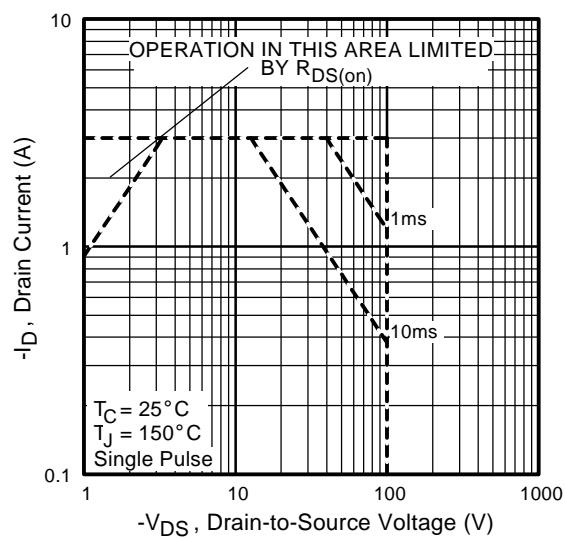
**Fig 5.** Typical Capacitance Vs.  
Drain-to-Source Voltage



**Fig 6.** Typical Gate Charge Vs.  
Gate-to-Source Voltage



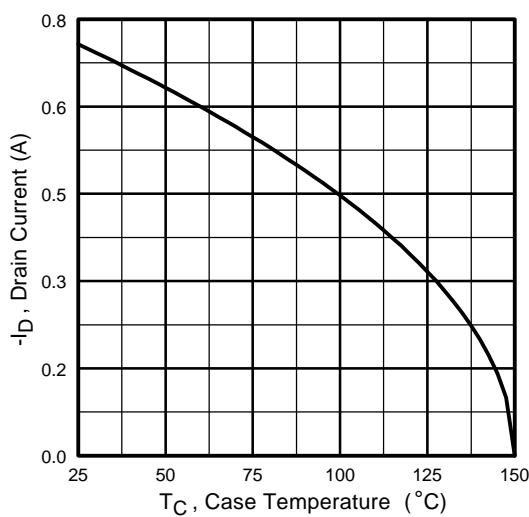
**Fig 7.** Typical Source-Drain Diode  
Forward Voltage



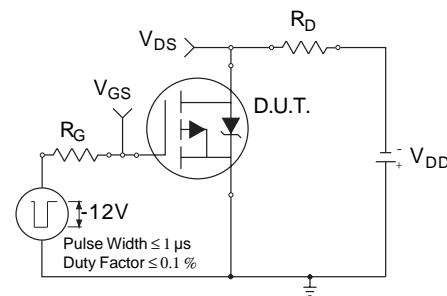
**Fig 8.** Maximum Safe Operating Area

## IRHG9110

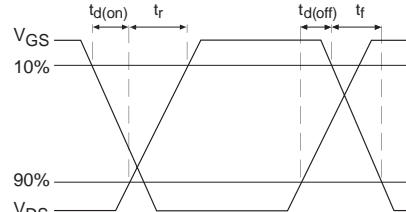
## Pre-Irradiation



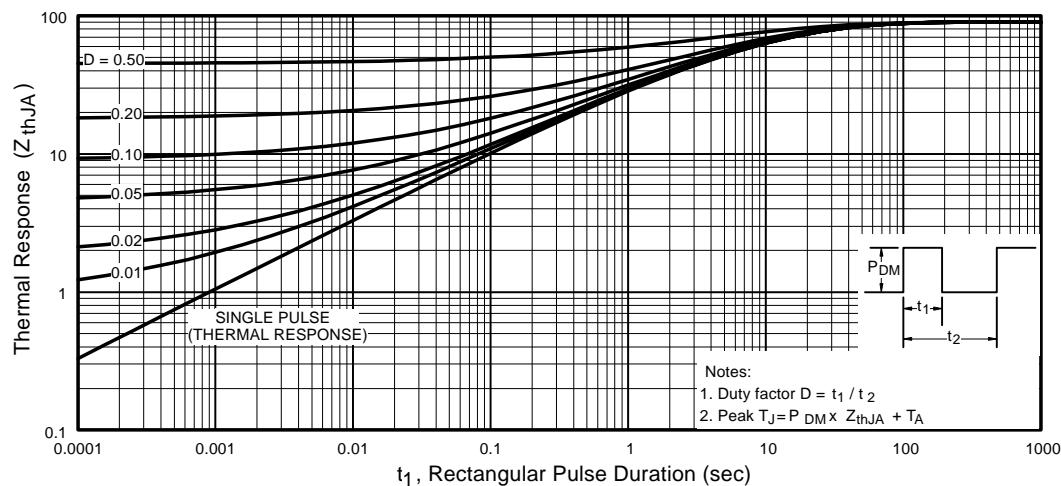
**Fig 9.** Maximum Drain Current Vs.  
Case Temperature



**Fig 10a.** Switching Time Test Circuit



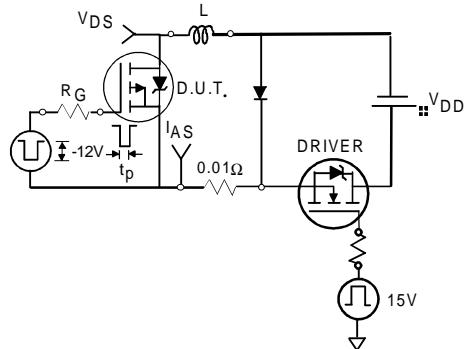
**Fig 10b.** Switching Time Waveforms



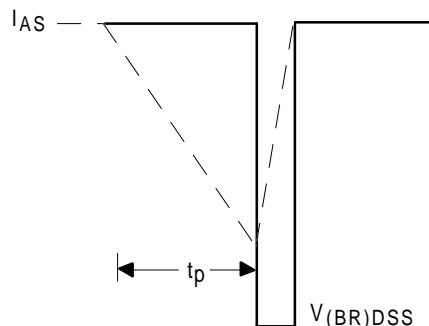
**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

## Pre-Irradiation

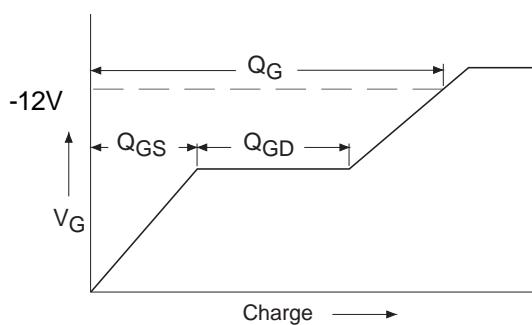
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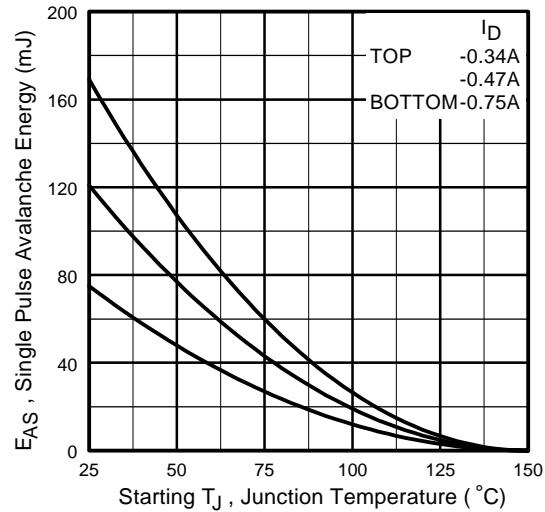
**Fig 12a.** Unclamped Inductive Test Circuit



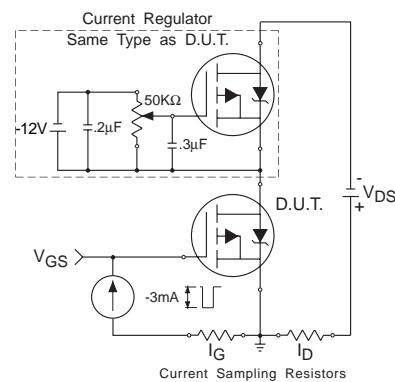
**Fig 12b.** Unclamped Inductive Waveforms



**Fig 13a.** Basic Gate Charge Waveform



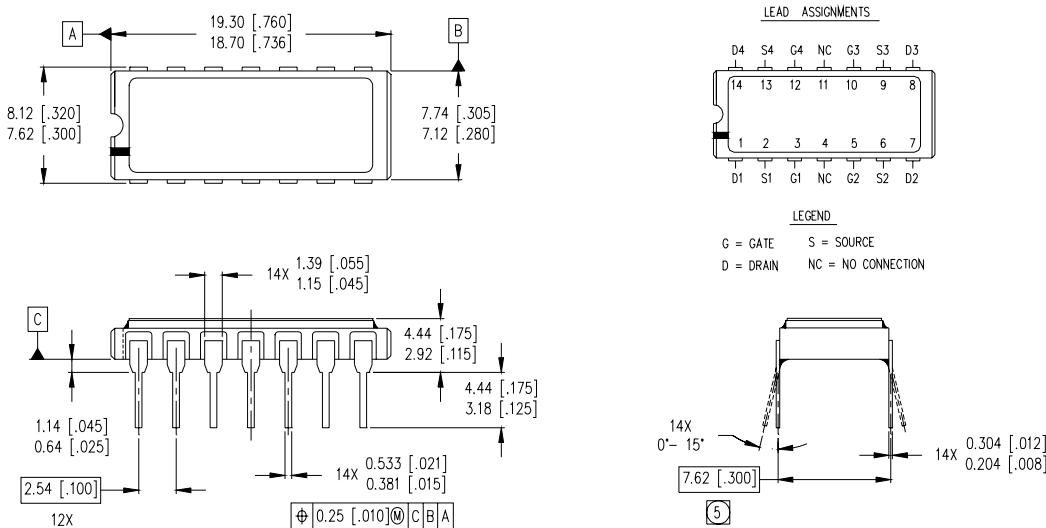
**Fig 12c.** Maximum Avalanche Energy Vs. Drain Current



**Fig 13b.** Gate Charge Test Circuit

**Footnotes:**

- ① Repetitive Rating; Pulse width limited by maximum junction temperature.
- ②  $V_{DD} = -25V$ , starting  $T_J = 25^\circ C$ ,  $L = 267mH$ , Peak  $I_L = -0.75A$ ,  $V_{GS} = -12V$
- ③  $I_{SD} \leq -0.75A$ ,  $dI/dt \leq -132A/\mu s$ ,  $V_{DD} \leq -100V$ ,  $T_J \leq 150^\circ C$
- ④ Pulse width  $\leq 300 \mu s$ ; Duty Cycle  $\leq 2\%$
- ⑤ **Total Dose Irradiation with  $V_{GS}$  Bias.**  
12 volt  $V_{GS}$  applied and  $V_{DS} = 0$  during irradiation per MIL-STD-750, method 1019, condition A
- ⑥ **Total Dose Irradiation with  $V_{DS}$  Bias.**  
80 volt  $V_{DS}$  applied and  $V_{GS} = 0$  during irradiation per MIL-STD-750, method 1019, condition A

**Case Outline and Dimensions — MO-036AB**

International  
**IR** Rectifier

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*Data and specifications subject to change without notice. 3/00*