

International  
**IR** Rectifier  
**RADIATION HARDENED**  
**POWER MOSFET**  
**SURFACE MOUNT (LCC-18)**

PD - 90881B

**IRHE9130**  
**100V, P-CHANNEL**  
**REF: MIL-PRF-19500/630**  
**RAD-Hard™ HEXFET®**  
**MOSFETTECHNOLOGY**

**Product Summary**

Part Number	Radiation Level	R <sub>d(on)</sub>	I <sub>D</sub>	QPL Part Number
IRHE9130	100K Rads (Si)	0.30Ω	-6.5A	JANSR2N7389U
IRHE93130	300K Rads (Si)	0.30Ω	-6.5A	JANSF2N7389U

International Rectifier's RAD-Hard™ HEXFET® MOSFET technology provides high performance power MOSFETs for space applications. This technology has over a decade of proven performance and reliability in satellite applications. These devices have been characterized for both Total Dose and Single Event Effects (SEE). The combination of low Rdson and low gate charge reduces the power losses in switching applications such as DC to DC converters and motor control. These devices retain all of the well established advantages of MOSFETs such as voltage control, fast switching, ease of parallelizing and temperature stability of electrical parameters.



LCC - 18

**Features:**

- Single Event Effect (SEE) Hardened
- Low R<sub>d(on)</sub>
- Low Total Gate Charge
- Proton Tolerant
- Simple Drive Requirements
- Ease of Parallelizing
- Hermetically Sealed
- Ceramic Package
- Surface Mount
- Light Weight

**Absolute Maximum Ratings**

**Pre-Irradiation**

	Parameter	Units	
I <sub>D</sub> @ V <sub>GS</sub> = 12V, T <sub>C</sub> = 25°C	Continuous Drain Current	A	-6.5
I <sub>D</sub> @ V <sub>GS</sub> = 12V, T <sub>C</sub> = 100°C	Continuous Drain Current		-4.1
I <sub>DM</sub>	Pulsed Drain Current ①		-26
P <sub>D</sub> @ T <sub>C</sub> = 25°C	Max. Power Dissipation	W	25
	Linear Derating Factor	W/°C	0.2
V <sub>GS</sub>	Gate-to-Source Voltage	V	±20
EAS	Single Pulse Avalanche Energy ②	mJ	165
I <sub>AR</sub>	Avalanche Current ①	A	-6.5
E <sub>AR</sub>	Repetitive Avalanche Energy ①	mJ	2.5
dv/dt	Peak Diode Recovery dv/dt ③	V/ns	-22
T <sub>J</sub>	Operating Junction	°C	-55 to 150
T <sub>STG</sub>	Storage Temperature Range		
	Pckg. Mounting Surface Temp.		300 ( for 5s)
	Weight	g	0.42 (Typical)

For footnotes refer to the last page

**Electrical Characteristics @  $T_J = 25^\circ\text{C}$  (Unless Otherwise Specified)**

	Parameter	Min	Typ	Max	Units	Test Conditions
BVDSS	Drain-to-Source Breakdown Voltage	-100	—	—	V	$V_{GS} = 0\text{ V}$ , $I_D = -1.0\text{mA}$
$\Delta BVDSS/\Delta T_J$	Temperature Coefficient of Breakdown Voltage	—	-0.112	—	$\text{V}/^\circ\text{C}$	Reference to $25^\circ\text{C}$ , $I_D = -1.0\text{mA}$
$R_{DS(on)}$	Static Drain-to-Source On-State Resistance	—	—	0.30	$\Omega$	$V_{GS} = -12\text{V}$ , $I_D = -4.1\text{A}$ ④
		—	—	0.35		$V_{GS} = -12\text{V}$ , $I_D = -6.5\text{A}$ ④
$V_{GS(\text{th})}$	Gate Threshold Voltage	-2.0	—	-4.0	V	$V_{DS} = V_{GS}$ , $I_D = -1.0\text{mA}$
$g_{fs}$	Forward Transconductance	2.5	—	—	S ( $\text{S}$ )	$V_{DS} > -15\text{V}$ , $I_{DS} = -4.1\text{A}$ ④
$I_{DSS}$	Zero Gate Voltage Drain Current	—	—	-25	$\mu\text{A}$	$V_{DS} = -80\text{V}$ , $V_{GS} = 0\text{V}$
		—	—	-250		$V_{DS} = -80\text{V}$ $V_{GS} = 0\text{V}$ , $T_J = 125^\circ\text{C}$
$I_{GSS}$	Gate-to-Source Leakage Forward	—	—	-100	$\text{nA}$	$V_{GS} = -20\text{V}$
$I_{GSS}$	Gate-to-Source Leakage Reverse	—	—	100		$V_{GS} = 20\text{V}$
$Q_g$	Total Gate Charge	—	—	45	$\text{nC}$	$V_{GS} = -12\text{V}$ , $I_D = -6.5\text{A}$
$Q_{gs}$	Gate-to-Source Charge	—	—	10		$V_{DS} = -50\text{V}$
$Q_{gd}$	Gate-to-Drain ('Miller') Charge	—	—	25		
$t_{d(on)}$	Turn-On Delay Time	—	—	30	$\text{ns}$	$V_{DD} = -50\text{V}$ , $I_D = -6.5\text{A}$ , $R_G = 7.5\Omega$
$t_r$	Rise Time	—	—	50		
$t_{d(off)}$	Turn-Off Delay Time	—	—	70		
$t_f$	Fall Time	—	—	70		
$L_S + L_D$	Total Inductance	—	6.1	—	nH	Measured from the center of drain pad to center of source pad
$C_{iss}$	Input Capacitance	—	1200	—	$\text{pF}$	$V_{GS} = 0\text{V}$ , $V_{DS} = -25\text{V}$ $f = 1.0\text{MHz}$
$C_{oss}$	Output Capacitance	—	290	—		
$C_{rss}$	Reverse Transfer Capacitance	—	76	—		

**Source-Drain Diode Ratings and Characteristics**

	Parameter	Min	Typ	Max	Units	Test Conditions
$I_S$	Continuous Source Current (Body Diode)	—	—	-6.5	A	$T_J = 25^\circ\text{C}$ , $I_S = -6.5\text{A}$ , $V_{GS} = 0\text{V}$ ④
$I_{SM}$	Pulse Source Current (Body Diode) ①	—	—	-26		
$V_{SD}$	Diode Forward Voltage	—	—	-3.0	V	$T_J = 25^\circ\text{C}$ , $I_F = -6.5\text{A}$ , $dI/dt \geq -100\text{A}/\mu\text{s}$
$t_{rr}$	Reverse Recovery Time	—	—	250	nS	$V_{DD} \leq -25\text{V}$ ④
$Q_{RR}$	Reverse Recovery Charge	—	—	0.74	$\mu\text{C}$	
$t_{on}$	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by $L_S + L_D$ .				

**Thermal Resistance**

	Parameter	Min	Typ	Max	Units	Test Conditions
$R_{thJC}$	Junction-to-Case	—	—	5.0	$^\circ\text{C}/\text{W}$	Solder to a copper clad PC Board
$R_{thJPCB}$	Junction-to-PC Board	—	19	—		

Note: Corresponding Spice and Saber models are available on the G&S Website.

For footnotes refer to the last page

## Radiation Characteristics

IRHE9130

International Rectifier Radiation Hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at International Rectifier is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 5 and 6) using the TO-3 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

**Table 1. Electrical Characteristics @  $T_j = 25^\circ\text{C}$ , Post Total Dose Irradiation<sup>⑤⑥</sup>**

	Parameter	100K Rads(Si) <sup>1</sup>		300K Rads (Si) <sup>2</sup>		Units	Test Conditions
		Min	Max	Min	Max		
$\text{BV}_{\text{DSS}}$	Drain-to-Source Breakdown Voltage	-100	—	-100	—	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = -1.0\text{mA}$
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage <sup>④</sup>	-2.0	-4.0	-2.0	-5.0		$\text{V}_{\text{GS}} = \text{V}_{\text{DS}}, \text{I}_D = -1.0\text{mA}$
$\text{I}_{\text{GSS}}$	Gate-to-Source Leakage Forward	—	-100	—	-100	nA	$\text{V}_{\text{GS}} = -20\text{V}$
$\text{I}_{\text{GSS}}$	Gate-to-Source Leakage Reverse	—	100	—	100		$\text{V}_{\text{GS}} = 20\text{ V}$
$\text{I}_{\text{DSS}}$	Zero Gate Voltage Drain Current	—	-25	—	-25	$\mu\text{A}$	$\text{V}_{\text{DS}} = -80\text{V}, \text{V}_{\text{GS}} = 0\text{V}$
$\text{R}_{\text{DS}(\text{on})}$	Static Drain-to-Source <sup>④</sup> On-State Resistance (TO-3)	—	0.259	—	0.259	$\Omega$	$\text{V}_{\text{GS}} = -12\text{V}, \text{I}_D = -4.1\text{A}$
$\text{R}_{\text{DS}(\text{on})}$	Static Drain-to-Source <sup>④</sup> On-State Resistance (LCC-18)	—	0.30	—	0.30	$\Omega$	$\text{V}_{\text{GS}} = -12\text{V}, \text{I}_D = -4.1\text{A}$
$\text{V}_{\text{SD}}$	Diode Forward Voltage <sup>④</sup>	—	-3.0	—	-3.0	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_S = -6.5\text{A}$

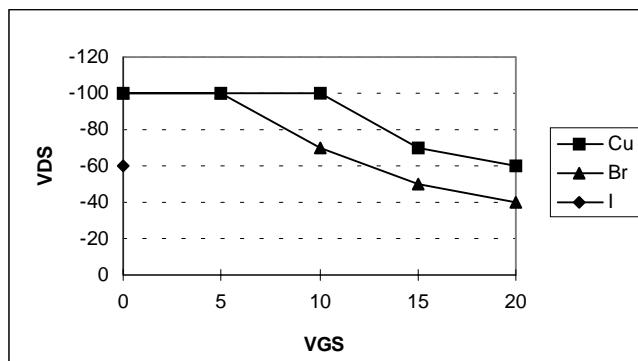
1. Part number IRHE9130 (JANSR2N7389U)

2. Part number IRHE9130 (JANSF2N7389U)

International Rectifier radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. a and Table 2.

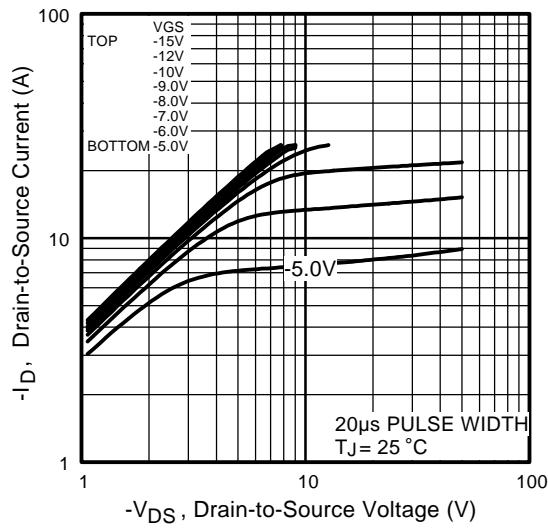
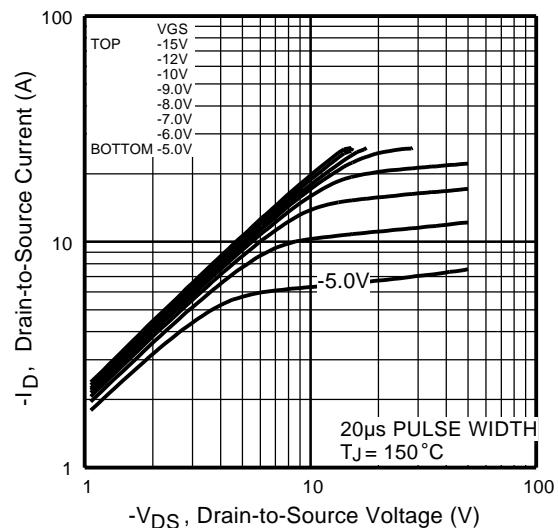
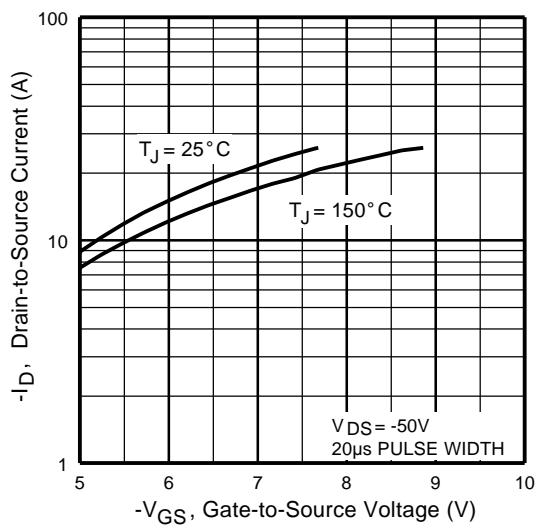
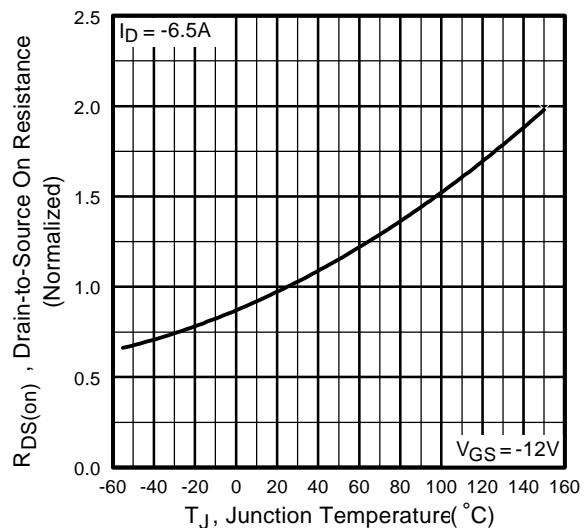
**Table 2. Single Event Effect Safe Operating Area**

Ion	LET MeV/(mg/cm <sup>2</sup> )	Energy (MeV)	Range ( $\mu\text{m}$ )	V <sub>DS</sub> (v)				
				@ $\text{V}_{\text{GS}}=0\text{V}$	@ $\text{V}_{\text{GS}}=-5\text{V}$	@ $\text{V}_{\text{GS}}=-10\text{V}$	@ $\text{V}_{\text{GS}}=-15\text{V}$	@ $\text{V}_{\text{GS}}=-20\text{V}$
Cu	28	285	43	-100	-100	-100	-70	-60
Br	36.8	305	39	-100	-100	-70	-50	-40
I	59.8	343	32.6	-60	—	—	—	—



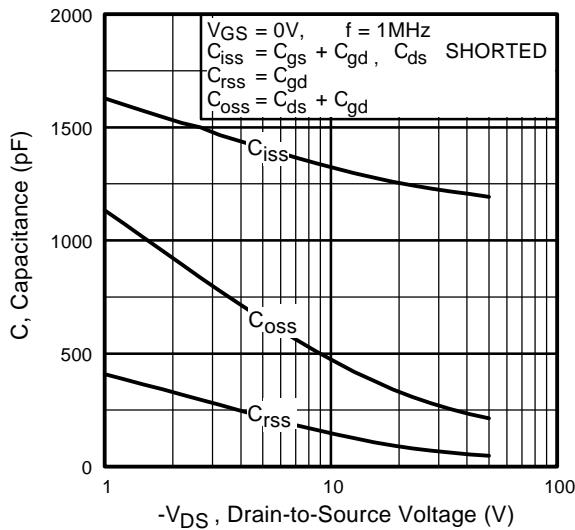
**Fig a.** Single Event Effect, Safe Operating Area

For footnotes refer to the last page

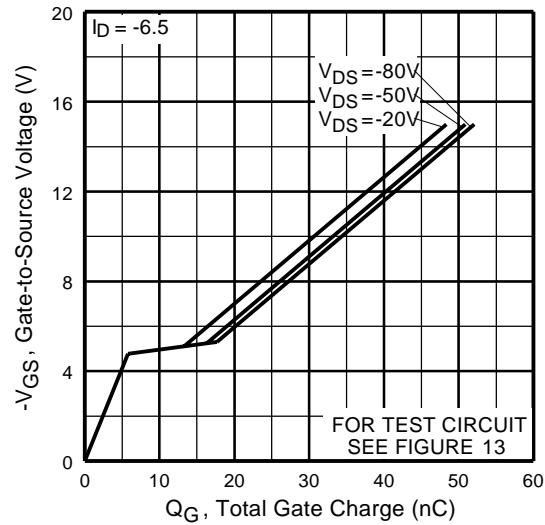
**IRHE9130****Pre-Irradiation****Fig 1.** Typical Output Characteristics**Fig 2.** Typical Output Characteristics**Fig 3.** Typical Transfer Characteristics**Fig 4.** Normalized On-Resistance Vs. Temperature

## Pre-Irradiation

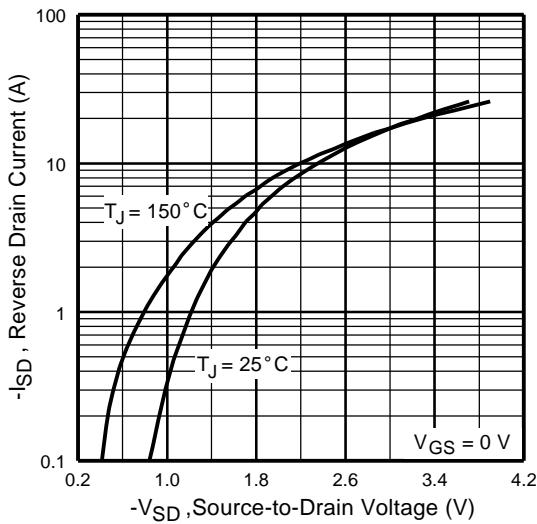
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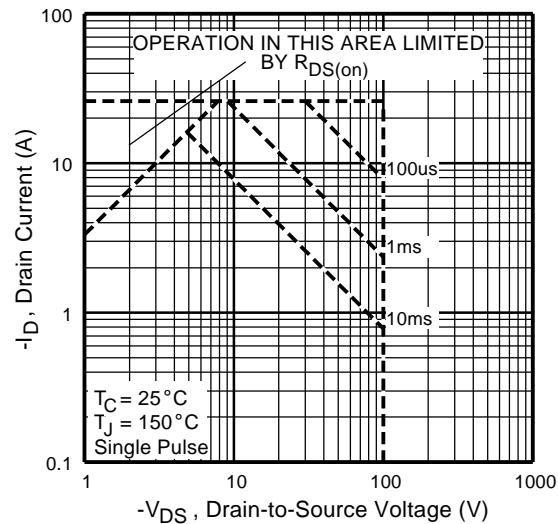
**Fig 5.** Typical Capacitance Vs.  
Drain-to-Source Voltage



**Fig 6.** Typical Gate Charge Vs.  
Gate-to-Source Voltage



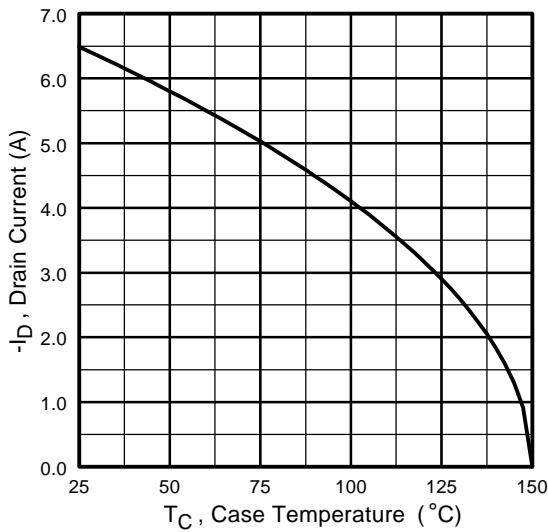
**Fig 7.** Typical Source-Drain Diode  
Forward Voltage



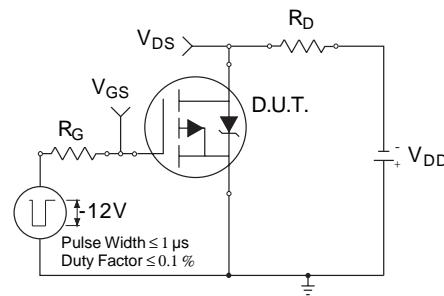
**Fig 8.** Maximum Safe Operating Area

## IRHE9130

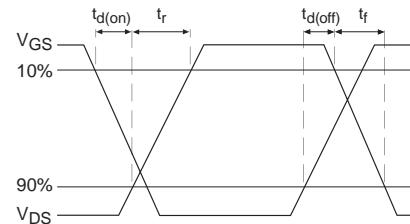
## Pre-Irradiation



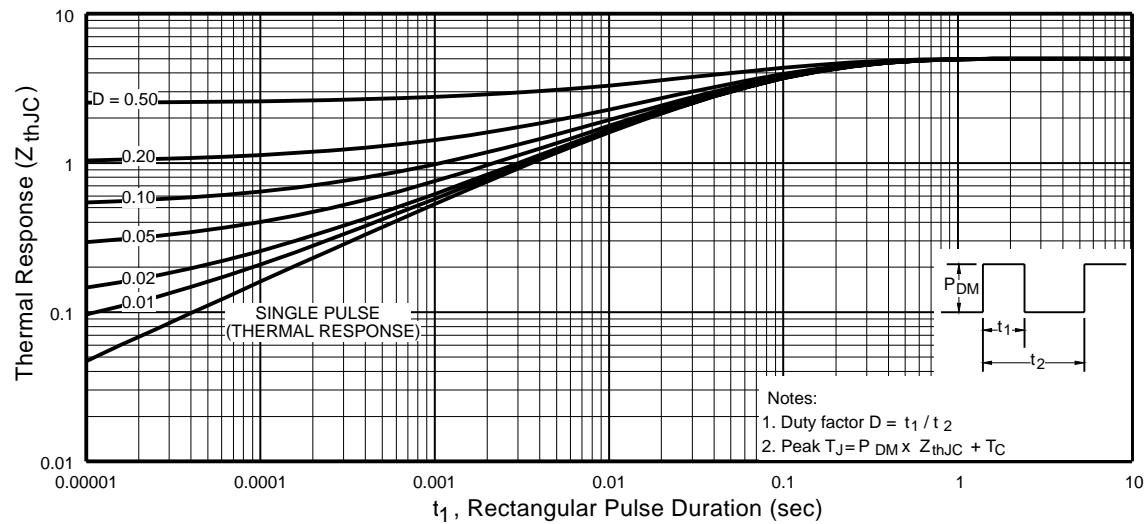
**Fig 9.** Maximum Drain Current Vs.  
Case Temperature



**Fig 10a.** Switching Time Test Circuit



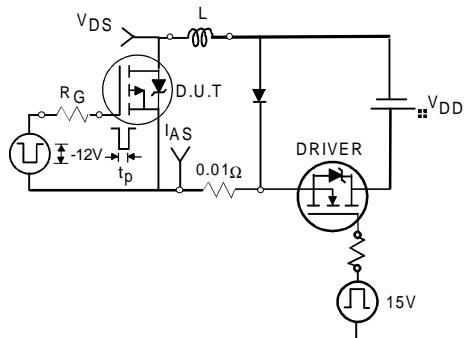
**Fig 10b.** Switching Time Waveforms



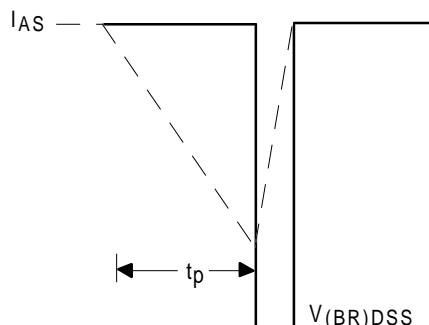
**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Case

## Pre-Irradiation

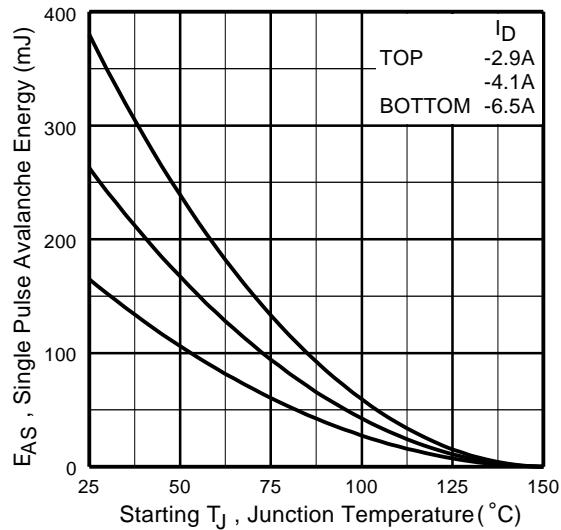
IRHE9130



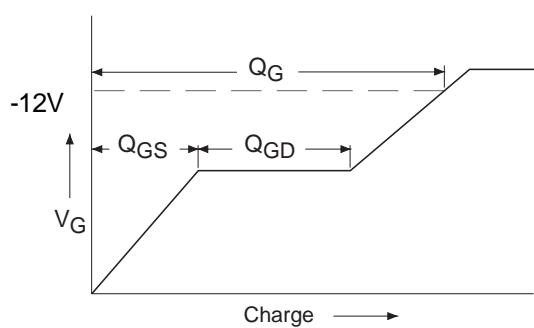
**Fig 12a.** Unclamped Inductive Test Circuit



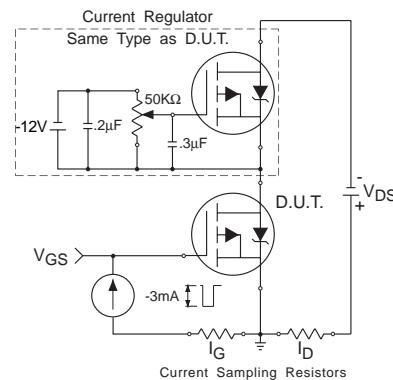
**Fig 12b.** Unclamped Inductive Waveforms



**Fig 12c.** Maximum Avalanche Energy Vs. Drain Current



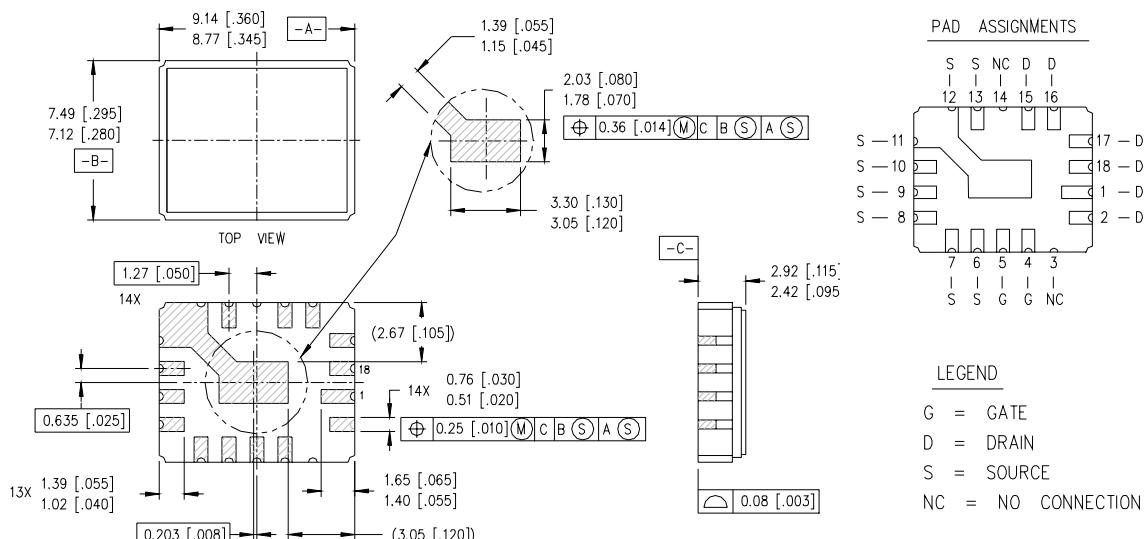
**Fig 13a.** Basic Gate Charge Waveform



**Fig 13b.** Gate Charge Test Circuit

**Footnotes:**

- ① Repetitive Rating; Pulse width limited by maximum junction temperature.
- ② V<sub>DD</sub> = -25V, starting T<sub>J</sub> = 25°C, L = 7.8mH  
Peak I<sub>L</sub> = -6.5A, V<sub>GS</sub> = -12V
- ③ ISD ≤ -6.5A, di/dt ≤ -430A/μs,  
V<sub>DD</sub> ≤ -100V, T<sub>J</sub> ≤ 150°C
- ④ Pulse width ≤ 300 μs; Duty Cycle ≤ 2%
- ⑤ **Total Dose Irradiation with V<sub>GS</sub> Bias.**  
12 volt V<sub>GS</sub> applied and V<sub>DS</sub> = 0 during irradiation per MIL-STD-750, method 1019, condition A.
- ⑥ **Total Dose Irradiation with V<sub>DS</sub> Bias.**  
80 volt V<sub>DS</sub> applied and V<sub>GS</sub> = 0 during irradiation per MIL-STD-750, method 1019, condition A.

**Case Outline and Dimensions — LCC-18**

International  
**IR** Rectifier

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*Data and specifications subject to change without notice. 3/00*