

HFA08TA60C

Ultrafast, Soft Recovery Diode

Features

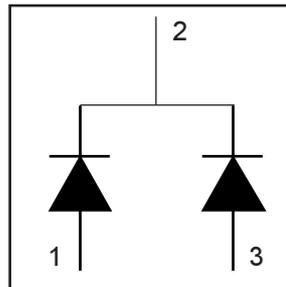
- Ultrafast Recovery
- Ultrasoft Recovery
- Very Low I_{RRM}
- Very Low Q_{rr}
- Guaranteed Avalanche
- Specified at Operating Conditions

Benefits

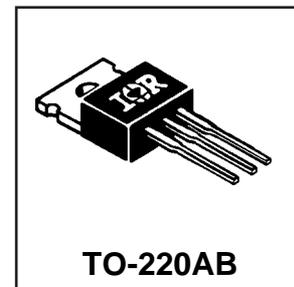
- Reduced RFI and EMI
- Reduced Power Loss in Diode and Switching Transistor
- Higher Frequency Operation
- Reduced Snubbing
- Reduced Parts Count

Description

International Rectifier's HFA08TA60C is a state of the art center tap ultra fast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 600 volts and 4 amps per Leg continuous current, the HFA04TB60 is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultra fast recovery time, the HEXFRED product line features extremely low values of peak recovery current (I_{RRM}) and does not exhibit any tendency to "snap-off" during the t_b portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED HFA08TA60C is ideally suited for applications in power supplies and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.



$V_R = 600V$
$V_F = 1.8V$
$Q_{rr}^* = 40nC$
$di_{(rec)M}/dt^* = 280A/\mu s$ * 125°C



Absolute Maximum Ratings

	Parameter	Max.	Units
V_R	Cathode-to-Anode Voltage	600	V
$I_F @ T_C = 25^\circ C$	Continuous Forward Current		A
$I_F @ T_C = 100^\circ C$	Continuous Forward Current	4.0	
I_{FSM}	Single Pulse Forward Current	25	
I_{FRM}	Maximum Repetitive Forward Current	16	
$I_{AS} \textcircled{1}$	Single Pulse Avalanche Current	0.5	
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation	25	W
$P_D @ T_C = 100^\circ C$	Maximum Power Dissipation	10	
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to +150	C

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Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Test Conditions	
V _{BR}	Cathode Anode Breakdown Voltage	600	—	—	V	I _R = 100μA	
V _{FM}	Max Forward Voltage		1.5	1.8	V	I _F = 4.0A	
		—	1.8	2.2		I _F = 8.0A	See Fig. 1
			1.4	1.7		I _F = 4.0A, T _J = 125°C	
I _{RM}	Max Reverse Leakage Current		0.17	3.0	μA	V _R = V _R Rated	
			44	300		T _J = 125°C, V _R = 0.8 x V _R Rated	See Fig. 2
C _T	Junction Capacitance	—	4.0	8.0	pF	V _R = 200V	
L _S	Series Inductance	—	8.0	—	nH	Measured lead to lead 5mm from package body	

Dynamic Recovery Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Test Conditions
t _{rr}	Reverse Recovery Time	—	17	—	ns	I _F = 1.0A, di _F /dt = 200A/μs, V _R = 30V
t _{rr1}	See Fig. 5, 6 & 16	—	28	42		T _J = 25°C
t _{rr2}		—	38	57		T _J = 125°C
I _{RRM1}	Peak Recovery Current	—	2.9	5.2	A	I _F = 4.0A
I _{RRM2}	See Fig. 7 & 8	—	3.7	6.7		
Q _{rr1}	Reverse Recovery Charge	—	40	60	nC	V _R = 200V
Q _{rr2}	See Fig. 9 & 10	—	70	105		
di _{(rec)M} /dt1	Peak Rate of Fall of Recovery Current	—	280	—	A/μs	di _F /dt = 200A/μs
di _{(rec)M} /dt2	During t _b See Fig. 11 & 12	—	235	—		

Thermal - Mechanical Characteristics

	Parameter	Min.	Typ.	Max.	Units
T _{lead} ②	Lead Temperature	—	—	300	°C
R _{θJC}	Thermal Resistance, Junction to Case	—	—	5.0	K/W
R _{θJA} ③	Thermal Resistance, Junction to Ambient	—	—	80	
R _{θCS} ④	Thermal Resistance, Case to Heat Sink	—	0.5	—	
Wt	Weight	—	2.0	—	g
		—	0.07	—	(oz)
T	Mounting Torque	6.0	—	12	Kg-cm
		5.0	—	10	lbf-in

- ① L=100μH, duty cycle limited by max T_J
- ② 0.063 in. from Case (1.6mm) for 10 sec
- ③ Typical Socket Mount
- ④ Mounting Surface, Flat, Smooth and Greased

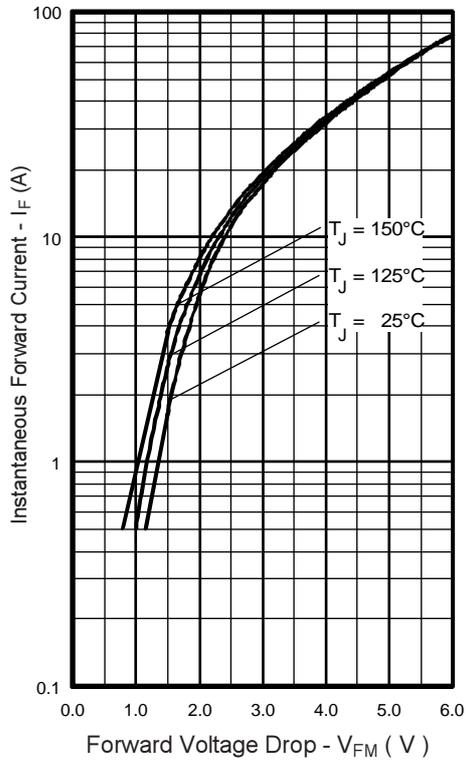


Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current,

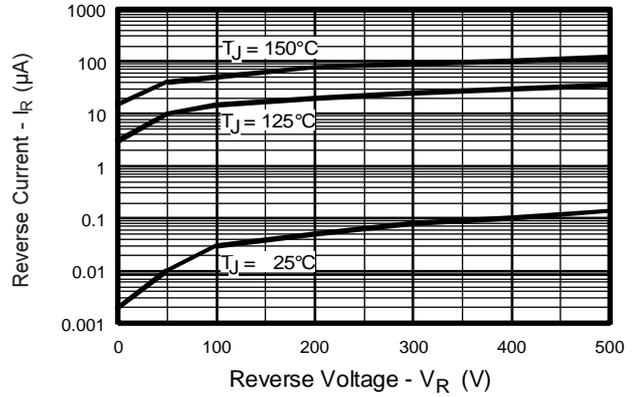


Fig. 2 - Typical Reverse Current vs. Reverse Voltage

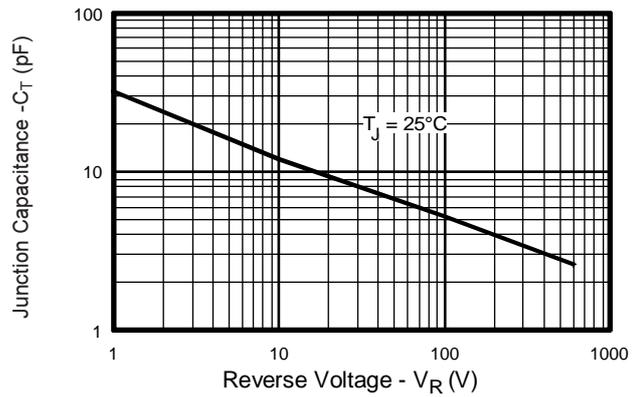


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

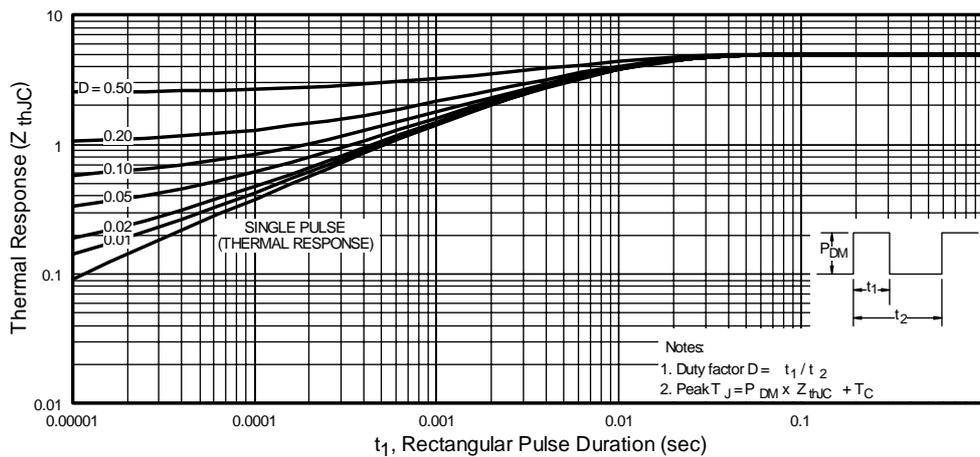


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

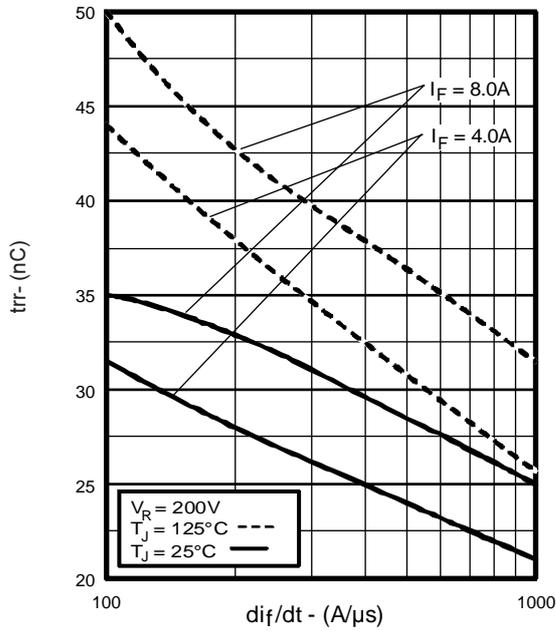


Fig. 5 - Typical Reverse Recovery vs. dI_f/dt

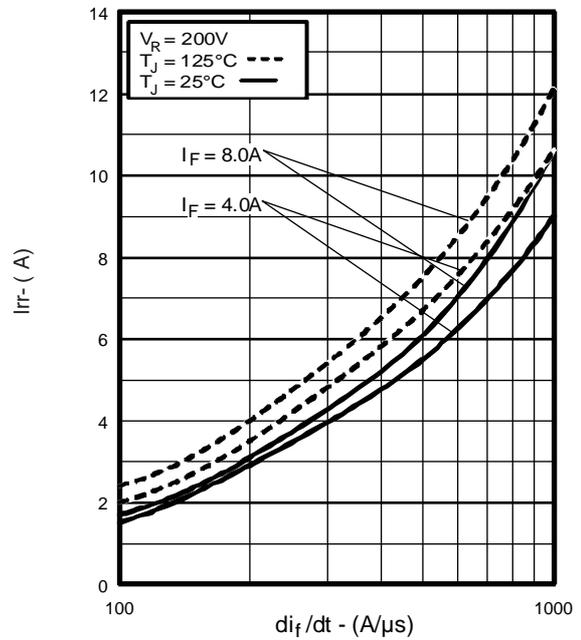


Fig. 6 - Typical Recovery Current vs. dI_f/dt

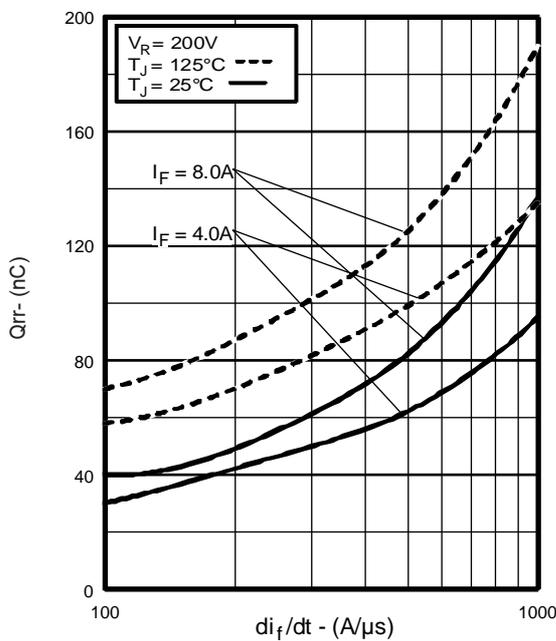


Fig. 7 - Typical Stored Charge vs. dI_f/dt

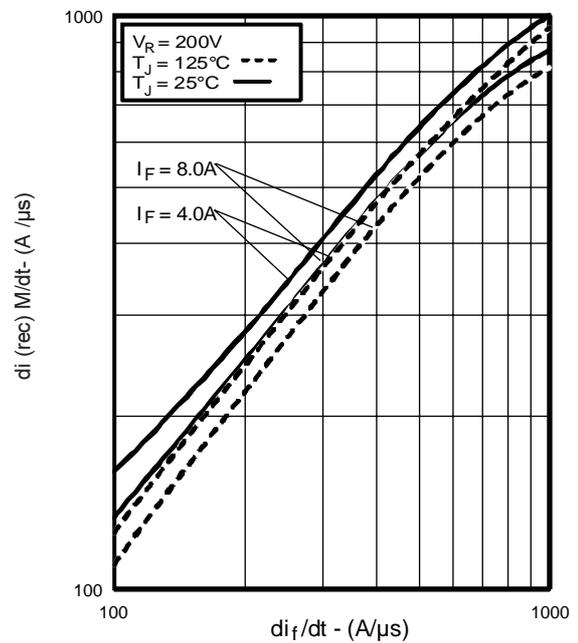


Fig. 8 - Typical $dI_{(rec)M}/dt$ vs. dI_f/dt

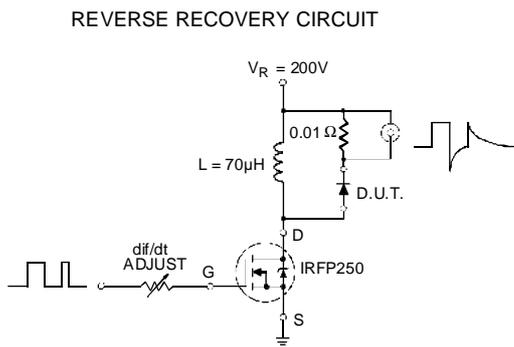


Fig. 9 - Reverse Recovery Parameter Test Circuit

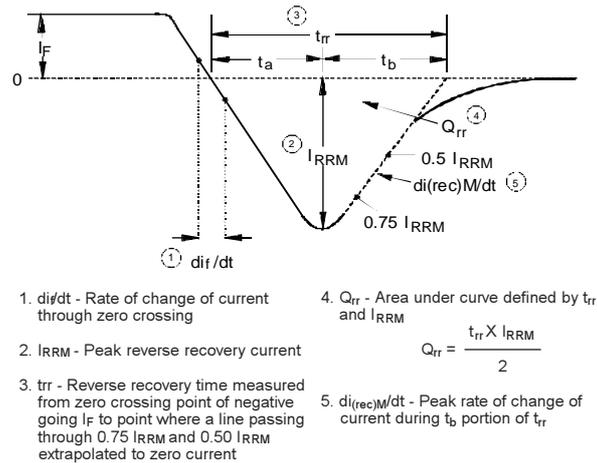


Fig. 10 - Reverse Recovery Waveform and Definitions

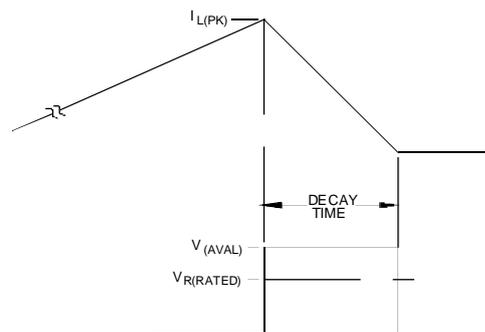
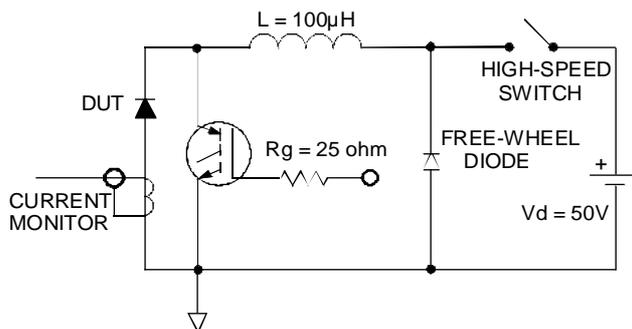
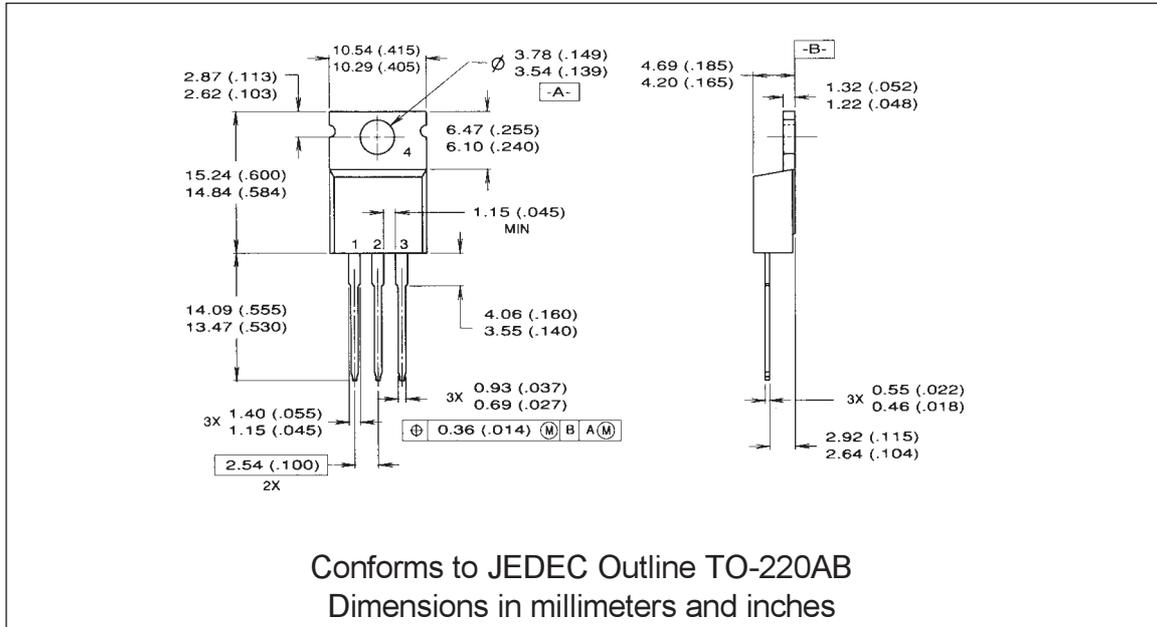


Fig. 11 - Avalanche Test Circuit and Waveforms

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Data and specifications subject to change without notice.

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