FUJITSU SEMICONDUCTOR
DATA SHEET

DS04-28315-2E

ASSP

3-Channel 8-Bit D/A Converter

MB40958

■ DESCRIPTION

The MB40958 is an 8-bit resolution high-speed digital-to-analog converter, designed for video processing applications.

The MB40958 has 8-bit resolution 3 channel D/A converters. Digital signals are input to the 8-bit digital input ports, and the input digital data are converted to the analog signals in minimum 60 Mega sample per seconds (MSPS).The analog output voltage is provided in a range of DC +3V to +5V (2Vp-p level) .

The MB40958 is fabricated by the Fujitsu's advanced bipolar process and housed in a 48-pin plastic QFP.

The MB40958 is designed for video signal processing applications, and it is suitable for TVs and VCRs.

■ FEATURES

- 8-bit x 3 channels D/A converters
- Max. 60 MHz input clock frequency providing 60 MSPS data conversion rate
- Linearity error : Max. +/-0.2%
- Analog input voltage range : 3V to 5V (2Vp-p level)
- Digital input voltage level : TTL level

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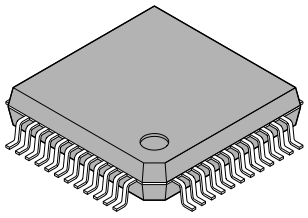
■ PACKAGE

48 pin, Plastic QFP



(FPT-48P-M15)

This device contains circuitry to protect the inputs against damage due to high static voltages or electric fields. However, it is advised that normal precautions be taken to avoid application of any voltage higher than maximum rated voltages to this high impedance circuit.

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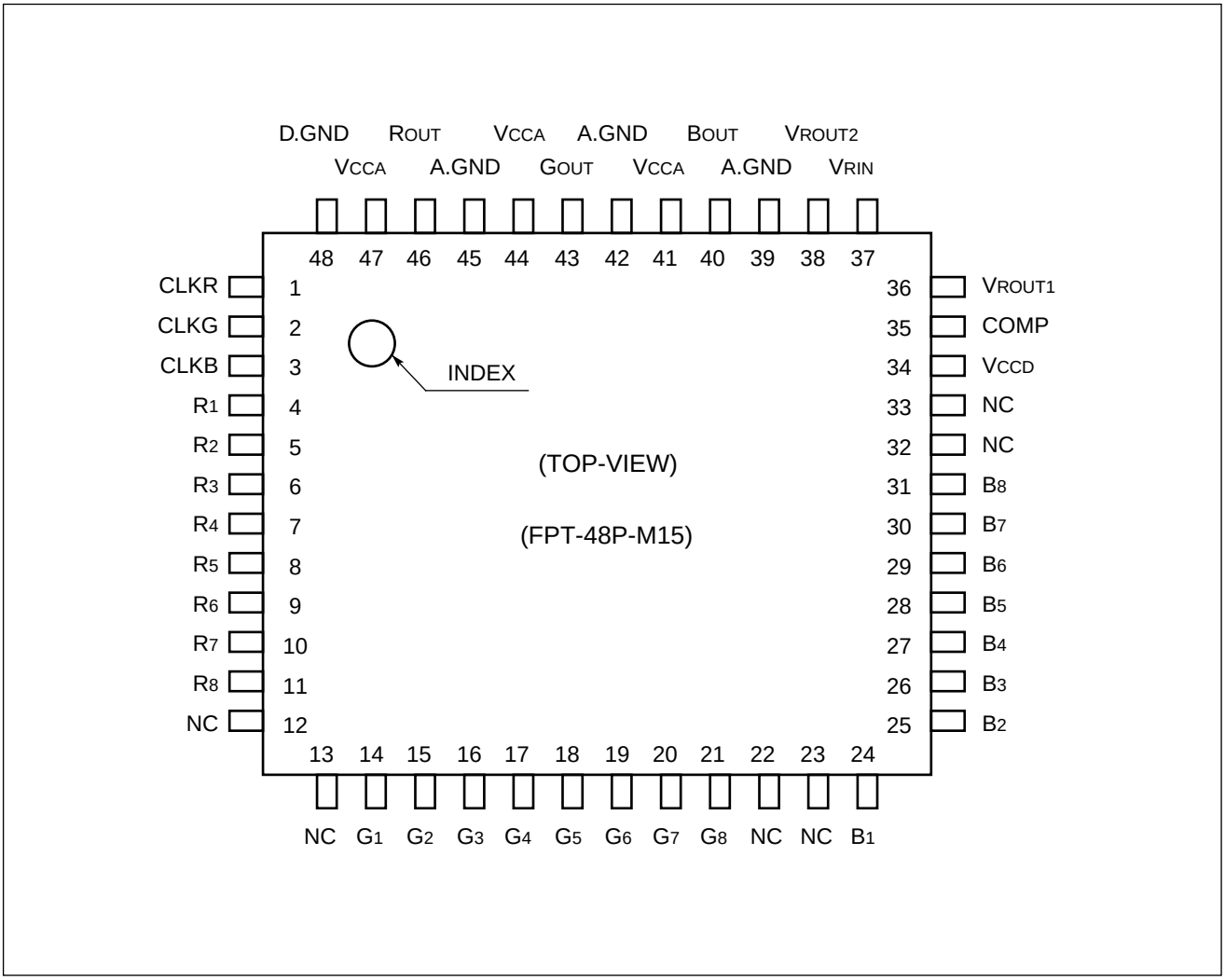
- On-chip reference voltage outputs :
  - Output by resistor divided (0.6 x V_{CCA})
  - Output by band gap reference (V_{CCA} - 2[V])
- Low power consumption : Typical 400mW at 2Vp-p analog output voltage
  Typical 300mW at 1Vp-p analog output voltage
- Single +5V power supply
- Operating temperature range : -20°c to +70°c
- Fujitsu's advanced bipolar process
- Package : 48-pin plastic QFP (Suffix : -PF)

■ PIN ASSIGNMENT



■ PIN DESCRIPTION

Symbol	Pin No.	Type	Name & Function
Power Supply			
V_{CCD}	34	—	+5V DC power supply pins for digital block.
D. GND	48	—	Ground pin for digital block.
V_{CCA}	41, 44, 47	—	DC power supply pins for analog block.
A. GND	39, 42, 45	—	Ground pins for analog block.
Clock			
CLKR	1	I	Clock input pin for R channel.
CLKG	2	I	Clock input pin for G channel.
CLKB	3	I	Clock input pin for B channel.
Digital Input			
R_1 R_2 R_3 R_4 R_5 R_6 R_7 R_8	4 5 6 7 8 9 10 11	I	Digital data input pins for R channel. 8-bit data is input to the pins. The R_1 pin is the MSB and the R_8 pin is the LSB.
G_1 G_2 G_3 G_4 G_5 G_6 G_7 G_8	14 15 16 17 18 19 20 21	I	Digital data input pins for G channel. 8-bit data is input to the pins. The G_1 pin is the MSB and the G_8 pin is the LSB.
B_1 B_2 B_3 B_4 B_5 B_6 B_7 B_8	24 25 26 27 28 29 30 31	I	Digital data input pins for B channel. 8-bit data is input to the pins. The B_1 pin is the MSB and the B_8 pin is the LSB.

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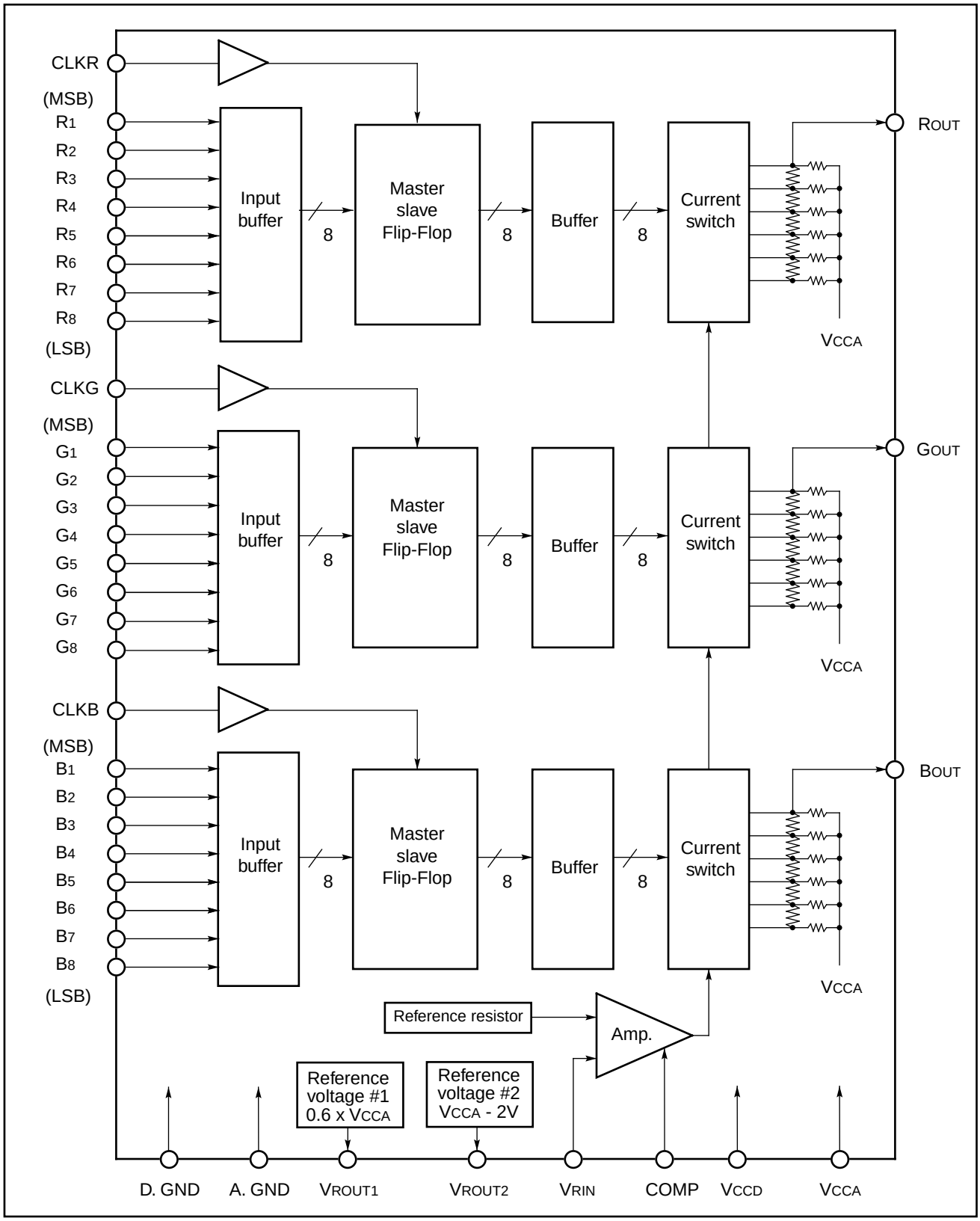
Symbol	Pin No.	Type	Name & Function
Analog Output			
ROUT	46	O	Analog signal output pin for R channel.
GOUT	43	O	Analog signal output pin for G channel.
BOUT	40	O	Analog signal output pin for B channel.
Reference Voltage			
VRIN	37	I	Reference voltage input pin. This pin is used to set the analog output dynamic range. When the internal reference voltage is used, this pin is connected with VROUT1 pin (36 pin) or VROUT2 pin (38 pin). When the reference voltage is supplied from the external generator, 2.65V to 4.3V or VCCA - VRIN = 0.7V to 2.2V is input to this pin.
VROUT1	36	O	Reference voltage output #1 pin. The output voltage is set to 0.6 x VCCA by the resistor divided method. When this pin is connected with VRIN pin (37 pin), an analog voltage is output from this pin in a range of 0.6 x VCCA to VCCA.
VROUT2	38	O	Reference voltage output #2 pin. The output voltage is set to VCCA - 2V by the band-gap reference method. When this pin is connected with VRIN pin (37 pin), an analog voltage is output from this pin in a range of VCCA - 2V to VCCA.
Compesation Capacitor			
COMP	35	—	Phase compesation capacitor pin. A phase compesation capacitor of 0.1µF or greater is connected between this pin and A. GND pin.
No Connection			
NC	12, 13, 22, 23, 32, 33	—	No connection, Leave those pins open.

■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATINGS

(A. GND = D. GND = 0V)

Parameter	Symbol	Rating	Unit
Power supply voltage	V_{CCA}, V_{CCD}	–0.5 to +7.0	V
Power supply voltage difference	$V_{CCD} – V_{CCA}$	1.5	V
Analog reference voltage	V_{RIN}	–0.5 to V_{CCA} +7.0	V
Digital input Voltage	V_{ID}	–0.5 to +7.0	V
Storage temperature	Tstg	–55 to +125	°C

Note: Permanent device damage may occur if the above **Absolute Maximum Ratings** are exceeded. Functional operation should be restricted to the conditions as detailed in the operational sections of this data sheet. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

■ RECOMMENDED OPERATING CONDITIONS

(A. GND = D. GND = 0V)

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
Power supply voltage	V_{CCA}, V_{CCD}	4.75	5.00	5.25	V
Power supply voltage difference	$V_{CCA} – V_{CCD}$	–0.2	—	0.2	V
Analog reference voltage	$V_{CCA} – V_{RIN}$	0.70	2.00	2.20	V
	V_{RIN}	2.65	3.00	4.30	V
Digital "H" level input voltage	V_{IHD}	2.0	—	—	V
Digital "L" level input voltage	V_{ILD}	—	—	0.8	V
Clock frequency	f_{CLK}	—	—	60	MHz
Setup time	t_{SU}	8.0	—	—	ns
Hold time	t_h	2.0	—	—	ns
Minimum clock "H" level pulse width	t_{wH}	6.5	—	—	ns
Minimum clock "L" level pulse width	t_{wL}	6.5	—	—	ns
Phase compesation capacitance	C_{COMP}	0.1	—	—	µF
Operating ambient temperature	T_{OP}	–20	—	70	°C

■ ELECTRICAL CHARACTERISTICS

(Recommended Operating Conditions Otherwise Noted)

1. DC Characteristics

Parameter	Symbol	Value			Unit	Remark
		Min.	Typ.	Max.		
Resolution	—	—	—	8	bit	—
Linearity error	LE	—	—	±0.2	%	DC Accuracy
Digital "H" level input current	I_{IHD}	—	—	20	µA	V_{IHD} = 2.7 (V)
Digital "L" level input current	I_{ILD}	–100	—	—	µA	V_{ILD} = 0.4 (V)
Reference input current	I_{RIN}	—	—	10	µA	V_{RIN} = 3.000 (V)
Reference voltage (Resister divided)	V_{ROUT1}	2.900	3.000	3.100	V	V_{CCA} = V_{CCD} = 5.00 (V)
Reference voltage (BGR)	V_{ROUT2}	V_{CCA} –2.100	V_{CCA} –2.000	V_{CCA} –1.900	V	—
Reference voltage (BGR)	—	—	100	—	ppm/°C	—
RGB output voltage ratio	FSR	0	—	6	%	V_{CCA} = V_{CCD} = 5.00 (V)
Full-scale output voltage	V_{OFS}	V_{CCA} –20	V_{CCA}	—	mV	—
Zero-scale output voltage	V_{OZS}	2.938	3.008	3.078	V	V_{CCA} = V_{CCD} = 5.00 (V) V_{RIN} = 3.000 (V)
Output resistance	R_O	192	240	288	Ω	Ta = 25°C
Supply current	I_{CC}	—	80*	133	mA	V_{CCA} = V_{CCD} = 5.25 (V) V_{RIN} = V_{ROUT1}

* : V_{CCA} = V_{CCD} = 5.00V

2. AC Characteristics

Parameter	Symbol	Value			Unit	Remark
		Min.	Typ.	Max.		
Maximum conversion rate	F_S	60	—	—	MSPS	Terminated A. OUT pin with 240Ω, C_L = 15pF
Output propagation delay time	t_{pd}	—	7	—	ns	
Output rising time	t_r	—	5	—	ns	
Output falling time	tf	—	5	—	ns	
Setting time	tset	—	15	—	ns	

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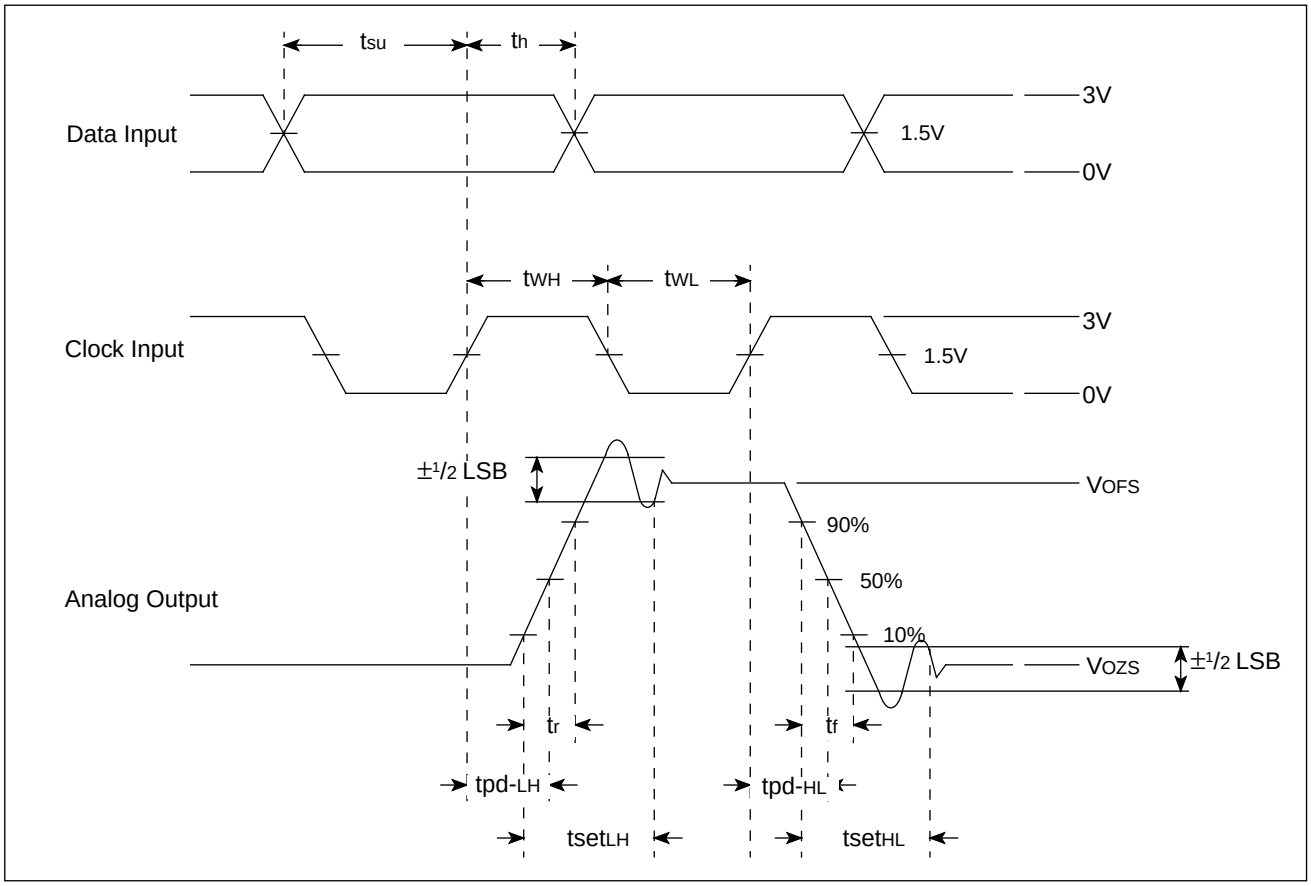
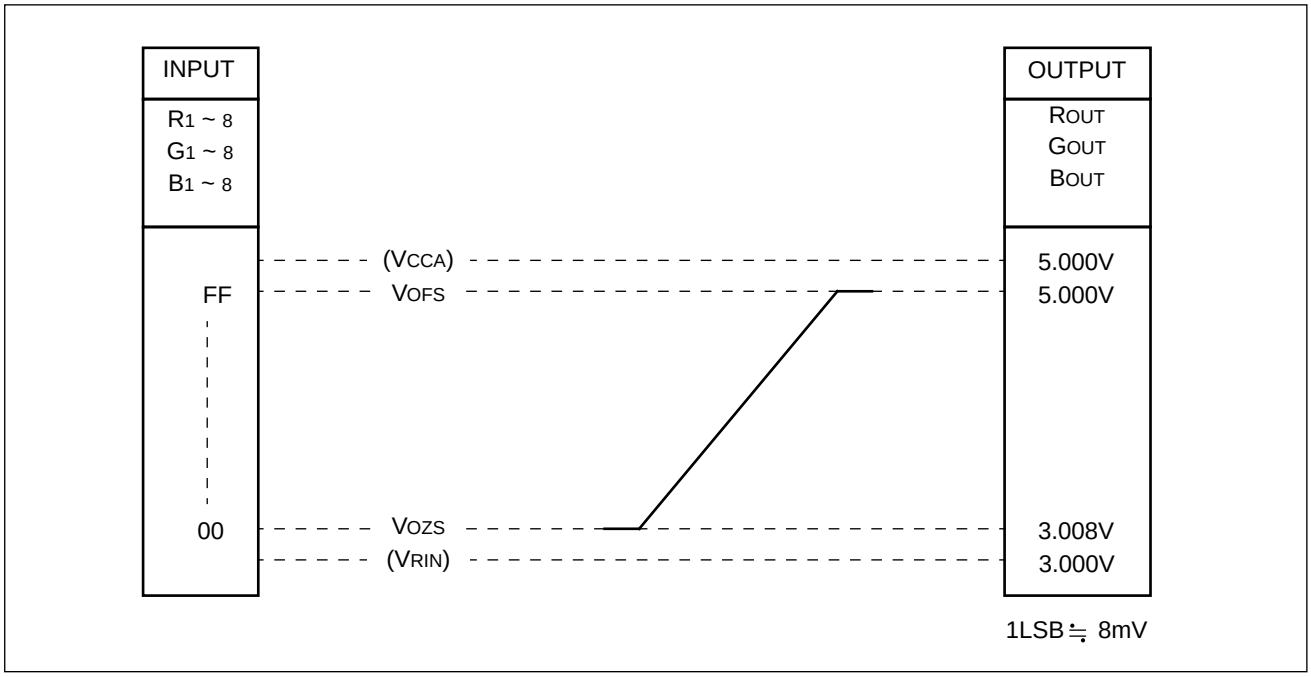
■ AC TIMINT CHART



■ DAC OUTPUT VOLTAGE RANGE



■ CALCULATION OF DAC OUTPUT VOLTAGE AT IDEAL CONVERSION

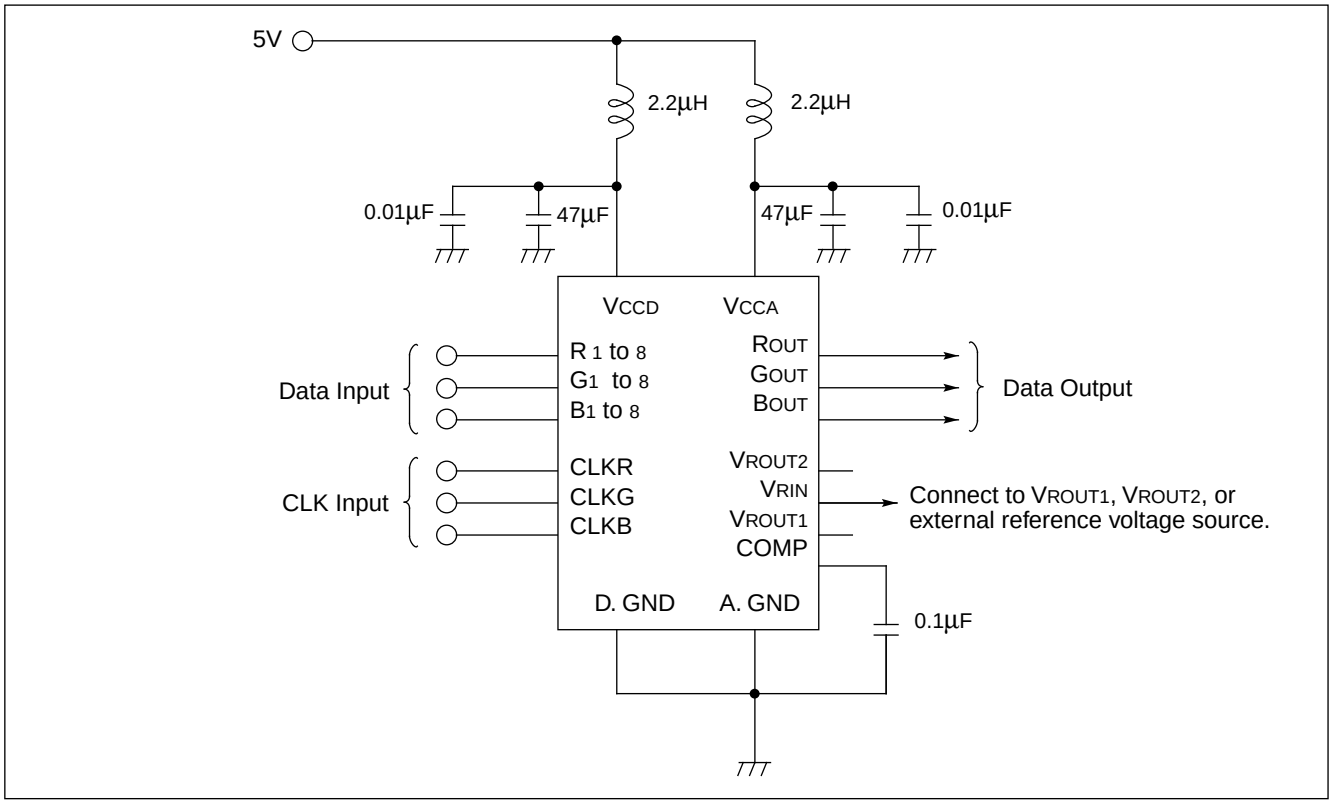
$$R_{OUT}\ (G_{OUT},\ B_{OUT}) = V_{CCA} - \frac{255 - N}{256} \times (V_{CCA} - V_{RIN})$$

[N : Digital Input Code (0 to 255)]

$$V_{OFS} = V_{CCA}$$

$$V_{OZS} = V_{CCA} - \frac{255}{256} \times (V_{CCA} - V_{RIN})$$

■ TYPICAL CONNECTION EXAMPLE



■ NOTES ON USE

1. Power Supply Patterns of the PCB

The power supply wire patterns (V_{CC} and GND patterns) of the PCB should be designed as wide as possible in order to reduce parasitic impedance.

2. Switching Noise

In order to reduce switching noise as much as possible, noise limit capacitor must be connected between V_{CCD} and D. GND pins and V_{CCA} and A. GND pins.

In this case, the capacitor should be connected to the GND pins side as near as possible.

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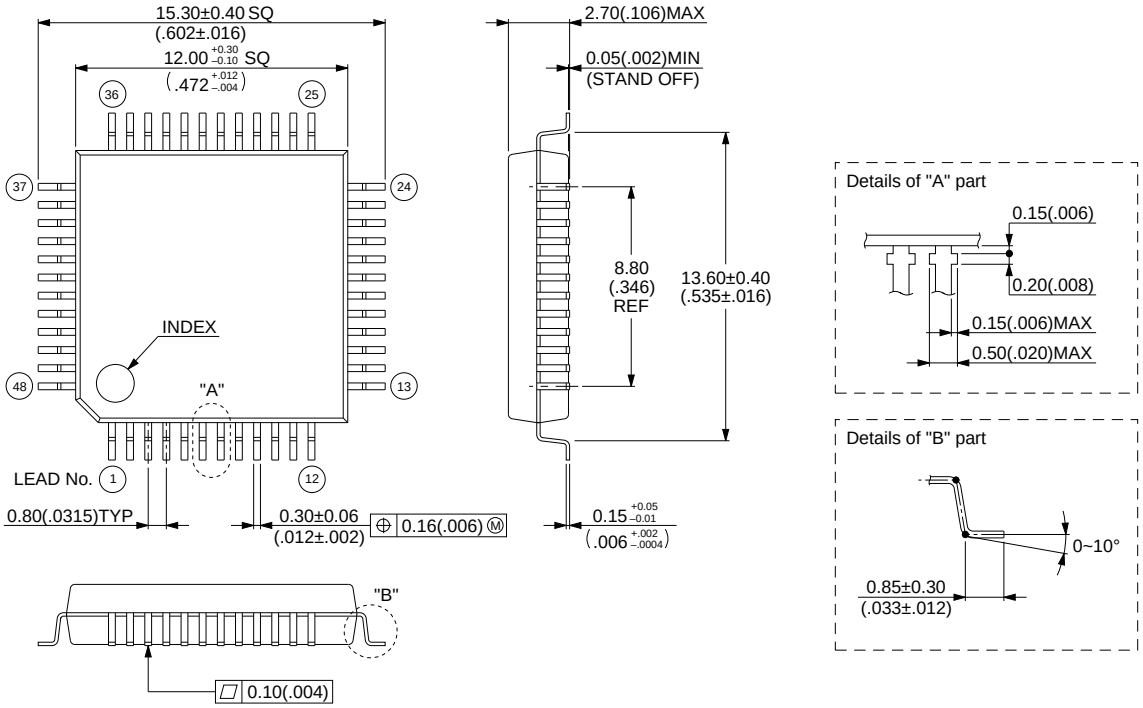
■ PACKAGE DIMENSION

48 pin, Plastic QFP

(FPT-48P-M15)



Dimensions in mm (inches).

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